ANALOG DEVICES

Four Channel RTD/Strain Gage Conditioner

MODEL 2B34

FEATURES

Low Input Offset Drift: ±1.0µV/°C Low Gain Drift: ±25ppm/°C Low Nonlinearity: ±0.01% max (±0.005% typ) Differential Input Protection: ±130V rms. Channel Multiplexing: 3000 chan/sec Scanning Speed Solid State Reliability Internal RTD Excitation/Lead Wire Compensation

APPLICATIONS

Multichannel Signal Conditioning Data Acquisition Industrial Process Monitoring



GENERAL DESCRIPTION

The model 2B34 is a four channel signal conditioner providing input protection, multiplexing, and amplification in a single, low cost package. A multi-purpose device, the 2B34 is designed to effectively condition low level signals (± 30 mV to ± 100 mV) such as those produced by RTD and strain gage sensors. The superior design of the 2B34 provides low input drift ($\pm 1.0\mu$ V/°C), high common mode rejection (94dB @ 60Hz), and extremely stable gain (± 25 ppm/°C). Other features include low nonlinearity ($\pm 0.01\%$ max), excitation and lead wire compensation for RTD inputs, and a wide operating temperature range (-25° C to $+85^{\circ}$ C).

APPLICATIONS

The 2B34 is a superior alternative to the relay multiplexing technique used in multichannel data acquisition systems, computer interface systems, and measurement and control instrumentation. Advantages over relay circuits include functional versatility, superior signal conditioning, and solid state speed and reliability.

DESIGN FEATURES AND USER BENEFITS

Solid State Design: Complete solid state construction offers both high performance and reliability.

Ease of Use: The multichannel, functionally complete design in a compact $(2'' \times 4'' \times 0.4'')$ module, conserves board space and eliminates the need for a number of discrete components that would otherwise be required.

Low Cost: The 2B34 offers the lowest cost per channel for solid state, low level sensor signal conditioning.

Wide Operating Temperature Range: The 2B34 has been designed to operate over -25°C to +85°C ambient temperature range.

FUNCTIONAL DESCRIPTION

The internal structure of the 2B3# is shown in Figure 1. Four individual input channels are multiplexed into a single, low



Figure 1. 2B34 Functional Diagram

SPECIFICATIONS (typical @ +25°C, V_S = ±15V, unless otherwise noted)

Model	2B34 Strain Gage Mode	J RTD Mode	OUTLINE DIMENSIONS
ANALOG INPUT	e e e e e e e e e e e e e e e e e e e		Dunchalous alown in menes and (mun).
Number of Channels	4	· •	2.01 (51.1) MAX
Input Range	±30mV & ±100mV	25-175Ω & 0-350Ω	0.41
Gain Range ($R_G = 945\Omega$)	166.6V/V & 50V/V		(10.4)
Expanded'	SUV/V to 1000V/V	$V_{} = [0.4 \times 10^{-3} $	
Transfer Function	NA	$(R_{PTTD}) = 0.041 G$	0.02 (0.5) MAX [•
Cuin France	+0.6% max (C = 50)	*	0.2 (5.1) MAX
Gain Error	$\pm 0.8\% \max (G = 166.6)$		
Cain Temperature Coefficient ²	+25ppm/°C		
Gain Nonlinearity	±0.01% of Span, max	•	
Offset Voltage	•		
Input Offset, Initial ³ (Adj. to Zero)	±150µV	•	
vs. Temperature	$\pm 1 \mu V / C$	±0.015 deg/deg	
Channel to Channel Offset	±25µV	•	
Total Offset Drift (RTI)	$\pm 1\mu V/C$	*	
Input Noise Voltage			
0.01Hz - 100Hz, R _S = 1kS2	1.5µV p-p	NI/A	
Common Mode Voltage	IOV	IN/A	◆ 19 + + + + + + + + + + + + + + + + + +
$P_{0} = 1080$ f = 60Hz	94dB(@C = 166.6)	N/A	(102.1)
$R_{c} = 1000$, $I = 00Hz$	86dB (@ G = 166.6)		
Maximum Safe Differential Input (10 min) 130V rms	•	
Normal Mode Rejection @ 60Hs	24dB	•	
Input Resistance	20ΜΩ	•	
nput Bias Current	OnA max	•	
Lead Resistance Effect	MA	±0.03 deg/Ω	
ANALOG OUTPUT			
Output Voltage Swing	±5V @ 1mA		
Output Resistance	\bigcirc		
Direct Output	0.152		
Switched Output	+9V po load	////	
	277,1101040		- 0.1 (2.54) ORID
Excitation Level (per channel)	NA	0.4mA 1%	NOTE: TERMINAL PINS INSTALLED PNLY IN SHADED HOLE LOCATIONS
vs. Temperature	NA	(±1.7% max) ±10ppm/°C	2124 PDF DESIGNATIONS
CHANNEL SELECTION			
Channel Selection Time to ±0.01% F.S.	300µs	•	
Channel Scanning Speed	>3000 chan/sec	•	1 NC 37 RT
DYNAMIC RESPONSE			2 38
Input Settling Time to ±0.01% F.S.	0.4 sec	•	3 SWD OUTPUT ENABLE 39 Rg/OFS
Bandwidth	4Hz	•	5 41 LO
POWER SUPPLY			6 DIRECT OUTPUT 42 COM
Voltage, ±VS, Rated Performance	±15V dc ±5%	•	7 SWD INPUT 43 VQFS (+10V) 8 44
Current	+35mA, -15mA, max	•	9 OUTPUT OFFSET 45
Supply Effect on Offset	±0.003%/%	±0.02%/%	10 46 11 -15V 47 HI
ENVIRONMENTAL			12 ANA COM 48
Temperature			13 +15V 49 Rg/OFS
Rated Performance	$0 \text{ to } + 70^{\circ} \text{ C}$	•	15 51 LO
Operating	-25°C to +85°C	•	16 52 COM
Storage	-55°C to +85°C	•	17 18 54
CASE SIZE	$2'' \times 4'' \times 0.4''$		19 20 56 HI
NOTES	malificas es abaura in Pinton S		21 57
³ Does not include effects of sensor excitation drift.	inpinier as snown in Figure 3.		22 ADR1 CHANNEL 58 RG/OFS
³ With no induced offset, using circuit shown in Fig	ure 2 (pots centered).		24 GAIN SELECT 60 LO
*Specifications same as Strain Gage Mode.			25 RTD ENABLE 61 COM
Specifications subject to change without notice.			20 62 V _{QFS} (+10V) / 63
			28 64
			29 65
			31 DIG COM 66 HI
			32 SYNCIN 68 Rg/OFS
			33 SYNC OUT 69 RG CHANNEL D
			35 71 COM
			36 72 Yoss (+10V)

*SHORTED INTERNALLY FOR FEEDTHROUGH FOR USE WITH 2864/56 MODELS.

VOFS (+10V)

drift differential instrumentation amplifier, with the desired channel specified by the two digital channel select inputs. This signal is then fed to a digitally controlled programmable gain amplifier (PGA). The appropriate gain for a particular sensor type is selected by the gain select input.

User selectable direct or switched output permits direct output connection of several modules, should more than four channels be required.

An internally selectable constant current excitation source provides direct connection of 2 or 3 wire RTDs, thus eliminating the need for external excitation sources. Each channel contains an input protection and filtering network to preserve signal integrity in the presence of series mode 50/60Hz noise.

OPERATING INSTRUCTIONS

Connection of the 2B34 with three wire RTD inputs is shown in Figure 2 and will be all that is needed in most cases. The following sections describe the basic application, as well as detail some optional connections that enhance the module's performance in more complex applications. All unused inputs

AD1	AD0	Channel
0	0	A
0	1	В
1	0	C
1	1	D

Table I. Channel Selection

brated to provide gains of 50 and 166.6, with gain components shown in Figure 2. This provides proper amplification of input signals over the span of ±30mV to ±100mV. Selection of the desired gain and sensor input mode is achieved by applying the appropriate binary codes shown in Table II. A 200 Ω pot provides ±3% full scale span adjustment.

Selected Gain

(Pin 24)

Gain Select Input



inputs (ADR0, ADR1). Channels may be selected in any order and there are no restrictions on rate other than the 300µs settling time for access to a channel (Table I, channel select truth table).

Gain Selection: The 2B34 is designed to provide signal conditioning of both RTD and strain gage sensor inputs. To accommodate both of these sensor types, the 2B34 is precali-

> Figure 3. Zero Suppressed Switched Output RTD Application

OUTPUT ENABLE

ADRO G

GAIN

ENABLE

 $V_{0} = \left[\left(0.4 \times 10^{-3} (R_{RTD}) - 0.04 \right) 50 \right] - \frac{100k}{R_{1}} \left| \frac{R_{2}}{10k} \right|$

gain range of the 2B34 may be supplemented by use of an external amplifier (Figure 3). A low drift, operational amplifier (such as the AD741K) should be used to maintain signal integrity.

Optional Offset Adjustment: All channels of the 2B34 are typically within $\pm 150\mu V$ (RTI) offset. For use in more demanding applications, the module has provisions for fine adjustment of the input offset (RTI) of each input as well as the output offset (RTO) of the entire module. None of the offset adjustments will affect drift performance.

In some applications, where $\pm 25\mu V$ channel-to-channel offset voltage can be tolerated, adjustment of only the output offset will be sufficient. The offset circuit shown in Figure 2 (for channel "A") is required when a potentiometer is not used to adjust input offset. The output offset adjustment may then be used to null the $150\mu V$ (RTI) offset, leaving an offset difference between channels of $\pm 25\mu V$. If input offset adjustment is desired, the input offset circuitry shown in Figure 3 should be used. This provides approximately $\pm 140mV$ (RTO) of adjustment, and should be adequate, in most cases, for elimination of sensor offset errors.

o calibrate in the mV (strain gage) mode, (Figure 3), short the signal inputs (for example, pins 66, 70 for channel "D") to common and center the input offset adjustment potentiometer. Adjust the output offset potentiometer until the outis nulled for that channel at the appropriate gain. The t offset pots on each channel may then be used to clim int nate any errors on subsequent channels that are selected. To calibrate in the RTD mode, follow same procedure, but replace the short with a 100Ω resistant Channel Expansion: The 2B34 has provisions for dire stly interconnecting several modules when more than four channels are required. The series switched outputs of the modules are connected together, the channel select inputs are driven in parallel, and the switched output of the desired module is selected using the ENABLE pin. This technique is shown in Figure 4.

Channel address and ENABLE (active low) inputs are CMOS/



Figure 4. Channel Expansion

2B34 Strain Gage Application: Figure 5 shows a four channel strain gage input system utilizing the multiplexing feature of the 2B34. Input offset and gain adjustments are used to eliminate inherent sensor errors. The model 2B35 triple output supply may be used to provide power for the 2B34 as well as excitation for the strain gage sensors.



Figure 5. 2B34 Strain Gage Application

VOL. II, 9-38 TEMPERATURE TRANSDUCERS & SIGNAL CONDITIONERS