

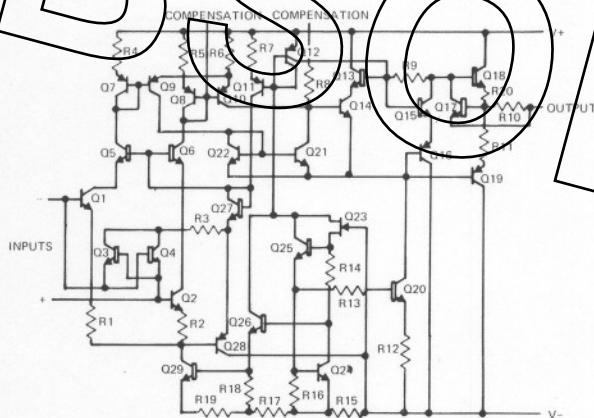
GENERAL DESCRIPTION

The ANALOG DEVICES AD108, AD208 and AD308 are precision operational amplifiers fabricated on a single silicon chip. The use of super beta transistors in the input stage, along with improved process control, results in guaranteed input currents nearly a thousand times lower than industry standards such as the AD741. Guaranteed offset voltage drift and current drift specifications permit the circuits to be used in applications requiring excellent temperature stability. Operation with supply voltages as low as $\pm 2V$, and extremely low power consumption make the devices ideal for battery powered applications. Frequency Compensation is accomplished using a single external capacitor.

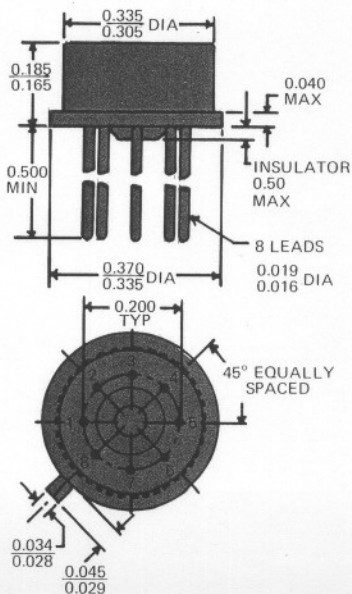
The AD108 and AD208 have identical specifications, with the AD108 guaranteed over the $-55^{\circ}C$ to $+125^{\circ}C$ temperature range and the AD208 guaranteed over the $-25^{\circ}C$ to $+85^{\circ}C$ temperature range. The AD308 is specified over the $0^{\circ}C$ to $+70^{\circ}C$ temperature range.

All devices are pin compatible with the popular AD101A, AD201A, AD301A series and are available in the TO-99 package configuration.

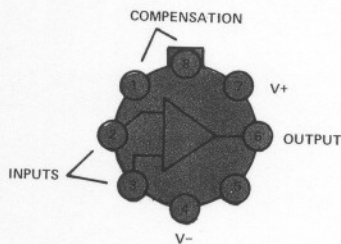
SCHEMATIC DIAGRAM



PHYSICAL DIMENSIONS (In Inches) (TO-99)



CONNECTION DIAGRAM



Note: Pin 4 connected to case.
TOP VIEW

AD 108 208 308

PRECISION OPERATIONAL AMPLIFIERS

DESIGN FEATURES

Bias Current 0.8nA
Offset Current 50pA
Offset Voltage 0.7mV
Offset Voltage Drift $3.0\mu V/^{\circ}C$
Supply Current $150\mu A$
Operating Voltage $\pm 2V$ to $\pm 20V$

ORDERING INFORMATION

Order Number	Package Type	Operating Temperature Range
AD108H	TO-99	$-55^{\circ}C$ to $+125^{\circ}C$
AD208H	TO-99	$-25^{\circ}C$ to $+85^{\circ}C$
AD308H	TO-99	$0^{\circ}C$ to $+70^{\circ}C$

ANALOG DEVICES

LINEAR INTEGRATED CIRCUITS

ROUTE ONE INDUSTRIAL PARK
P. O. BOX 280, NORWOOD, MASS. 02062
TEL: 617/329-4700 TWX: 710/394-6577



ABSOLUTE MAXIMUM RATINGS

AD108, AD208, and AD308
unless otherwise specified

Supply Voltage	
AD108, AD208	±20V
AD308	±18V
Power Dissipation (Note 1)	500 mW
Differential Input Current (Note 2)	±10 mA
Input Voltage (Note 3)	±15V
Output Short-Circuit Duration	Indefinite
Operating Temperature Range	
AD108	-55°C to +125°C
AD208	-25°C to +85°C
AD308	0°C to +70°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 60 sec)	300°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified) (Note 4)

Parameters	Conditions	AD108 AD208			AD308			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage			0.7	2.0		2.0	7.5	mV
Input Offset Current			0.05	0.2		0.2	1	nA
Input Bias Current			0.8	2.0		1.5	7	nA
Input Resistance		30	70		10	40		MΩ
Supply Current			0.3	0.6		0.3		mA
Large Signal Voltage Gain		50	300		25	300		V/mV

$V_S = \pm 15\text{V}$
 $V_{OUT} = \pm 10\text{V}$
 $R_L \geq 10\text{K}\Omega$

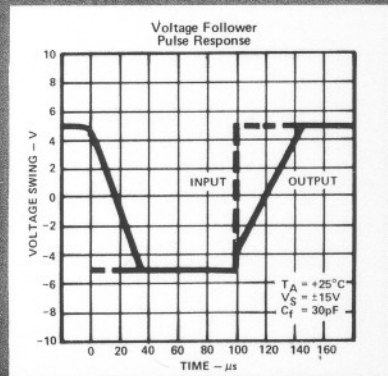
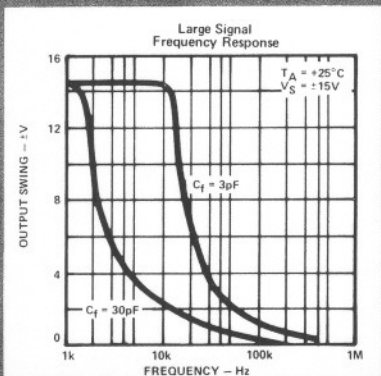
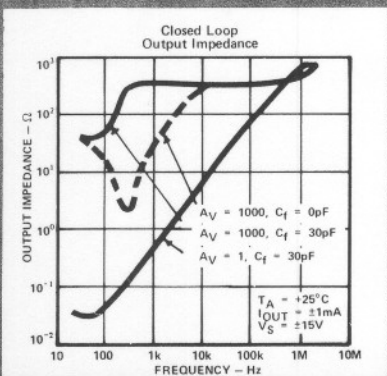
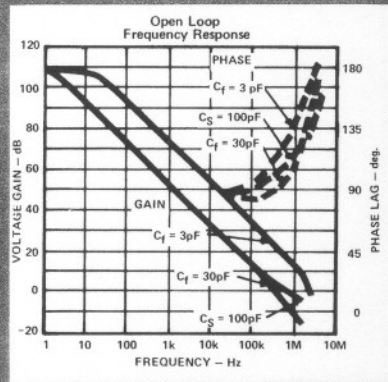
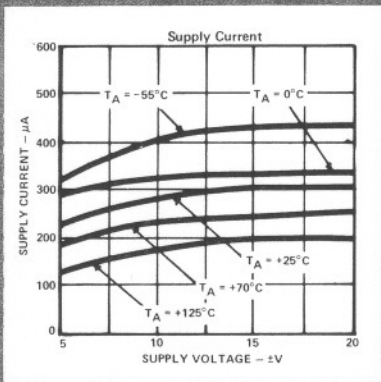
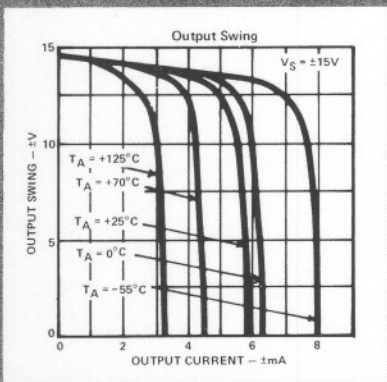
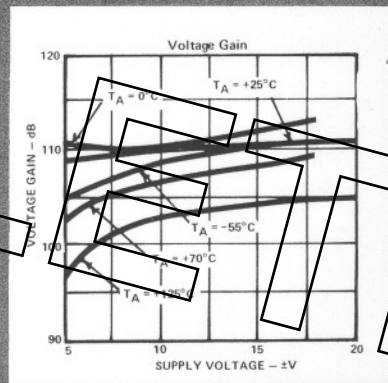
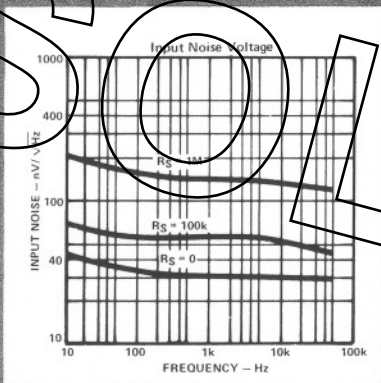
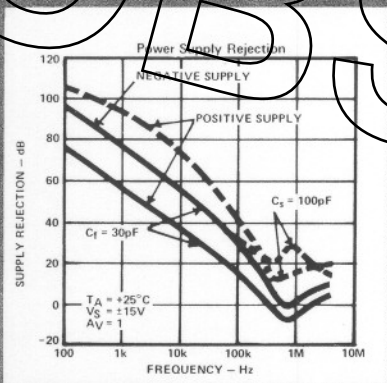
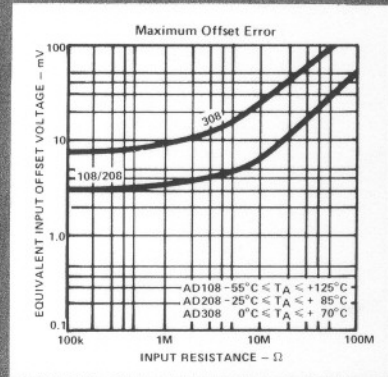
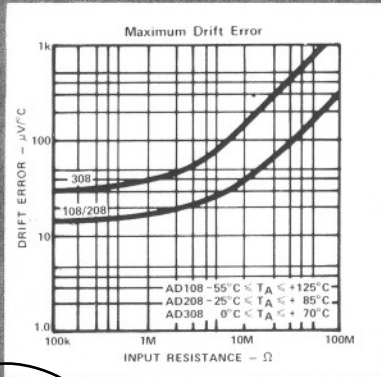
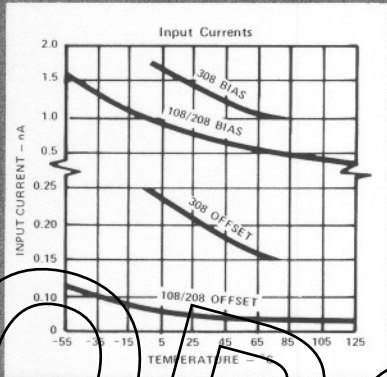
The Following Specifications Apply Over The Operating Temperature Ranges (Note 4)

Input Offset Voltage				3.0		10		mV
Average Temp. Coefficient of Input Offset Voltage			3.0	15		6.0	30	$\mu\text{V}/^\circ\text{C}$
Input Offset Current				0.4			1.5	nA
Average Temp. Coefficient of Input Offset Current			0.5	2.5		2.0	10	$\text{pA}/^\circ\text{C}$
Input Bias Current				3.0			10	nA
Supply Current	$T_A = +125^\circ\text{C}$ $V_S = \pm 20\text{V}$ $V_S = \pm 15\text{V}$		0.15	0.4				mA
Large Signal Voltage Gain		$V_{OUT} = \pm 10\text{V}$ $R_L \geq 10\text{K}\Omega$	25			15		
Output Voltage Swing	$V_S = \pm 15\text{V}$ $R_L = 10\text{K}\Omega$	±13	±14		±13	±14		V
Input Voltage Range	$V_S = \pm 15\text{V}$	±14			±14			V
Common Mode Rejection Ratio		85	100		80	100		dB
Supply Voltage Rejection Ratio		80	96		80	96		dB

NOTES:

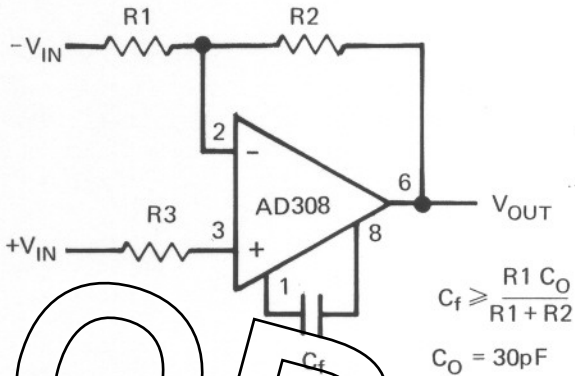
- Note 1: The maximum junction temperature of the AD108 is 150°C, of the AD208 is 100°C, and of the AD308 is 85°C. For operation at elevated temperatures, the TO-99 package must be derated based on a thermal resistance of 150°C/W junction to ambient or 45°C/W junction to case.
- Note 2: The inputs are shunted with back-to-back diodes for overvoltage protection. Therefore, excessive current will flow if a differential input voltage in excess of 1V is applied between the inputs unless some limiting resistance is used.
- Note 3: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.
- Note 4: Unless otherwise noted, these specifications apply for supply voltages and ambient temperatures of ±5V to ±20V and -55°C to +125°C for the AD108, ±5V to ±20V and -25°C to +85°C for the AD208, and ±5V to ±15V and 0°C to +70°C for the AD308.

TYPICAL PERFORMANCE CURVES

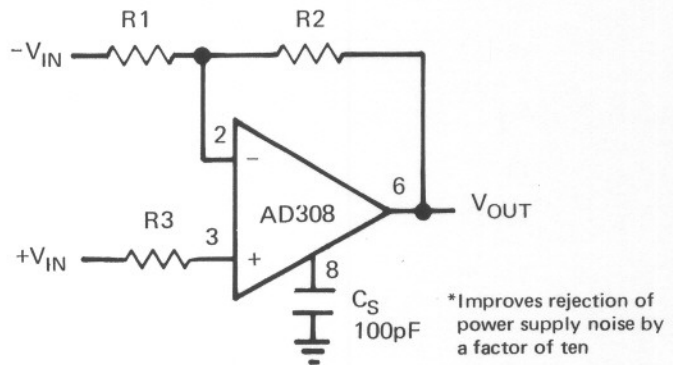


COMPENSATION CIRCUITS

Standard Compensation Circuit



Alternate* Frequency Compensation



For other selected Analog Devices Linear Integrated Circuits, refer to the specification sheets listed below.

Operational Amplifiers

AD101A, 201A, 301A
 AD108, 208, 308
 AD108A, 208A, 308A
 AD741, 741C, 741K
 AD502J, 502K, 502L
 AD503J, 503K
 AD503L
 AD504J, 504K
 AD505J, 505K
 AD513J, 513K

- General Purpose, Externally Compensated
- High Performance, General Purpose
- High Performance, General Purpose
- General Purpose, Internally Compensated
- General Purpose, Internally Compensated
- FET Input, Internally Compensated
- FET Input, Internally Compensated, Internally Trimmed
- High Precision, Low Drift
- High Speed
- FET Input, High Speed

Special Function Circuits

AD520J, 520K
 AD530J, 530K

Instrumentation Amplifier
 Complete Multiplier, Divider, Squarer, Square Rooter