## FEATURES

Third Generation $1^{2}$ L LSI Design
Logic Powered (+5 V DC)
Large $0.56^{\prime \prime}$ Red Orange LEDs
Balanced Differential Input/Fioating
1000 V, CMV
Terminal Block Interface (AC Version) High Reliability: >250,000 Hour MTBF Small Size and Weight
Low Cost

## GENKRAD DESGRIPTION

The AD 020 is specifically designed to provide a digital alternative to anlog papel perer. The AD2026 logic powered ( +5 V dc). Mos of the analog digtal firchitry is implemented on a single I2/L LSI hip, the AD2020. On y 13 dditional compenents are equifed to complete the AD\&026. The enyre assemThe AD2026 offers as a stardard feature, $0.56^{\circ} \mathrm{h} / \mathrm{gh}$ EED Displays. Brightness is enhanced due to Red Orapge lens. Ih addition to the Red Orange lens, thC AD2026 is a so available with a dark red lens for applications where maximum brightness is not required and minimum backlighting is desired
A unique patented case design utilizes molded-in fingers, both to capture the PCB in the case and to provide snap-in mounting of the DPM in a standard panel cutout. No mounting hardware of any kind is used. The AD2026 occupies less than $1^{\prime \prime}$ of space behind the panel.

## EXCELLENT PERFORMANCE

The AD2026 offers the instrument designer digital accuracy, resolution and use of readout while occupying less space than its analog counterpart. Other features of analog meters such as reliability and instantaneous response are retained in the AD2026.
The AD2026 measures and displays inputs from -99 mV to +999 mV , with an accuracy of $0.1 \%$ of reading $\pm 1$ digit. Zero shift is less than one bit over the full operating temperature range, resulting in the same performance as a DPM with auto zero. The balanced differential input of the AD2026 rejects common-mode voltages up to 200 mV , enough to eliminate most ground loop problems.

## WIRING CONNECTIONS

For Balanced Differential operation with the AD2026, connect input as shown in Figure 1. The common-mode loop must provide a return path for the bias currents internal to the AD2026. The resistance of this path must be less than $100 \mathrm{k} \Omega$ and total common-mode voltages must not exceed 200 mV .
*Covered by Patent Numbers: 4,092,698, 29,992; 3,872,466; and 3,887,863.
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## ADM 120 —

## DISPLAY OUTPUT

- Light Emitting Diode, Planar Seven Segment Display Readouts, $0.56^{\prime \prime}$ ( 14.6 mm ) High (Orange)
- Overload Indication: EEE
- Negative Indication: -XX
- Negative Overload Indication: - - -
- Decimal Points: Three (3) Selectable at Input Connector


## ANALOG INPUT

- Configuration: Balanced Differential Input
- Full-Scale Range: -99 mV to +999 mV
- Automatic Polarity
- Input Impedance: $100 \mathrm{M} \Omega ; 100 \mathrm{k} \Omega$ ( 10 V Option)
- Overvoltage Production: $\pm 15 \mathrm{~V}$ dc, Sustained


## ACCURACY

- $\pm 0.1 \% \pm 1$ Digit $^{1}$
- Resolution: 1 mV or 10 mV
- Temperate Range ${ }^{2}:-10^{\circ} \mathrm{C}$ to $+60^{\circ} \mathrm{C}$ Operating: $-25^{\circ} \mathrm{C}$
to $+80^{\circ} \mathrm{C}$ Stonge
- Temperature C efficient: Gain: $50 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$


Hold: When the Hold input is at Logic " 0 ", grounded or open circuit, the AD2026 will convert at 4 conversions per second. If a voltage of 0.6 V to 2.4 V is applied to this input, the DPM will stop converting and hold the last reading. A $12 \mathrm{k} \Omega$ resistor in series with this input to +5 V will provide the proper voltage input.

## DECIMAL POINT

- To Illuminate Decimal Points Ground Appropriate Pin (A, B or 3)


## POWER INPUT LOGIC POWER ${ }^{3}$

- Converter: $+5 \mathrm{~V} \pm 5 \%, 0.2$ Watts Typ; 0.33 Watts Max
- Display: $+5 \mathrm{~V} \pm 40 \%, 0.45$ Watts Typ; 0.75 Watts Max


## CALIBRATION ADJUSTMENTS

- Gain
- Zero
- Recommended Recalibration Interval: Six Months

SIZE

- $3.43^{\prime \prime} \mathrm{W}^{\prime \prime} 2.0^{\prime \prime} \mathrm{H} \times 0.85^{\prime \prime} \mathrm{D}\left(87^{\prime \prime} \times 52 \times 22 \mathrm{~mm}\right)$
- $0.88^{\prime \prime}(22 \mathrm{~mm})$ Overall Depth to Rear of Connector
- Panel Cutout Required: $3.175 \pm 0.015^{\prime \prime}$ " $1.810 \pm 0.015^{\prime \prime}$
( Warm-Up Time to Rated teelaraly: Instantaneous
- Setting Time /o
( $1 \mathrm{k} \Omega$ Source Imbalance, DC to 1 kHz )
- $50 \mathrm{~dB}, \pm 200 \mathrm{mV}$ Common-Mode Vol age
- 116 dB ( 96 dB on 10 V Range); 1000 V rms Max CMY (AC Version)
NORMAL-MODE REJECTION
- 30 dB at $50 \mathrm{~Hz}-60 \mathrm{~Hz}$ (AC Version)


## CONVERSION RATE

- 4 Conversions per Second
- Hold and Read On Command


## CONTROL INPUTS

Display Blanking/Display Power Input: The display of the AD2026 can be blanked by removal of power to the display power input, with no effect on conversion circuitry. If externat logic switching is used, the display requires 110 mA peak ( 85 mA Average) when illuminated.
( $80.65 \pm 0.38 \times 45.97 \pm 0.38 \mathrm{~mm}$ )

## OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).



