# Low Cost 4 $1 / 2$ Digit DPMs With LED Displays AD2024/AD2027 

## FEATURES

"Second Generation" MOS-LSI Design
Large 0.43" (11mm) LED Displays
41/2 Digit Resolution - 20,000 Counts Full Scale
Limited Differential Input
Either Line Powered (AD2024) or Logic Powered (AD2027)
Industry Standard Case Designs

## APPLICATIONS

High Resolution/High Accuracy Readout for:


GENERAL DESCKIPTION
The AD2024 and AD2027 are low fost $41 / 2$ digit DP Mi with large LED displays. Both units offer the same fetures a ad identical performance, but the AD2024 is ac line powered and the AD2027 is +5 V dc powered.

## THE BENEFITS OF SECOND GENERATION DESIGN

The AD2024 and AD2027 are designed around MOS-LSI (Metal Oxide Semiconductor, Large Scale Integration) integrated circuits which greatly reduce the number of components and interconnections required to provide the performance and features expected in a high resolution $41 / 2$ digit DPM.

This "Second Generation" design, therefore, offers greatly increased reliability and significantly lower cost at little compromise in performance. The large 0.43 inch ( 11 mm ) LED displays offer the brightness and readability previously available only with gas discharge displays with the added advantage of an all solid state component.

## HIGH RESOLUTION AND VERSATILE FEATURES

These DPMs measure dc input voltages over a full scale range of $\pm 1.9999 \mathrm{~V}$ with an accuracy of $\pm 0.005 \%$ reading $\pm 0.005 \%$ of full scale $\pm 1$ digit. Using the "limited differential" input first used by Analog Devices on the AD2010, the AD2024 and AD2027 prevent ground loop problems and provide 50 dB of common mode rejection at common mode voltages up to $\pm 200 \mathrm{mV}$. Normal mode rejection is 25 dB at $50-60 \mathrm{~Hz}$.
BCD data outputs are provided in a bit parallel, character serial format compatible with CMOS logic systems. When applications require parallel BCD data, such as interfaces to printers, comparators or slave displays, parallel BCD output options are available that are compatible to standard TTL logic systems.
 or the A 2027) are frade avaitable for powering external circuitry. Ip addition, on ot inputs $f$ f conversion "Hold", fisp lay planking and dekimal point setection are proxided. INDUSTRY STAY DARDEASE DESI INS
Although both the A D2024 and AD200 27 bave ide ntic 1 electrical designs, theyare packaged in the cape sizes that have become industry standards for ac lin $\&$ powered apd $\sqrt[5 \mathrm{~V}]{\mathrm{da}}$ logic powered DPMs respectively. The AD2024 fits the $3.924^{\prime \prime}$ x $1.682^{\prime \prime}$ ( $99.67 \times 42.74 \mathrm{~mm}$ ) panel cutout comfnon to most ac line powered DPMs, and the AD2027 fits the $3.175^{\circ} \times$ $1.810^{\prime \prime}$ ( $80.65 \times 45.97 \mathrm{~mm}$ ) panel cutout of the Analog Devices logic power case design, now used by several other manufacturers of logic powered DPMs. Thus, interchangeability is assured, allowing mechanical second sourcing for both these DPMs.

## DESIGNED AND BUILT FOR RELIABILITY

Even beyond the reliability advantages of the LSI-IC design and LED displays, the AD2024 and AD2027 have had extreme care taken in their design and manufacture to insure reliability. Manufacturing processes are monitored by continual quality assurance inspections to insure proper workmanship and testing. Automatic test equipment is used to test each DPM thoroughly and without error. And each AD2024 and AD2027, like every Analog Devices DPM, receives a full one week failurefree burn-in before shipment.

## DISPLAY OUTPUTS

- Seven Segment LED Display, $0.43^{\prime \prime}$ ( 11 mm ) high, for four data digits, $100 \%$ overrange and polarity indication.
- Overload indication by blanking all digits, polarity sign remains valid.
- Decimal points (4) selectable at input.
- Display Blanking


## ANALOG INPUT

- Configuration: Bipolar, limited differential
- Full Scale Range: $\pm 1.9999 \mathrm{~V}$ ( $\pm 19.999 \mathrm{~V}$, "V" Option)
- Automatic Polarity
- Input Impedance: $100 \mathrm{M} \Omega$ ( $1 \mathrm{M} \Omega$, "V" Option)
- Bias Current: 30nA (3nA, "V" Option)
- Overvoltage Protection: 120 V rms sustained


## ACCURACY

$\pm 0.005 \%$ reading $\pm 0.005 \%$ full scale $\pm 1$ digit ${ }^{1}$

- Resolution: $100 \mu \mathrm{~V}$
- Temperature Range ${ }^{2}: 0$ to $+50^{\circ} \mathrm{C}$ operating. -20 to $+85^{\circ} \mathrm{C}$ storage (AD2024/B: 0 to $+45^{\circ} \mathrm{C}$ operating)
- Temperathre Coefficient: Gain: $\pm 30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$. Zero Offset: $\pm 10 \mu \mathrm{~V} \chi^{\circ} \mathrm{C} \pm 40 \mathrm{pm} / \mathrm{p}^{\circ}+100 \mu \mathrm{~V} / \mathrm{C}$, "V" Option)
- Warmup Tine: One misure to rated accuracy
- Settling Tinge to R tred Act
 COMMON MODE REJEETIO.
- 50 dB , de to $1 \mathrm{kHz}+200 \mathrm{~m} y \mathrm{conm}$
- AD2024 (floated on power supkly trasfermer if date outputs and control signals are not used -110 B at 120 rms common mode voltage, $1 \mathrm{k} \Omega$ imbalance at inpu


## CONVERSION TIME

- 180 ms for full scale reading
- 210 ms for overload conversion


## CONVERSION RATE

- 5 conversions per second

DIGITAL INTERFACE SIGNALS

- Inputs

Display Blank - (DTL/TTL Compatible, 4 TTL Loads). Logic " 0 " or grounding blanks display, including polarity sign, but not decimal points. The display is valid immediately upon removal of the blanking signal.
Hold - (DTL/TTL Compatible, 2 TTL Loads). Logic " 0 " or grounding causes the DPM to hold and display the last conversion. Upon removal of the hold, the DPM resumes conversions.
Decimal Points - (Not TTL Compatible). Logic " 0 " or grounding turns on appropriate decimal point. External circuitry must sink 60 mA when a decimal point is illuminated.

- Outputs

DTL/TTL Compatible ${ }^{3}$ - Status. Logic " 1 " indicates conversion in process. All digital outputs are valid when status is at logic " 0 ". 5 TTL loads. - Polarity. Logic " 1 " indicates positive polarity, latched. 5 TTL loads. CMOS and LP Schottky Compatible - Overload, Logic " 1 " indicates overload ( $\geqslant 20,000$ ), latched. - BCD outputs. $41 / 2 \mathrm{BCD}$ digits, character serial, bit parallel, 1 LP Schottky Load.
Digital Strobe Outputs - (CMOS or LP Schottky Compatible) Logic " 1 " on any of these line indicates the output data is valid for that digit, 1 LP Schottky Load.
Parallel BCD Output (Option "B"). $-41 / 2$ BCD digits, positive true, latched. Drives 5 TTL loads. - Data Ready. Logic " 1 " indicates output data is valid. Drives 2 TTL loads.

## EXTERNAL POWER SUPPLY OUTPUTS

- AD2024: +5 V at $25 \mathrm{~mA},-25 \mathrm{~V}$ at 5 mA
- AD2027: -25 V at 5 mA

POWER SUPPLY INPUTS

- AD2024: ac line, $50-60 \mathrm{~Hz}, 4.2 \mathrm{~W}$
- AD2027: $+5 \mathrm{~V} \mathrm{dc}, \pm 5 \%$ at 850 mA


## CALIBRATION ADJUSTMENTS

- Gain
- Zero
- Recommended recalibration interval: 6 months

SIZE

- AD2024: $3.92^{\prime \prime} \mathrm{W} \times 1.67^{\prime \prime} \mathrm{H} \times 4.48^{\prime \prime} \mathrm{D}(100 \times 42 \times 114 \mathrm{~mm})$ Panel cutout: $3.930^{\prime \prime} \times 1.682^{\prime \prime}(99.8 \times 42.7 \mathrm{~mm})$
- AD2027: $3^{\prime \prime} \mathrm{W} \times 1.8^{\prime \prime} \mathrm{H} \times 4^{\prime \prime} \mathrm{D}(76 \times 46 \times 102 \mathrm{~mm})$ Panel cutout: $3.175^{\prime \prime} \times 1.810^{\prime \prime}(80.65 \times 45.97 \mathrm{~mm})$


## WEIGHT

- AD2024: 14 ounces ( 395 grams)
- AD2027: 10 ounces ( 280 grams)

OPTIONS - ORDERING GUIDE

- AD2024

AC Power Inputs

card edge connector, Viking $2 \mathrm{VK} 15 \mathrm{D} / 1-2$ or equivalent.

- Optional: Order AC1501.
- AD2027

Input Range Options
AD2027 - $\pm 1.9999$ V Full Scale
AD2027/V - $\pm 19.999$ V Full Scale
Data Output Options
AD2027 - Character serial data outputs
AD2027/B - Parallel BCD option
Display Lens Options ${ }^{4}$
Lens 5 - Red with ADI logo
Lens 6 - Red without ADI logo
Connector - AC1501 (see above) or equivalent. AD2027/B option requires two each.

## NOTES

${ }^{1}$ Guaranteed at $+25^{\circ} \mathrm{C}$ and nominal power supply voltage.
${ }^{2}$ Guaranteed.
${ }^{3}$ For CMOS compatibility, 3.3 k pullup resistors to the +5 V output of the DPM are required.
${ }^{4}$ If no lens is specified, Lens 5 or 7 is supplied as appropriate. Specifications subject to change without notice.


Figure 1. AD2024 Wiring Interconnections
 Interfacing Data Outpus The standard data outputs are in a it parałtel charecte serial format compatible to CMOS and LP Setky logic ssstems. The BCD outputs are multiplexed, and a digit outplt is provided for each of the five digits of output. Thus, the four BCD lines on the output at a given time are valid for the digit whose digit line is currently high. The bit parallel character serial data is valid when the DPM status line is low for a period of 50 ms between each conversion cycle. The overload line is latched output which is at logic " 1 ", whenever the input exceeds the full 20,000 count range.
Optional parallel BCD outputs (" $B$ " option) are available on a separate card which is internally connected to the DPM. The output data then is presented in a full parallel format and is latched. The parallel data is valid whenever the data ready output is high.
Pin connections for the " $B$ " option are shown in the interconnection tables and a full timing diagram is shown in Figure 3.


NOTE 1. APPROX. 5 ms REQUIRED TO LOAD PARALLEL-OUT REGISTERS. NOTE 2. B OPTION.

| PIN | FUNCTION | PIN | FUNCTION |
| :---: | :---: | :---: | :---: |
| 1 | Analog Ground | A | Analog High |
| 2 | $10^{1}$ Digit | B | $10^{\circ}$ Digit (LSD) |
| 3 | BCD 2 | C | $10^{2}$ Digit |
| 4 | $10^{3}$ Digit | D | BCD 4 |
| 5 | BCD 8 | E | BCD 1 |
| 6 | Mux Step 1 | F | $10^{4}$ Digit (MSD) |
| 7 | +5V dc (Out) | H | -25V dc (Out) |
| 8 | NC | J | NC |
| 9 | ac High | K | ac High |
| 10 | NC | L | NC |
| 11 | ac RETURN | M | ac Return |
| 12 | NC | N | NC |
| 13 | Digital Ground | P | Digital Ground |
| 14 | NC | R | Status |
| 15 | Polarity | S | Hold |
| 16 | Display Blank | $T$ | DP1.XXXX |
| 17 | DP1XXX.X | U | DP1X.XXX |
| 18 | Overload | V | DP1XX.XX |

AD2024 - Signal and Pin Connections


NOTE 1: BCD multiplexer clock pulse available for remote placement of BCD option.

## AD2027 - Signal and Pin Connections

| PIN | FUNCTION | PIN | FUNCTION |
| :---: | :---: | :---: | :---: |
| 1 | +5V dc ( l ) | A | NC (Do Not Use) |
| 2 | BCD 1 (In) | B | BCD 8 (In) |
| 3 | BCD 4 (In) | C | BCD 2 (In) |
| 4 | BCD 2000 | D | BCD 4 |
| 5 | BCD 40 | E | BCD 80 |
| 6 | BCD 800 | F | BCD 8000 |
| 7 | BCD 100 | H | BCD 1000 |
| 8 | BCD 10,000 | J | BCD 200 |
| 9 | BCD 20 | K | BCD 2 |
| 10 | BCD 8 | L | BCD 10 |
| 11 | BCD 1 | M | BCD 4000 |
| 12 | BCD 400 | N | $10^{4}$ Digit ( In ) |
| 13 | Data Ready | P | Status (In) |
| 14 | Polarity (In) | R | Mux Step (In) |
| 15 | Polarity (Out) | S | Digital Ground |

NOTES: 1) Overload output is on main DPM connector. 2) Pins marked "In" are made available for remote placement of BCD option card and are not normally used since all connections are interval.


Figure 5. AD2027 Mechanical Outline (Dimensions shown in inches and (mm))

