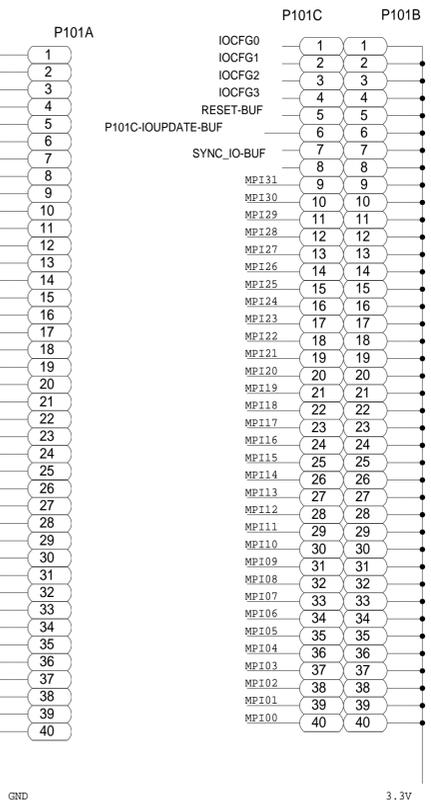
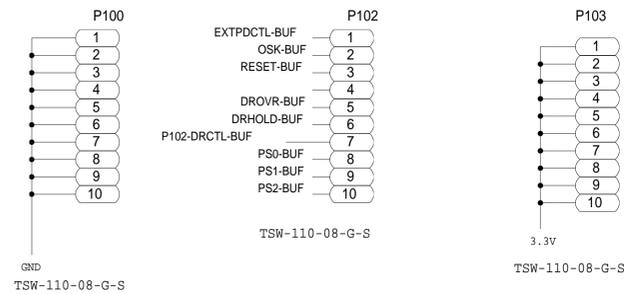


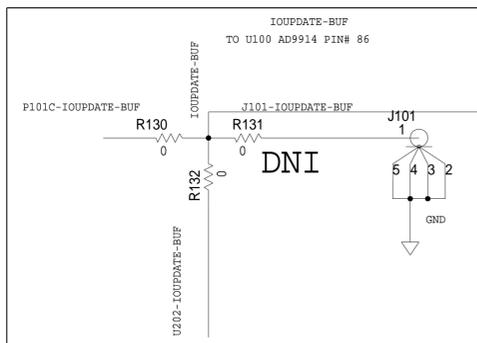
CONFIGURE AS TRIPLE ROW HEADER
 LABEL ALL SIGNALS ON SILKSCREEN AT PIN
 LABEL POWER/GND ON BOTH EDGES OF HEADER



CONFIGURE AS TRIPLE ROW HEADER
 LABEL ALL SIGNALS ON SILKSCREEN AT PIN
 LABEL POWER/GND ON BOTH EDGES OF HEADER



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
E	CONTACTOR SYMBOL REPLACED WITH CORRECT LIBRARY SYMBOL QFN88_12X12_PAD_6X6	03/01/2012	M.S.



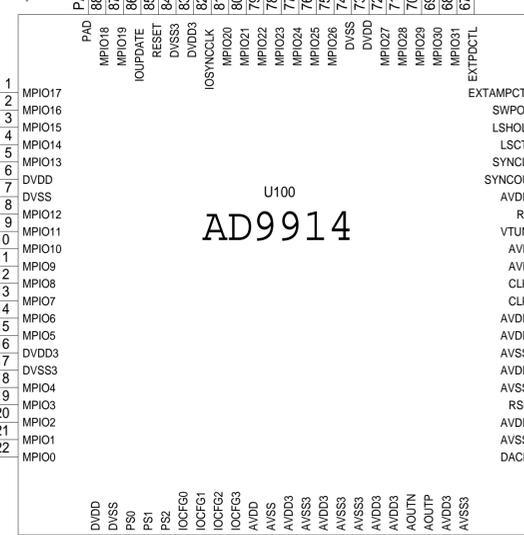
ONLY USE WITH J101

DNI

PIN 1

PLEASE SILKSCREEN AT PIN

REMOVE FOR PARALLEL USE



U100
AD9914

EXTAMPCTL

OSK-BUF

DROVR

DRHOLD-BUF

LSHOLD

DRCTL-BUF

SYNC IN

SYNC OUT

SYNC IN

SYNC OUT

AVDD3

REG

VTUNE

AVDD

1.8V

CLKP

600OHM

CLKN

AVDD3

3.3V

AVSS3

3.3V

AVSS3

RSET

AVDD3

3.3V

AVSS3

DACBP

AVSS3

DACBP

AD9914_PRELIM

100 OHM DIFF PAIR

3.3V

DNI

R123

50

DNI

R124

50

3.3V

0.1UF

C106

0.1UF

C107

0.1UF

T102

TC1-1-13M+

T102 PIN3 TO J108 ALL GND PINS

GROUND POUR TOP LAYER

3.3V

DNI

R125

150

GND

R126

150

GND

0.1UF

C114

0.1UF

R122

100

GND

100 OHM DIFF PAIR

3.3V

VTUNE

DNI

R111B

TP100

TPGND

DNI

C117

0.22UF

C116B

0.22UF

SEVERAL VIAS

CLOSE TO DUT

GND

J103

SYNC OUT

J102

SYNC IN

CLASE TO DUT

GND

R139B

50

DNI

R137B

50

GND

DRCTL-BUF

ONLY USE WITH J105

DNI

R137B

50

GND

EXTAMPCTL

OSK-BUF

DROVR

DRHOLD-BUF

LSHOLD

DRCTL-BUF

SYNC IN

SYNC OUT

SYNC IN

SYNC OUT

AVDD3

3.3V

REG

VTUNE

AVDD

1.8V

CLKP

600OHM

CLKN

AVDD3

3.3V

AVSS3

3.3V

AVSS3

RSET

AVDD3

3.3V

AVSS3

DACBP

AVSS3

DACBP

AD9914_PRELIM

100 OHM DIFF PAIR

3.3V

VTUNE

DNI

R111B

TP100

TPGND

DNI

C117

0.22UF

C116B

0.22UF

SEVERAL VIAS

CLOSE TO DUT

GND

J103

SYNC OUT

J102

SYNC IN

CLASE TO DUT

GND

R139B

50

DNI

R137B

50

GND

DRCTL-BUF

ONLY USE WITH J105

DNI

R137B

50

GND

EXTAMPCTL

OSK-BUF

DROVR

DRHOLD-BUF

LSHOLD

DRCTL-BUF

SYNC IN

SYNC OUT

SYNC IN

SYNC OUT

AVDD3

3.3V

REG

VTUNE

AVDD

1.8V

CLKP

600OHM

CLKN

AVDD3

3.3V

AVSS3

3.3V

AVSS3

RSET

AVDD3

3.3V

AVSS3

DACBP

AVSS3

DACBP

AD9914_PRELIM

100 OHM DIFF PAIR

3.3V

VTUNE

DNI

R111B

TP100

TPGND

DNI

C117

0.22UF

C116B

0.22UF

SEVERAL VIAS

CLOSE TO DUT

GND

J103

SYNC OUT

J102

SYNC IN

CLASE TO DUT

GND

R139B

50

DNI

R137B

50

GND

DRCTL-BUF

ONLY USE WITH J105

DNI

R137B

50

GND

EXTAMPCTL

OSK-BUF

DROVR

DRHOLD-BUF

LSHOLD

DRCTL-BUF

SYNC IN

SYNC OUT

SYNC IN

SYNC OUT

AVDD3

3.3V

REG

VTUNE

AVDD

1.8V

CLKP

600OHM

CLKN

AVDD3

3.3V

AVSS3

3.3V

AVSS3

RSET

AVDD3

3.3V

AVSS3

DACBP

AVSS3

DACBP

AD9914_PRELIM

100 OHM DIFF PAIR

3.3V

VTUNE

DNI

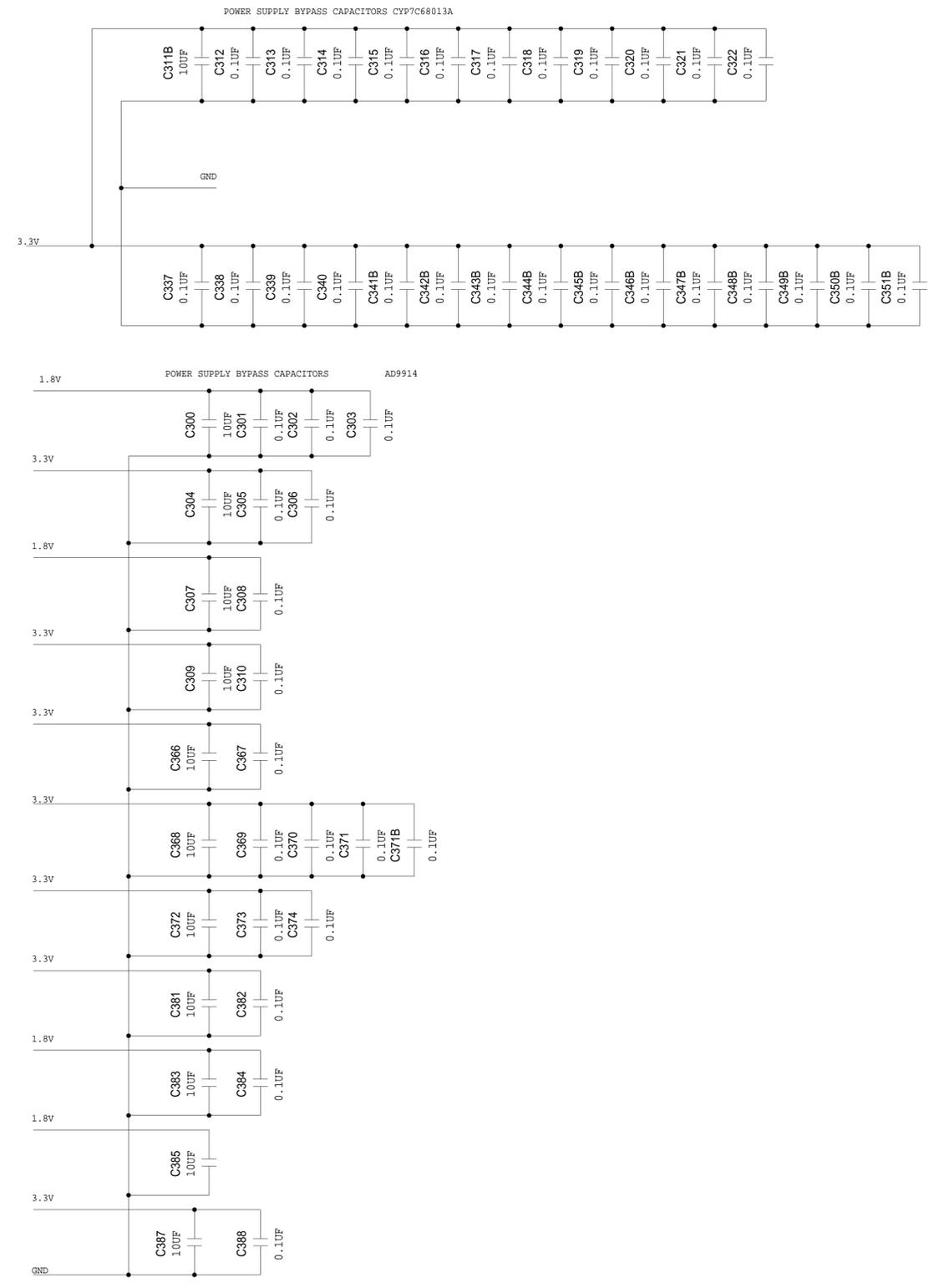
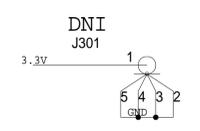
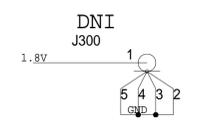
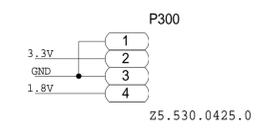
R111B

TP100

TPGND

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

POWER SUPPLY HOOKUP
 LABEL ALL SUPPLY NAMES ON SILKSCREEN AT PIN



	SCHEMATIC			
	<DRAWING_TITLE_HEADER> AD9914 ENGINEERING BD.			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 9914CE01	REV E	
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE NONE	SHEET 3 OF 3

Grid lines A-D and 1-8 are present on the drawing.