

Micropower; Low Cost; Fixed 5 V, 12 V and Adjustable DC-to-DC Converter

ADP1109



that when a logic low is applied it will shut the oscillator down. The 120 kHz operating frequency allows for the use of small

surface mount components. The gated oscillator capability eliminates the need for frequency compensation.

Model	Output Voltage	Package Description	Package Option*				
ADP1109AN	09AN ADJ PDIP		N-8				
ADP1109AR	ADJ	SOIC	SO-8				
ADP1109AN-5	5 V	PDIP	N-8				
ADP1109AR-5	5 V	SOIC	SO-8				
ADP1109AN-12	12 V	PDIP	N-8				
ADP1109AR-12	12 V	SOIC	SO-8				

*For outline information see Package Information section.

V_{IN} 1 NC 2 SW 3 GND 4 VIN 1 ADP1109 7 SHUTDOWN 7 SHUTDOWN 6 NC 5 NC

8-Lead SOIC

(SO-8 Package)

NC = NO CONNECT

This information applies to a product under development. Its characteristics and specifications are subject to change without notice. Analog Devices assumes no obligation regarding future manufacture unless otherwise agreed to in writing.

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ADP1109—SPECIFICATIONS (@ $T_A = +25^{\circ}C$, $V_{IN} = 3 V$, unless otherwise noted)

			ADP1109			
Parameter	Conditions ¹	Vs	Min	Тур	Max	Units
QUIESCENT CURRENT*	Switch Off	I _Q		320	550	μΑ
INPUT VOLTAGE*		V_{IN}	3			V
COMPARATOR TRIP POINT VOLTAGE*	ADP1109		1.20	1.25	1.30	V
OUTPUT VOLTAGE* ADP1109-5 ADP1109-12	$\begin{array}{l} 3 \hspace{0.1cm} V \leq V_{IN} \leq 5 \hspace{0.1cm} V \\ 3 \hspace{0.1cm} V \leq V_{IN} \leq 12 \hspace{0.1cm} V \end{array}$	V _{OUT}	4.75 11.45	5.00 12.00	5.25 12.55	V V
OUTPUT VOLTAGE RIPPLE*	ADP1109-5 ADP1109-12			25 60	50 120	mV mV
OSCILLATOR FREQUENCY*		$\mathbf{f}_{\mathrm{OSC}}$	100 90	120	140 150	kHz kHz
purry cycle*	Full Load	DC	45	50	60	%
SWATCH ON TIME*		t _{on}	3.3 3.0	4.2	5.3 5.5	μs μs
SWITCH SATURATION VOLTAGE ADP1109-5 ADP1109-12	$I_{SW} = 300 \text{ mA}$ $V_{IN} = 3 \text{ V}$ $V_{N} = 5 \text{ V}$	VCIISAT		$\begin{array}{c} 0.4 \\ 0.5 \\ 0 \\ 0 \\ 5 \end{array}$	0.7 0.8 0.8	V V
SWITCH LEAKAGE CURRENT	$V_{SW} = 12V$			1	7 10	- HA
SHUTDOWN PIN HIGH*			2.0		' / _ /	
SHUTDOWN PIN LOW*				ן ר	þ.8 /	
SHUTDOWN PIN INPUT CURRENT*	$V_{SHUTDOWN} = 4 V$	I _{IH}			J ₁₀ /	μΑ
SHUTDOWN PIN INPUT CURRENT*	$V_{SHUTDOWN} = 0 V$	I _{IL}			20	- try

NOTES

*Denotes the specifications that apply over the full operating temperature range.

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