



AHEAD OF WHAT'S POSSIBLE™

HIGH DOSE RADIATION TEST REPORT ADUM7442S

July 2016
Generic



Radiation Test Report	
Product:	ADuM7442S
Gamma:	0,100k, 168hr Accelerated Anneal
Gamma Source:	Co60/TM1019 Condition A
Dose Rate:	129 Rad/s
Facilities:	VPT RAD
Tested:	7/7/16

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Wafer #	SN	Idd1 Quiescent 4.5V, 4.5V			Idd2 Quiescent 4.5V, 4.5V			Idd1 Dynamic 2MBPS 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.3604	2.3336	2.3478	2.3661	2.3735	2.3226	3.10416	3.09269	3.07743	
	68	2.3381	2.2237	2.3181	2.3486	2.3503	2.3486	3.11507	2.98270	3.08252	
	11	2.3193	2.3564	2.3617	2.3507	2.3710	2.3551	3.11934	3.08207	3.11833	
	12	2.3263	2.2603	2.3425	2.3372	2.3417	2.3458	3.07678	2.95886	3.06282	
	13	2.3112	2.2677	2.3181	2.3023	2.3580	2.3181	3.09542	2.96940	3.0601	
	14	2.3698	2.2603	2.3535	2.3470	2.3682	2.3560	3.10868	2.97530	3.07743	
	15	2.3141	2.2424	2.3466	2.3393	2.3869	2.3311	3.11474	2.97680	3.08276	
	16	2.3238	2.2725	2.3495	2.3433	2.3722	2.3560	3.10832	2.96671	3.07674	
	71	2.3108	2.2681	2.3206	2.3702	2.3905	2.4072	3.10193	2.96346	3.06799	
	72	2.3336	2.2787	2.3507	2.3193	2.3637	2.3523	3.11487	2.98506	3.08842	
	73	2.3568	2.3047	2.3804	2.3759	2.4015	2.3885	3.13598	2.98909	3.09558	
	74	2.3173	2.2900	2.3527	2.3629	2.3946	2.3828	3.12191	2.97412	3.09188	
	75	2.3588	2.3027	2.3417	2.3881	2.4207	2.3767	3.12777	2.98230	3.09241	
	76	2.3014	2.2534	2.3238	2.3421	2.3893	2.3759	3.11450	2.98157	3.07629	
	Min	2.3014	2.2424	2.3181	2.3023	2.3417	2.3181	3.07678	2.95886	3.0601	
	Max	2.3698	2.3564	2.3804	2.3881	2.4207	2.4072	3.13598	3.08207	3.11833	
	Mean	2.3286	2.2798	2.3451	2.3482	2.3799	2.3621	3.11169	2.98373	3.0825625	
	Std. Dev	0.0218	0.0306	0.0178	0.0239	0.0215	0.0251	0.01549	0.03225	0.016161638	
	Mean - 3 Sigma	2.2631	2.1880	2.2916	2.2766	2.3154	2.2867	3.06523	2.88697	3.034077586	
Mean + 3 Sigma	2.3941	2.3716	2.3987	2.4198	2.4443	2.4375	3.15814	3.08049	3.131047414		
Limit: <3.8mA				Limit: >-3.4mA				Limit: <20mA			

Wafer #	SN	Idd2 Dynamic 2MBPS 4.5V, 4.5V			Idd1 Dynamic 10MBPS 4.5V, 4.5V			Idd2 Dynamic 10MBPS 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.0739	3.0590	3.0696	7.2802	7.2474	7.1816	6.94545	6.94215	6.98439	
	68	3.1295	3.0805	3.0937	7.4621	7.2411	7.3447	7.16623	7.04135	7.06162	
	11	3.0798	3.0672	3.0820	7.5010	7.2055	7.3352	7.03016	6.88087	6.96957	
	12	3.0762	3.0453	3.0575	7.2640	6.9125	7.1319	7.14825	6.88392	6.9758	
	13	3.1062	3.0674	3.0726	7.4356	7.0277	7.2217	7.19793	6.92221	7.00066	
	14	3.0874	3.0695	3.0699	7.2642	6.9504	7.1328	7.10556	6.92152	6.96506	
	15	3.1275	3.0824	3.0945	7.3870	7.0231	7.2388	7.23776	6.98813	7.05091	
	16	3.1075	3.0782	3.0788	7.3676	6.9537	7.1753	7.17681	6.89357	6.94158	
	71	3.1305	3.0876	3.0902	7.4021	6.9847	7.1708	7.18629	6.92079	6.95301	
	72	3.0979	3.0664	3.0614	7.3957	7.0130	7.1828	7.17925	6.92893	6.92152	
	73	3.1492	3.1129	3.1143	7.4084	6.9843	7.1899	7.24350	7.00807	7.01433	
	74	3.1193	3.0861	3.1023	7.5116	7.0000	7.2835	7.12326	6.92925	6.95961	
	75	3.1319	3.0927	3.0969	7.4583	6.9699	7.2448	7.11024	6.86297	6.93051	
	76	3.1259	3.0898	3.1060	7.5184	7.1101	7.3062	7.14027	6.95977	7.03826	
	Min	3.0762	3.0453	3.0575	7.2640	6.9125	7.1319	7.03016	6.86297	6.92152	
	Max	3.1492	3.1129	3.1143	7.5184	7.2055	7.3352	7.24350	7.00807	7.05091	
	Mean	3.1116	3.0788	3.0855	7.4095	7.0112	7.2178	7.15661	6.92500	6.976735	
	Std. Dev	0.0229	0.0172	0.0181	0.0845	0.0786	0.0659	0.06018	0.04331	0.041274056	
	Mean - 3 Sigma	3.0428	3.0272	3.0313	7.1560	6.7754	7.0202	6.97606	6.79506	6.852912831	
Mean + 3 Sigma	3.1804	3.1303	3.1398	7.6629	7.2470	7.4154	7.33715	7.05494	7.100557169		
Limit: <20mA				Limit: <20mA				Limit: <20mA			

Wafer #	SN	Idd1 Dynamic 25MBPS 4.5V, 4.5V			Idd2 Dynamic 25MBPS 4.5V, 4.5V			CHA Prop Delay Rise 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	14.6934	14.6559	14.4702	14.0472	14.0724	14.2007	40.06400	40.06400	40.377	
	68	15.2632	14.8876	14.9362	14.6359	14.3380	14.4121	41.00300	41.62900	41.316	
	11	15.2377	14.5488	14.8825	14.2701	13.9174	14.1703	40.69000	40.37700	40.69	
	12	14.7137	13.9594	14.3818	14.5829	13.9081	14.1848	41.31600	40.06400	40.69	
	13	15.1713	14.2724	14.6467	14.7761	14.0268	14.2518	41.62900	40.37700	41.003	
	14	14.7282	14.0293	14.4006	14.4718	13.9769	14.1160	40.37700	39.75100	40.064	
	15	15.0908	14.2644	14.7284	14.7921	14.1335	14.3397	41.31600	40.69000	41.003	
	16	15.0430	14.0497	14.4748	14.6735	13.9444	14.0769	40.69000	39.75100	40.064	
	71	15.1636	14.1827	14.5631	14.6514	13.9600	14.1136	41.62900	41.00300	41.316	
	72	15.0861	14.1847	14.5124	14.6940	14.0514	14.0443	41.31600	40.37700	40.69	
	73	15.0481	14.1177	14.4766	14.8212	14.1812	14.2181	40.69000	40.06400	40.064	
	74	15.3997	14.3632	14.8296	14.4805	13.9716	14.0809	41.94200	41.31600	41.629	
	75	15.2718	14.1534	14.6841	14.4346	13.8151	13.9911	41.31600	40.69000	41.003	
	76	15.3772	14.4803	14.8436	14.5669	14.0727	14.3130	41.62900	41.00300	41.629	
	Min	14.7137	13.9594	14.3818	14.2701	13.8151	13.9911	40.37700	39.75100	40.064	
	Max	15.3997	14.5488	14.8825	14.8212	14.1812	14.3397	41.94200	41.31600	41.629	
	Mean	15.1109	14.2172	14.6187	14.6013	13.9966	14.1584	41.21167	40.45525	40.82041667	
	Std. Dev	0.2167	0.1788	0.1760	0.1657	0.1021	0.1076	0.48734	0.50160	0.557653437	
	Mean - 3 Sigma	14.4609	13.6806	14.0907	14.1042	13.8356	13.8356	39.74965	38.95045	39.14745635	
Mean + 3 Sigma	15.7610	14.7537	15.1467	15.0983	14.3029	14.4811	42.67369	41.96005	42.49337698		
Limit: <20mA				Limit: <20mA				Limit: 29<x<50ns			

Wafer #	SN	CHB Prop Delay Rise 4.5V, 4.5V			CHC Prop Delay Rise 4.5V, 4.5V			CHD Prop Delay Rise 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	40.0640	40.0640	40.0640	40.3770	40.0640	40.0640	40.69000	40.69000	40.377	
	68	41.0030	41.3160	41.3160	40.6900	40.6900	41.0030	40.69000	40.69000	40.69	
	11	41.0030	40.3770	40.6900	41.0030	40.0640	40.6900	41.31600	40.37700	40.69	
	12	40.6900	40.0640	40.0640	41.3160	41.0030	40.3770	40.69000	40.37700	40.377	
	13	41.0030	40.0640	40.6900	40.6900	40.3770	41.0030	41.00300	40.37700	40.69	
	14	40.0640	39.4380	39.7510	40.6900	40.0640	40.3770	40.37700	40.06400	40.064	
	15	40.3770	39.7510	40.0640	40.6900	40.3770	40.6900	40.69000	40.06400	40.377	
	16	40.6900	39.7510	40.3770	40.0640	39.4380	40.0640	40.69000	40.06400	40.377	
	71	40.6900	40.0640	40.3770	40.6900	40.0640	40.3770	40.37700	40.06400	40.377	
	72	40.3770	39.7510	39.7510	40.6900	40.0640	40.3770	40.69000	40.06400	40.377	
	73	40.3770	39.7510	40.0640	40.6900	40.3770	40.6900	40.69000	40.06400	40.377	
	74	40.6900	39.7510	40.3770	40.0640	39.4380	40.0640	40.69000	40.06400	40.377	
	75	40.6900	40.0640	40.3770	40.6900	40.0640	40.3770	40.37700	40.06400	40.377	
	76	40.3770	39.7510	39.7510	40.6900	40.0640	40.3770	40.69000	40.06400	40.377	
	Min	40.0640	39.4380	39.7510	40.0640	39.4380	40.0640	40.37700	40.06400	40.064	
	Max	41.0030	40.3770	40.6900	41.3160	41.0030	41.0030	41.31600	40.37700	40.69	
	Mean	40.5857	39.8814	40.1944	40.6639	40.1162	40.4553	40.69000	40.14225	40.40308333	
	Std. Dev	0.2778	0.2482	0.3392	0.3392	0.4185	0.2711	0.26693	0.14156	0.161172668	
	Mean - 3 Sigma	39.7522	39.1368	39.1769	39.6464	38.8606	39.6421	39.88922	39.71757	39.91956533	
	Mean + 3 Sigma	41.4191	40.6260	41.2119	41.6814	41.3717	41.2684	41.49078	40.56693	40.88660134	
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHA Prop Delay Fall 4.5, 4p.5			CHB Prop Delay Fall 4.5V, 4.5V			CHC Prop Delay Fall 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	40.3770	40.6900	40.6900	41.0030	41.0030	41.0030	41.31600	41.00300	41.003	
	68	41.9420	42.2550	41.9420	41.6290	41.9420	41.9420	41.31600	41.31600	41.629	
	11	41.6290	40.6900	41.3160	42.2550	41.0030	41.6290	41.62900	41.00300	41.316	
	12	41.9420	40.6900	41.0030	41.6290	40.3770	41.0030	41.94200	41.62900	41.316	
	13	42.2550	41.0030	41.6290	41.9420	41.0030	41.3160	41.94200	41.31600	41.942	
	14	41.3160	40.3770	40.6900	41.0030	40.3770	40.6900	41.62900	41.00300	41.316	
	15	41.9420	41.3160	41.3160	41.3160	40.3770	41.0030	41.62900	41.00300	41.316	
	16	41.3160	40.0640	40.6900	41.3160	40.3770	40.6900	41.31600	40.37700	40.69	
	71	42.2550	41.6290	41.9420	41.6290	41.0030	41.0030	41.31600	41.00300	41.316	
	72	42.2550	41.0030	41.3160	41.3160	40.3770	40.6900	41.62900	41.00300	41.003	
	73	41.3160	40.3770	40.3770	41.3160	40.3770	40.6900	41.31600	40.37700	41.003	
	74	42.2550	41.3160	41.6290	42.2550	41.3160	41.6290	41.31600	40.06400	40.69	
	75	41.6290	41.0030	41.6290	41.3160	40.6900	41.0030	41.00300	40.06400	41.003	
	76	41.9420	41.0030	41.6290	41.9420	41.3160	41.6290	41.62900	41.31600	41.629	
	Min	41.3160	40.0640	40.3770	41.0030	40.3770	40.6900	41.00300	40.06400	40.69	
	Max	42.2550	41.6290	41.9420	42.2550	41.3160	41.6290	41.94200	41.62900	41.942	
	Mean	41.8377	40.8726	41.2638	41.6029	40.7161	41.0813	41.52467	40.84650	41.21166667	
	Std. Dev	0.3853	0.4518	0.4781	0.4105	0.3882	0.3804	0.27783	0.50821	0.361421269	
	Mean - 3 Sigma	40.6818	39.5173	39.8295	40.3715	39.5516	39.9400	40.69119	39.32186	40.12740286	
	Mean + 3 Sigma	42.9935	42.2279	42.6982	42.8343	41.8805	42.2225	42.35815	42.37114	42.29593047	
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHD Prop Delay Fall 4.5V, 4.5V			CHA PWD 4.5V, 4.5V			CHB PWD 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	42.5680	42.5680	42.5680	0.3130	0.6260	0.3130	0.93900	0.93900	0.939	
	68	42.8810	42.8810	42.8810	0.9390	0.6260	0.6260	0.62600	0.62600	0.626	
	11	42.8810	42.2550	42.5680	0.9390	0.3130	0.6260	1.25200	0.62600	0.939	
	12	42.5680	41.9420	42.2550	0.6260	0.6260	0.3130	0.93900	0.31300	0.939	
	13	43.1940	42.2550	42.5680	0.6260	0.6260	0.6260	0.93900	0.93900	0.626	
	14	42.5680	41.9420	42.2550	0.9390	0.6260	0.6260	0.93900	0.93900	0.939	
	15	42.5680	41.9420	42.2550	0.6260	0.6260	0.3130	0.93900	0.62600	0.939	
	16	43.1940	41.9420	42.2550	0.6260	0.3130	0.6260	0.62600	0.62600	0.313	
	71	41.9420	41.3160	41.6290	0.6260	0.6260	0.6260	0.93900	0.93900	0.626	
	72	42.8810	41.9420	42.5680	0.9390	0.6260	0.6260	0.93900	0.62600	0.939	
	73	42.2550	41.3160	41.6290	0.6260	0.3130	0.3130	0.93900	0.93900	0.626	
	74	42.5680	41.6290	42.2550	0.3130	0.0000	0.0000	0.62600	0.62600	0.626	
	75	41.9420	41.3160	41.6290	0.3130	0.3130	0.6260	0.62600	0.31300	0.313	
	76	42.5680	41.9420	42.2550	0.3130	0.0000	0.0000	0.93900	0.93900	0.939	
	Min	41.9420	41.3160	41.6290	0.3130	0.0000	0.0000	0.62600	0.31300	0.313	
	Max	43.1940	42.2550	42.5680	0.9390	0.6260	0.6260	1.25200	0.93900	0.939	
	Mean	42.5941	41.8116	42.1768	0.6260	0.4173	0.4434	0.88683	0.70425	0.730333333	
	Std. Dev	0.4105	0.3392	0.3563	0.2312	0.2437	0.2482	0.18071	0.23593	0.24367017	
	Mean - 3 Sigma	41.3627	40.7941	41.1080	-0.0675	-0.3137	-0.3012	0.34470	-0.00355	-0.000677175	
	Mean + 3 Sigma	43.8255	42.8291	43.2455	1.3195	1.1483	1.1880	1.42897	1.41205	1.461343842	
Limit: 29<x<50ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	CHC PWD 4.5V, 4.5V			ns	CHD PWD 4.5V, 4.5V			ns	AB_Rise Match Codirection 4.5V, 4.5V			ns
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	0.9390	0.9390	0.9390	1.8780	1.8780	2.1910	0.00000	0.00000	0.31300	0.313		
	68	0.6260	0.6260	0.6260	2.1910	2.1910	2.1910	0.00000	0.31300	0.31300	0		
	11	0.6260	0.9390	0.6260	1.5650	1.8780	1.8780	0.31300	0.00000	0.00000	0		
	12	0.6260	0.6260	0.9390	1.8780	1.5650	1.8780	0.62600	0.00000	0.00000	0.626		
	13	1.2520	0.9390	0.9390	2.1910	1.8780	1.8780	0.62600	0.31300	0.31300	0.313		
	14	0.9390	0.9390	0.9390	2.1910	1.8780	2.1910	0.31300	0.31300	0.31300	0.313		
	15	0.9390	0.6260	0.6260	1.8780	1.8780	1.8780	0.93900	0.93900	0.93900	0.939		
	16	1.2520	0.9390	0.6260	2.5040	1.8780	1.8780	0.00000	0.00000	0.00000	0.313		
	71	0.6260	0.9390	0.9390	1.5650	1.2520	1.2520	0.93900	0.93900	0.93900	0.939		
	72	0.9390	0.9390	0.6260	2.1910	1.8780	2.1910	0.93900	0.62600	0.62600	0.939		
	73	0.9390	0.9390	0.9390	1.8780	1.5650	1.5650	0.31300	0.62600	0.62600	0		
	74	0.6260	0.6260	0.6260	2.1910	2.1910	2.1910	0.31300	0.62600	0.62600	0.626		
	75	0.9390	0.6260	0.9390	1.8780	1.8780	1.5650	0.62600	0.31300	0.31300	0.313		
	76	0.6260	0.9390	0.6260	1.8780	1.8780	1.8780	0.62600	0.62600	0.62600	0.939		
	Min	0.6260	0.6260	0.6260	1.5650	1.2520	1.2520	0.00000	0.00000	0.00000	0		
	Max	1.2520	0.9390	0.9390	2.5040	2.1910	2.1910	0.93900	0.93900	0.93900	0.939		
	Mean	0.8608	0.8347	0.7825	1.9823	1.7998	1.8519	0.54775	0.44342	0.521666667			
	Std. Dev	0.2359	0.1541	0.1635	0.2778	0.2359	0.2818	0.30214	0.33917	0.361421269			
Mean - 3 Sigma	0.1530	0.3723	0.2921	1.1489	1.0920	1.0065	-0.35867	-0.57411	-0.562597139				
Mean + 3 Sigma	1.5685	1.2970	1.2729	2.8158	2.5075	2.6973	1.45417	1.46094	1.605930472				
Limit: <5ns				Limit: <5ns				Limit: <4ns					

Wafer #	SN	AB_Fall Match Codirection 4.5V, 4.5V			ns	CD_Rise Match Codirection 4.5V, 4.5V			ns	CD_Fall Match Codirection 4.5V, 4.5V			ns
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	0.6260	0.3130	0.3130	0.3130	0.6260	0.3130	0.3130	1.25200	1.56500	1.565		
	68	0.3130	0.3130	0.0000	0.0000	0.0000	0.0000	0.3130	1.56500	1.56500	1.252		
	11	0.6260	0.3130	0.3130	0.3130	0.3130	0.3130	0.0000	1.25200	1.25200	1.252		
	12	0.3130	0.3130	0.0000	0.0000	0.6260	0.0000	0.0000	0.62600	0.31300	0.939		
	13	0.3130	0.0000	0.3130	0.3130	0.3130	0.0000	0.3130	1.25200	0.93900	0.626		
	14	0.3130	0.0000	0.0000	0.0000	0.3130	0.0000	0.3130	0.93900	0.93900	0.939		
	15	0.6260	0.9390	0.3130	0.0000	0.3130	0.3130	0.3130	0.93900	0.93900	0.939		
	16	0.0000	0.3130	0.0000	0.6260	0.6260	0.3130	0.3130	1.87800	1.56500	1.565		
	71	0.6260	0.6260	0.9390	0.3130	0.0000	0.0000	0.0000	0.62600	0.31300	0.313		
	72	0.9390	0.6260	0.6260	0.6260	0.0000	0.0000	0.0000	1.25200	0.93900	1.565		
	73	0.0000	0.0000	0.3130	0.0000	0.3130	0.0000	0.0000	0.93900	0.93900	0.626		
	74	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	0.0000	1.25200	1.56500	1.565		
	75	0.3130	0.3130	0.6260	0.0000	0.0000	0.0000	0.0000	0.93900	1.25200	0.626		
	76	0.0000	0.3130	0.0000	0.0000	0.3130	0.0000	0.6260	0.93900	0.62600	0.626		
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.62600	0.31300	0.313		
	Max	0.9390	0.9390	0.9390	0.6260	0.6260	0.6260	0.6260	1.87800	1.56500	1.565		
	Mean	0.3391	0.3130	0.2869	0.2608	0.2087	0.1565	1.06942	0.96508	0.965083333			
	Std. Dev	0.3118	0.2984	0.3118	0.2247	0.2437	0.2110	0.33917	0.41046	0.431612717			
Mean - 3 Sigma	-0.5964	-0.5823	-0.6485	-0.4131	-0.5223	-0.4766	0.05189	-0.26630	-0.329754819				
Mean + 3 Sigma	1.2745	1.2083	1.2224	0.9348	0.9397	0.7896	2.08694	2.19646	2.259921486				
Limit: <4ns				Limit: <4ns				Limit: <4ns					

Wafer #	SN	AB_C Rise Match Opposing 4.5V, 4.5V			ns	AB_C Fall Match Opposing 4.5V, 4.5V			ns	AB_D Rise Match Opposing 4.5V, 4.5V			ns
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	0.3130	0.0000	0.3130	0.9390	0.6260	0.3130	0.62600	0.62600	0.62600	0.313		
	68	0.3130	0.9390	0.3130	0.6260	0.9390	0.3130	0.31300	0.93900	0.93900	0.626		
	11	0.3130	0.3130	0.0000	0.6260	0.3130	0.3130	0.62600	0.00000	0.00000	0		
	12	0.6260	0.9390	0.3130	0.3130	1.2520	0.3130	0.62600	0.31300	0.31300	0.313		
	13	0.9390	0.3130	0.3130	0.3130	0.3130	0.6260	0.62600	0.31300	0.31300	0.313		
	14	0.6260	0.6260	0.6260	0.6260	0.6260	0.6260	0.31300	0.62600	0.62600	0.313		
	15	0.6260	0.6260	0.6260	0.3130	0.6260	0.3130	0.62600	0.62600	0.62600	0.626		
	16	0.6260	0.3130	0.3130	0.0000	0.3130	0.0000	0.00000	0.31300	0.31300	0.313		
	71	0.9390	0.9390	0.9390	0.9390	0.6260	0.6260	1.25200	0.93900	0.93900	0.939		
	72	0.6260	0.3130	0.6260	0.6260	0.6260	0.3130	0.62600	0.31300	0.31300	0.626		
	73	0.3130	0.6260	0.0000	0.0000	0.0000	0.0000	0.62600	0.31300	0.31300	0		
	74	1.2520	1.8780	1.5650	0.9390	1.2520	0.9390	1.56500	1.87800	1.87800	1.565		
	75	1.2520	1.2520	0.9390	0.6260	0.9390	0.6260	1.25200	1.25200	1.25200	0.939		
	76	0.6260	0.6260	0.6260	0.3130	0.3130	0.0000	0.93900	0.93900	0.93900	1.252		
	Min	0.3130	0.3130	0.0000	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000	0		
	Max	1.2520	1.8780	1.5650	0.9390	1.2520	0.9390	1.56500	1.87800	1.87800	1.565		
	Mean	0.7303	0.7303	0.5738	0.4695	0.5999	0.4434	0.73033	0.65208	0.599916667			
	Std. Dev	0.3082	0.4687	0.4393	0.3130	0.3882	0.2818	0.44931	0.52474	0.489619418			
Mean - 3 Sigma	-0.1943	-0.6758	-0.7440	-0.4695	-0.5645	-0.4020	-0.61758	-0.92214	-0.868941587				
Mean + 3 Sigma	1.6550	2.1365	1.8917	1.4085	1.7644	1.2888	2.07825	2.22630	2.06877492				
Limit: <6ns				Limit: <6ns				Limit: <6ns					

Wafer #	SN	AB_D Fall Match Opposing 4.5V, 4.5V			ns	ChA Input Leakage @ 0V 4.5V, 4.5V			uA	ChB Input Leakage @ 0V 4.5V, 4.5V			uA
		PRE	100k	168hr Anneal		PRE	100k	168hr Anneal		PRE	100k	168hr Anneal	
CTRL	9	2.1910	1.8780	1.8780	0.0012	0.0003	-0.0011	-0.00067	0.00037	0.00114	-0.0002072		
	68	1.2520	0.9390	0.9390	-0.0021	0.0002	0.0011	0.00002	0.00114	0.00114	-0.0002072		
	11	1.2520	1.5650	1.2520	0.0004	-0.0003	-0.0006	-0.00044	0.00091	-0.0004811			
	12	0.9390	1.5650	1.2520	0.0011	-0.0002	0.0004	-0.00090	0.00029	-0.0004811			
	13	1.2520	1.2520	1.2520	-0.0003	0.0002	0.0004	0.00064	0.00106	0.0005601			
	14	1.5650	1.5650	1.5650	0.0013	-0.0002	-0.0016	0.00002	-0.00078	-0.00125			
	15	1.2520	1.5650	1.2520	0.0004	0.0001	0.0003	-0.00052	0.00052	-0.00117			
	16	1.8780	1.8780	1.5650	-0.0004	-0.0002	-0.0006	-0.00106	-0.00116	0.0005601			
	71	0.3130	0.3130	0.6260	-0.0003	-0.0006	0.0001	-0.00021	0.00037	-0.00094147			
	72	1.5650	1.5650	1.8780	0.0003	-0.0013	0.0004	-0.00175	-0.00070	-0.00055783			
	73	0.9390	0.9390	1.2520	-0.0009	-0.0009	-0.0015	0.00033	-0.00116	-0.00094147			
	74	0.3130	0.3130	0.6260	-0.0002	-0.0002	-0.0013	0.00071	-0.02959	-0.00025091			
	75	0.6260	0.6260	0.6260	0.0004	0.0001	-0.0005	-0.00113	0.00014	-0.00071128			
	76	0.6260	0.9390	0.6260	-0.0005	-0.0005	0.0004	0.00056	-0.00040	-0.00071128			
	Min	0.3130	0.3130	0.6260	-0.0009	-0.0002	-0.0016	-0.00175	-0.02959	-0.00125			
	Max	1.8780	1.8780	1.8780	0.0013	0.0004	0.0004	0.00071	0.00106	0.0005601			
	Mean	1.0433	1.1738	1.1477	0.0001	-0.0068	-0.0003	-0.00031	-0.00254	-0.000615368			
	Std. Dev	0.5053	0.5359	0.4290	0.0006	0.0222	0.0008	0.00079	0.00855	0.000428013			
Mean - 3 Sigma	-0.4725	-0.4341	-0.1394	-0.0018	-0.0735	-0.0027	-0.00270	-0.02820	-0.001899409				
Mean + 3 Sigma	2.5592	2.7816	2.4347	0.0020	0.0599	0.0020	0.00207	0.02311	0.000668672				
Limit: <6ns				Limit: +/-10uA				Limit: +/-10uA					

Wafer #	SN	ChC Input Leakage @ 0V 4.5V, 4.5V			uA	ChD Input Leakage @ 0V 4.5V, 4.5V			uA	ChA Input Leakage @ Vdd1 4.5V, 4.5V			uA
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	-0.0002	0.0010	-0.0003	0.0002	-0.0015	0.0001	0.00376	0.00245	0.00494			
	68	0.0000	-0.0018	-0.0002	0.0000	-0.0001	-0.0004	0.00529	0.00299	0.00487			
	11	-0.0007	0.0001	-0.0002	0.0010	0.0002	0.0001	0.00414	0.00253	0.0038			
	12	-0.0014	-0.0005	-0.0001	-0.0003	0.0001	0.0005	0.00437	0.00429	0.0041			
	13	-0.0016	0.0001	0.0001	0.0007	0.0000	-0.0006	0.00422	0.00544	0.00494			
	14	0.0008	0.0004	-0.0017	-0.0011	-0.0006	-0.0001	0.00445	0.00406	0.00296			
	15	-0.0001	0.0001	0.0011	-0.0005	-0.0016	-0.0002	0.00476	0.00260	0.00403			
	16	0.0004	0.0003	0.0003	-0.0011	0.0002	-0.0006	0.00714	0.00337	0.0041			
	71	0.0009	0.0007	-0.0007	0.0009	-0.0005	-0.0003	0.00268	0.00444	0.00288			
	72	-0.0004	0.0013	-0.0020	0.0004	-0.0002	0.0003	0.00322	0.00360	0.00364			
	73	-0.0007	-0.0002	-0.0013	0.0005	-0.0011	-0.0006	0.00407	0.00222	0.00303			
	74	-0.0017	0.0004	-0.0003	-0.0006	-0.0010	-0.0010	0.00453	0.00383	0.00403			
	75	0.0001	-0.0005	-0.0011	-0.0018	-0.0013	0.0010	0.00453	0.00345	0.00326			
	76	0.0006	0.0004	-0.0008	-0.0013	-0.0006	0.0000	0.00345	0.00421	0.00426			
	Min	-0.0017	-0.0005	-0.0020	-0.0018	-0.0016	-0.0010	0.00268	0.00222	0.00288			
	Max	0.0009	0.0013	0.0011	0.0010	0.0002	0.0010	0.00714	0.00544	0.00494			
	Mean	-0.0003	0.0002	-0.0006	-0.0002	-0.0005	-0.0001	0.00430	0.00367	0.0037525			
	Std. Dev	0.0009	0.0005	0.0009	0.0010	0.0006	0.0006	0.00109	0.00092	0.000620266			
Mean - 3 Sigma	-0.0031	-0.0013	-0.0032	-0.0031	-0.0024	-0.0018	0.00103	0.00092	0.001891703				
Mean + 3 Sigma	0.0024	0.0017	0.0021	0.0027	0.0013	0.0016	0.00756	0.00642	0.005613297				
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA					

Wafer #	SN	ChB Input Leakage @ Vdd1 4.5V, 4.5V			uA	ChC Input Leakage @ Vdd2 4.5V, 4.5V			uA	ChD Input Leakage @ Vdd2 4.5V, 4.5V			uA
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	0.0026	0.0025	0.0031	-0.0001	0.0013	0.0003	0.00121	0.00124	0.00096119			
	68	0.0023	0.0030	0.0025	0.0011	0.0007	0.0007	0.00098	0.00124	0.00073179			
	11	0.0033	0.0031	0.0034	0.0003	0.0017	0.0010	0.00067	0.00093	0.00034945			
	12	0.0049	0.0032	0.0018	0.0018	0.0023	0.0004	0.00067	0.00109	0.00012005			
	13	0.0039	0.0029	0.0020	0.0000	0.0015	0.0016	-0.00002	0.00124	0.00119			
	14	0.0029	0.0036	0.0022	-0.0004	0.0012	0.0008	0.00082	0.00055	0.00142			
	15	0.0038	0.0028	0.0032	0.0020	0.0007	-0.0006	0.00075	0.00193	0.00234			
	16	0.0043	0.0040	0.0030	0.0006	0.0009	0.0012	0.00006	0.00032	0.00042592			
	71	0.0025	0.0027	0.0025	0.0018	0.0004	-0.0001	0.00067	0.00039	-0.00018581			
	72	0.0023	0.0013	0.0017	0.0008	0.0001	0.0014	0.00098	0.00124	0.00034945			
	73	0.0025	0.0021	0.0024	-0.0002	0.0004	-0.0006	0.00121	-0.00037	0.00080826			
	74	0.0036	0.0014	0.0044	0.0004	0.0015	-0.0005	0.00151	0.00124	0.0018			
	75	0.0040	0.0024	0.0009	-0.0005	0.0008	0.0005	-0.00002	0.00101	0.00142			
	76	0.0046	0.0031	0.0035	0.0000	0.0019	0.0010	0.00044	-0.00022	0.00042592			
	Min	0.0023	0.0013	0.0009	-0.0005	0.0001	-0.0006	-0.00002	-0.00037	-0.00018581			
	Max	0.0049	0.0040	0.0044	0.0020	0.0023	0.0016	0.00151	0.00193	0.00234			
	Mean	0.0036	0.0027	0.0026	0.0005	0.0011	0.0005	0.00064	0.00078	0.000871937			
	Std. Dev	0.0009	0.0008	0.0009	0.0009	0.0007	0.0008	0.00048	0.00067	0.000759814			
Mean - 3 Sigma	0.0010	0.0003	-0.0003	-0.0021	-0.0009	-0.0018	-0.00079	-0.00122	-0.001407504				
Mean + 3 Sigma	0.0061	0.0051	0.0054	0.0032	0.0032	0.0028	0.00207	0.00278	0.003151378				
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA					

Wafer #	SN	Voh_A @ -20uA 4.5V, 4.5V			Voh_B @ -20uA 4.5V, 4.5V			Voh_C @ -20uA 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.4971	4.4938	4.4922	4.4967	4.4939	4.4944	4.49600	4.49531	4.49463	
	68	4.4975	4.4965	4.4922	4.4975	4.4964	4.4944	4.49694	4.49327	4.49422	
	11	4.4952	4.4941	4.4926	4.4949	4.4949	4.4947	4.49050	4.49527	4.49418	
	12	4.4978	4.4958	4.4923	4.4978	4.4965	4.4945	4.49718	4.49336	4.49445	
	13	4.4978	4.4940	4.4923	4.4976	4.4944	4.4948	4.49728	4.49366	4.49431	
	14	4.4976	4.4962	4.4921	4.4974	4.4960	4.4946	4.49715	4.49404	4.4944	
	15	4.4975	4.4961	4.4922	4.4977	4.4966	4.4945	4.49743	4.49448	4.4944	
	16	4.4976	4.4958	4.4887	4.4975	4.4959	4.4911	4.49714	4.49176	4.49452	
	71	4.4976	4.4963	4.4912	4.4976	4.4960	4.4937	4.49710	4.49180	4.49417	
	72	4.4977	4.4966	4.4923	4.4976	4.4964	4.4946	4.49691	4.48884	4.49452	
	73	4.4965	4.4960	4.4912	4.4974	4.4966	4.4937	4.49725	4.49420	4.49459	
	74	4.4979	4.4964	4.4920	4.4976	4.4966	4.4944	4.49721	4.49316	4.49441	
	75	4.4958	4.4957	4.4924	4.4958	4.4963	4.4949	4.49701	4.49399	4.49197	
	76	4.4973	4.4959	4.4923	4.4969	4.4960	4.4946	4.49722	4.49373	4.49425	
	Min	4.4952	4.4940	4.4887	4.4949	4.4944	4.4911	4.49050	4.48884	4.49197	
	Max	4.4979	4.4966	4.4926	4.4978	4.4966	4.4949	4.49743	4.49527	4.49459	
	Mean	4.4972	4.4957	4.4918	4.4971	4.4960	4.4942	4.49662	4.49319	4.494180833	
	Std. Dev	0.0009	0.0008	0.0011	0.0009	0.0007	0.0011	0.00193	0.00170	0.000708897	
	Mean - 3 Sigma	4.4946	4.4932	4.4885	4.4945	4.4939	4.4910	4.49082	4.48809	4.492054141	
Mean + 3 Sigma	4.4998	4.4983	4.4950	4.4998	4.4981	4.4973	4.50241	4.49829	4.496307526		
Limit: >4.4V				Limit: >4.4V				Limit: >4.4V			

Wafer #	SN	Voh_D @ -20uA 4.5V, 4.5V			Voh_A @ -4mA 4.5V, 4.5V			Voh_B @ -4mA 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.4960	4.4949	4.4945	4.2730	4.2649	4.2706	4.25463	4.25956	4.26256	
	68	4.4971	4.4926	4.4942	4.2747	4.2711	4.2678	4.26580	4.22567	4.23207	
	11	4.4902	4.4946	4.4942	4.2538	4.2615	4.2655	4.23399	4.25503	4.24541	
	12	4.4970	4.4926	4.4940	4.2580	4.2691	4.2620	4.24985	4.26274	4.26040	
	13	4.4971	4.4931	4.4942	4.2769	4.2587	4.2651	4.27186	4.24597	4.24454	
	14	4.4969	4.4934	4.4940	4.2759	4.2696	4.2638	4.24963	4.23851	4.24532	
	15	4.4969	4.4936	4.4941	4.2765	4.2707	4.2650	4.26767	4.24422	4.24235	
	16	4.4969	4.4909	4.4942	4.2763	4.2699	4.2489	4.26974	4.25309	4.22383	
	71	4.4969	4.4909	4.4941	4.2747	4.2659	4.2569	4.26745	4.23726	4.25419	
	72	4.4969	4.4874	4.4942	4.2750	4.2683	4.2638	4.25767	4.23682	4.23913	
	73	4.4971	4.4933	4.4942	4.2768	4.2714	4.2588	4.24950	4.24097	4.23704	
	74	4.4969	4.4928	4.4942	4.2767	4.2684	4.2616	4.24797	4.26190	4.24938	
	75	4.4969	4.4926	4.4917	4.2690	4.2677	4.2631	4.24575	4.23791	4.24853	
	76	4.4970	4.4927	4.4943	4.2731	4.2665	4.2626	4.25882	4.24310	4.23910	
	Min	4.4902	4.4874	4.4917	4.2538	4.2587	4.2489	4.23399	4.23682	4.22383	
	Max	4.4971	4.4946	4.4943	4.2769	4.2714	4.2655	4.27186	4.26274	4.26040	
	Mean	4.4964	4.4923	4.4939	4.2719	4.2673	4.2614	4.25583	4.24646	4.24410	
	Std. Dev	0.0019	0.0019	0.0007	0.0078	0.0038	0.0047	0.01165	0.00945	0.00920	
	Mean - 3 Sigma	4.4905	4.4867	4.4918	4.2484	4.2560	4.2473	4.22088	4.21812	4.21649	
Mean + 3 Sigma	4.5022	4.4979	4.4961	4.2954	4.2786	4.2755	4.29077	4.27480	4.27171		
Limit: >4.4V				Limit: >4.1V				Limit: >4.1V			

Wafer #	SN	Voh_C @ -4mA 4.5V, 4.5V			Voh_D @ -4mA 4.5V, 4.5V			Voh_CHA @ 20uA 4.5V, 4.5V			mV
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.2694	4.2739	4.2718	4.2655	4.2496	4.2665	6.61553	1.09593	0.51440	
	68	4.2727	4.2661	4.2569	4.2695	4.2543	4.2656	1.33180	1.33314	0.60183	
	11	4.2281	4.2643	4.2434	4.2380	4.2519	4.2611	2.67618	1.63277	0.82666	
	12	4.2736	4.2648	4.2654	4.2663	4.2532	4.2626	0.92536	1.87622	0.67053	
	13	4.2730	4.2618	4.2649	4.2690	4.2566	4.2614	1.00040	1.54537	0.91409	
	14	4.2685	4.2642	4.2651	4.2696	4.2555	4.2621	0.95037	1.05223	1.86000	
	15	4.2693	4.2646	4.2659	4.2695	4.2576	4.2618	0.88159	0.95860	0.49566	
	16	4.2718	4.2573	4.2653	4.2623	4.2520	4.2621	1.11295	0.80878	0.40199	
	71	4.2720	4.2515	4.2620	4.2691	4.2477	4.2590	1.05667	1.36435	0.40199	
	72	4.2703	4.2402	4.2646	4.2674	4.2357	4.2624	0.91286	1.15211	0.10846	
	73	4.2745	4.2654	4.2653	4.2705	4.2607	4.2620	0.88785	1.20205	0.38325	
	74	4.2708	4.2604	4.2634	4.2668	4.2547	4.2552	2.05089	1.11465	0.43321	
	75	4.2713	4.2610	4.2496	4.2684	4.2520	4.2421	1.04417	1.22077	0.42072	
	76	4.2719	4.2628	4.2631	4.2674	4.2562	4.2557	1.35056	1.96985	0.40199	
	Min	4.2281	4.2402	4.2434	4.2380	4.2357	4.2421	0.88159	0.80878	0.10846	
	Max	4.2745	4.2654	4.2659	4.2705	4.2607	4.2626	2.67618	1.96985	1.86000	
	Mean	4.2679	4.2599	4.2615	4.2654	4.2528	4.2589	1.23749	1.32481	0.60988	
	Std. Dev	0.0126	0.0073	0.0072	0.0089	0.0063	0.0059	0.55761	0.36178	0.44909	
	Mean - 3 Sigma	4.2300	4.2379	4.2399	4.2387	4.2338	4.2413	-0.43533	0.23946	-0.73739	
Mean + 3 Sigma	4.3058	4.2819	4.2831	4.2920	4.2718	4.2766	2.91031	2.41016	1.95715		
Limit: >4.1V				Limit: >4.1V				Limit: <100mV			

Wafer #	SN	Vol_CHB @ 20uA 4.5V, 4.5V			Vol_CHC @ 20uA 4.5V, 4.5V			Vol_CHD @ 20uA 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	6.7256	0.9790	1.0000	2.2568	1.9777	1.9800	2.21855	1.75608	2.05000	
	68	1.5010	1.5098	1.4400	1.8320	2.3210	1.5800	1.79355	2.04941	1.39000	
	11	2.9321	1.8658	1.4200	1.9257	1.9402	1.8600	1.69977	1.60630	1.73000	
	12	1.1260	1.8221	1.3200	2.9128	2.2211	1.5300	2.86860	1.91211	1.48000	
	13	0.9822	1.6285	1.5200	2.5942	2.1400	1.4700	2.47482	1.72488	1.32000	
	14	1.2072	1.2725	2.5600	2.6379	1.9652	1.6000	2.49982	1.73736	1.27000	
	15	1.0385	1.3349	0.9847	2.3755	2.0526	1.5100	2.30606	1.67495	1.34000	
	16	1.2635	1.1476	1.1500	2.0194	1.9090	1.4900	1.91853	1.83722	1.30000	
	71	1.2947	1.3787	0.9597	1.8133	2.0900	1.4900	1.71852	1.71240	1.37000	
	72	1.0510	1.2288	0.8349	1.7570	2.0089	1.3500	1.75602	1.60006	1.11000	
	73	1.1822	1.3974	1.1700	1.7508	2.0463	1.2900	1.71852	1.64999	1.31000	
	74	2.2259	1.2975	0.9659	2.1194	2.3147	1.3400	1.92478	1.94956	1.31000	
	75	1.3322	1.2725	1.0500	1.7883	3.3510	1.3700	1.59351	3.11039	1.28000	
	76	1.3197	1.9532	0.9535	2.1131	2.4333	1.5000	2.11230	1.96828	1.44000	
	Min	0.9822	1.1476	0.8349	1.7508	1.9090	1.2900	1.59351	1.60006	1.11000	
	Max	2.9321	1.9532	2.5600	2.9128	3.3510	1.8600	2.86860	3.11039	1.73000	
	Mean	1.4129	1.4666	1.2407	2.1506	2.2060	1.4833	2.04927	1.87363	1.35500	
	Std. Dev	0.5764	0.2765	0.4645	0.3926	0.3928	0.1500	0.40368	0.41005	0.14927	
	Mean - 3 Sigma	-0.3163	0.6372	-0.1529	0.9727	1.0276	1.0334	0.83823	0.64346	0.90719	
Mean + 3 Sigma	3.1422	2.2961	2.6343	3.3285	3.3844	1.9333	3.26032	3.10379	1.80281		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHA @ 400uA 4.5V, 4.5V			Vol_CHB @ 400uA 4.5V, 4.5V			Vol_CHC @ 400uA 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	24.5177	17.9440	17.3000	26.3493	18.5845	18.8700	20.10576	19.69332	19.90000	
	68	18.4398	18.6056	17.7800	19.3497	22.6378	22.8300	19.76215	20.43616	20.87000	
	11	20.9660	18.3934	17.5600	23.2370	19.3527	20.5200	20.86795	19.43115	21.59000	
	12	19.9655	18.6056	17.3100	20.6809	19.5463	18.6700	20.93042	19.68084	19.33000	
	13	17.7520	18.2686	17.5900	18.4873	19.7462	20.6700	20.54308	19.73702	19.22000	
	14	17.8645	17.9003	18.7600	20.7121	21.0514	21.5500	20.86170	19.34375	19.16000	
	15	17.8395	17.8191	17.0600	18.9310	20.4457	20.1500	20.59931	19.44363	19.08000	
	16	18.1209	17.4446	17.3000	18.9623	19.2653	20.6100	20.01829	19.46236	18.82000	
	71	18.2647	18.2686	17.2500	19.1560	21.2451	18.6100	19.59972	19.77447	19.21000	
	72	18.0521	17.8628	16.9600	19.8810	22.9500	20.3000	19.83087	19.38121	19.06000	
	73	17.5894	17.6755	17.1800	20.5934	20.9578	20.2000	19.44353	19.33751	19.04000	
	74	19.1151	17.7817	17.1200	21.8308	18.6158	19.3200	20.09326	19.96798	19.08000	
	75	18.0834	17.8191	17.1500	20.4122	20.9141	19.3700	19.74965	21.31008	19.24000	
	76	18.2960	18.5994	17.2300	19.8435	21.1514	20.4800	20.14324	19.94302	19.26000	
	Min	17.5894	17.4446	16.9600	18.4873	18.6158	18.6100	19.44353	19.33751	18.82000	
	Max	20.9660	18.6056	18.7600	23.2370	22.9500	21.5500	20.93042	21.31008	21.59000	
	Mean	18.4924	18.0365	17.3725	20.2273	20.4368	20.0375	20.22342	19.73442	19.34083	
	Std. Dev	1.0203	0.3772	0.4739	1.3471	1.1867	0.8749	0.52287	0.54529	0.72090	
	Mean - 3 Sigma	15.4317	16.9051	15.9508	16.1860	16.8767	17.4128	18.65479	18.09856	17.17813	
Mean + 3 Sigma	21.5532	19.1680	18.7942	24.2686	23.9969	22.6622	21.79204	21.37028	21.50354		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHD @ 400uA 4.5V, 4.5V			Vol_CHA @ 4mA 4.5V, 4.5V			Vol_CHB @ 4mA 4.5V, 4.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	20.3010	21.6900	20.2100	195.0850	178.9459	178.6500	214.73008	185.62214	189.59000	
	68	20.0260	21.2843	19.7800	180.7970	182.7100	181.2600	190.90047	225.23627	236.81000	
	11	19.8322	20.1048	19.6600	195.0350	177.7473	177.8000	219.19852	186.42779	202.80000	
	12	21.4886	20.5167	19.4500	199.8622	178.2343	177.9700	207.63057	187.42080	185.76000	
	13	20.9011	19.8551	19.2500	178.5960	178.5526	177.7200	185.26961	193.11029	203.90000	
	14	20.7761	19.8052	19.4800	178.6398	176.7174	180.0500	206.93687	209.75409	204.36000	
	15	20.5760	19.8988	19.5200	178.8649	176.0307	176.2400	188.80062	203.77731	204.48000	
	16	20.8323	19.7053	19.2700	179.6778	175.3753	176.8700	187.51945	193.72234	206.26000	
	71	19.8510	19.8427	19.6600	180.0405	179.4577	178.0800	188.97560	210.10384	185.70000	
	72	20.0323	19.5868	19.1600	180.8471	177.5538	176.7400	199.57487	231.72517	205.69000	
	73	19.7385	19.4994	19.2500	177.8394	175.5126	175.9400	206.78687	206.88750	203.41000	
	74	20.3698	20.3607	19.9100	181.7162	176.5551	177.2300	208.56801	184.97263	194.34000	
	75	19.8447	21.7899	20.0100	178.8837	176.0682	176.1000	203.54959	207.66816	195.59000	
	76	20.4260	20.3357	19.9200	180.1718	179.1706	176.6900	196.00011	204.47679	204.56000	
	Min	19.7385	19.4994	19.1600	177.8394	175.3753	175.9400	185.26961	184.97263	185.70000	
	Max	21.4886	21.7899	20.0100	199.8622	179.4577	180.0500	219.19852	231.72517	206.26000	
	Mean	20.3890	20.1084	19.5450	182.5145	177.2480	177.2858	199.90089	201.67056	199.73750	
	Std. Dev	0.5478	0.6168	0.2905	7.1319	1.4062	1.1415	10.62918	13.36860	7.53199	
	Mean - 3 Sigma	18.7458	18.2579	18.6735	161.1188	173.0294	173.8614	168.01334	161.56475	177.14153	
Mean + 3 Sigma	22.0323	21.9589	20.4165	203.9103	181.4665	180.7102	231.78844	241.77637	222.33347		
Limit: <100mV				Limit: <400mV				Limit: <400mV			

Wafer #	SN	Vol. CHC @ 4mA 4.5V, 4.5V			Vol. CHD @ 4mA 4.5V, 4.5V			Idd1 Quiescent 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	189.2427	188.0047	188.8200	194.0565	211.5427	194.3200	2.86947	2.85075	2.87964	
	68	189.6050	192.2432	202.9900	193.6189	203.6041	194.1300	2.82918	2.71484	2.82104	
	11	201.7376	185.0958	208.8800	192.8501	196.1398	192.1300	2.82552	2.78035	2.86377	
	12	191.9166	185.5015	187.6500	199.7631	196.9262	190.8700	2.81576	2.77873	2.84342	
	13	190.7671	186.5440	187.1700	195.4003	191.3093	191.0500	2.81209	2.78076	2.80192	
	14	194.8154	184.3592	186.0200	194.3815	193.4312	190.3600	2.84465	2.80477	2.88981	
	15	193.7284	186.2069	186.4600	194.6002	192.1893	191.3300	2.84465	2.76042	2.83162	
	16	190.7921	184.7337	186.3200	201.0132	190.0174	191.0500	2.83244	2.79419	2.85848	
	71	189.3864	188.6102	189.0500	193.2064	192.4139	193.2800	2.81169	2.75391	2.82918	
	72	190.6734	186.4254	186.8000	194.2002	190.3481	190.4500	2.83936	2.80518	2.87882	
	73	188.0744	184.6214	186.5400	192.6189	188.9938	190.5600	2.85116	2.83081	2.84993	
	74	191.5855	188.6227	188.4500	195.7066	194.4922	197.0400	2.84465	2.78687	2.85197	
	75	189.5051	190.4330	189.3100	193.3689	199.6972	198.0500	2.86987	2.79948	2.86173	
	76	191.0919	187.6801	188.1400	195.6066	194.3112	197.0600	2.83203	2.77507	2.83895	
	Min	188.0744	184.3592	186.0200	192.6189	188.9938	190.3600	2.81169	2.75391	2.80192	
	Max	201.7376	190.4330	208.8800	201.0132	199.6972	198.0500	2.86987	2.83081	2.88981	
	Mean	192.0061	186.5695	189.2325	195.2263	193.3558	192.7692	2.83532	2.78755	2.84997	
	Std. Dev	3.5668	1.9083	6.2833	2.6390	3.1418	2.9055	0.01737	0.02103	0.02348	
	Mean - 3 Sigma	181.3058	180.8447	170.3825	187.3092	183.9306	184.0527	2.78322	2.72446	2.77951	
Mean + 3 Sigma	202.7065	192.2942	208.0825	203.1434	202.7811	201.4856	2.88742	2.85063	2.92042		
Limit: <400mV				Limit: <400mV				Limit: <3.8mA			

Wafer #	SN	Idd2 Quiescent 5.5V, 5.5V			Idd1 Dynamic 2MBPS 5.5V, 5.5V			Idd2 Dynamic 2MBPS 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.8788	2.8825	2.8402	3.7217	3.7114	3.6969	3.68943	3.67743	3.67906	
	68	2.8674	2.8739	2.8683	3.7329	3.5953	3.6876	3.74880	3.68447	3.69399	
	11	2.8662	2.8809	2.8548	3.7396	3.6329	3.7298	3.69773	3.68471	3.68304	
	12	2.8369	2.8813	2.8333	3.6857	3.5803	3.6850	3.70217	3.65586	3.66201	
	13	2.8276	2.8683	2.8239	3.7119	3.6003	3.6852	3.73110	3.67849	3.67556	
	14	2.8219	2.8906	2.8390	3.7174	3.5986	3.6983	3.71250	3.68007	3.66624	
	15	2.8483	2.8959	2.8316	3.7355	3.6080	3.7092	3.74807	3.70030	3.69737	
	16	2.8463	2.9167	2.8678	3.7365	3.6063	3.6909	3.73887	3.68984	3.68622	
	71	2.8890	2.8980	2.8837	3.7294	3.5878	3.6764	3.75425	3.70579	3.69639	
	72	2.8508	2.8556	2.8170	3.7411	3.6146	3.7064	3.71686	3.67617	3.66136	
	73	2.8845	2.9024	2.9012	3.7508	3.6271	3.7240	3.77842	3.73435	3.71100	
	74	2.8715	2.9268	2.9069	3.7499	3.6051	3.7165	3.73907	3.71503	3.70176	
	75	2.9004	2.9419	2.8971	3.7529	3.6028	3.7029	3.76011	3.70624	3.69859	
	76	2.8886	2.9110	2.8589	3.7415	3.6116	3.6997	3.73855	3.70787	3.70363	
	Min	2.8219	2.8556	2.8170	3.6857	3.5803	3.6764	3.69773	3.65586	3.66136	
	Max	2.9004	2.9419	2.9069	3.7529	3.6329	3.7298	3.77842	3.73435	3.71100	
	Mean	2.8610	2.8974	2.8596	3.7327	3.6063	3.7020	3.73481	3.69456	3.68693	
	Std. Dev	0.0261	0.0245	0.0317	0.0194	0.0147	0.0163	0.02416	0.02110	0.01722	
	Mean - 3 Sigma	2.7826	2.8239	2.7645	3.6745	3.5623	3.6533	3.66234	3.63125	3.63528	
Mean + 3 Sigma	2.9393	2.9709	2.9547	3.7908	3.6503	3.7508	3.80727	3.75787	3.73858		
Limit: <4.3mA				Limit: <20mA				Limit: <20mA			

Wafer #	SN	Idd1 Dynamic 10MBPS 5.5V, 5.5V			Idd2 Dynamic 10MBPS 5.5V, 5.5V			Idd1 Dynamic 25MBPS 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	8.7306	8.7158	8.6229	8.3215	8.3596	8.4012	17.68685	17.67664	17.42806	
	68	8.9690	8.7348	8.7929	8.5975	8.4537	8.4761	18.34432	17.89591	17.93616	
	11	9.0151	8.6764	8.8276	8.4377	8.3287	8.3914	18.35946	17.64514	17.96183	
	12	8.7150	8.3777	8.6109	8.5957	8.2947	8.4052	17.67057	16.96659	17.43245	
	13	8.9439	8.5998	8.7364	8.6783	8.3710	8.4220	18.25452	17.47579	17.76803	
	14	8.7549	8.4766	8.6181	8.5512	8.3663	8.3724	17.79451	17.17419	17.44641	
	15	8.9020	8.5750	8.7514	8.6884	8.4115	8.4575	18.23075	17.48999	17.81352	
	16	8.8861	8.4960	8.6660	8.6331	8.3145	8.3896	18.08557	17.21330	17.56340	
	71	8.9526	8.5271	8.6735	8.6243	8.3383	8.3487	18.31718	17.36230	17.65539	
	72	8.9209	8.5138	8.6650	8.6254	8.3638	8.3163	18.19552	17.28097	17.59383	
	73	8.9282	8.5182	8.6697	8.7001	8.4384	8.4148	18.17403	17.24609	17.54891	
	74	9.0373	8.5691	8.8081	8.5555	8.3649	8.3491	18.52360	17.53463	17.95093	
	75	9.0003	8.4991	8.7564	8.5546	8.2920	8.3393	18.40450	17.26969	17.77657	
	76	9.0584	8.6757	8.8188	8.5743	8.3629	8.4367	18.52132	17.69588	17.94963	
	Min	8.7150	8.3777	8.6109	8.4377	8.2920	8.3163	17.67057	16.96659	17.43245	
	Max	9.0584	8.6764	8.8276	8.7001	8.4384	8.4575	18.52360	17.69588	17.96183	
	Mean	8.9262	8.5420	8.7168	8.6015	8.3539	8.3869	18.21096	17.36288	17.70508	
	Std. Dev	0.1046	0.0842	0.0766	0.0737	0.0437	0.0428	0.26110	0.21171	0.19248	
	Mean - 3 Sigma	8.6123	8.2895	8.4871	8.3805	8.2229	8.2584	17.42765	16.72776	17.12763	
Mean + 3 Sigma	9.2401	8.7946	8.9465	8.8226	8.4849	8.5154	18.99427	17.99800	18.28252		
Limit: <20mA				Limit: <20mA				Limit: <20mA			

Wafer #	SN	Idd2 Dynamic 25MBPS 5.5V, 5.5V			CHA Prop Delay Rise 5.5V, 5.5V			CHB Prop Delay Rise 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	16.8956	16.9618	17.1119	37.5600	37.5600	37.5600	37.56000	37.56000	37.56000	
	68	17.6299	17.2768	17.3696	38.4990	38.4990	38.4990	38.18600	38.49900	38.49900	
	11	17.1969	16.8793	17.1374	37.8730	37.5600	37.8730	38.18600	37.56000	38.18600	
	12	17.5902	16.7923	17.1576	38.4990	37.5600	38.1860	38.18600	37.24700	37.56000	
	13	17.8403	17.0088	17.2286	38.4990	37.5600	38.1860	38.18600	37.56000	37.87300	
	14	17.4542	16.9750	17.0652	37.5600	37.2470	37.2470	37.56000	36.93400	37.24700	
	15	17.8271	17.0548	17.2554	38.4990	37.8730	38.1860	37.87300	37.24700	37.56000	
	16	17.7319	16.8506	17.0846	37.8730	37.2470	37.2470	37.56000	37.24700	37.56000	
	71	17.6517	16.8625	16.9894	39.1250	38.1860	38.4990	37.87300	37.56000	37.56000	
	72	17.7272	16.9726	16.9593	38.8120	37.5600	37.8730	37.56000	36.93400	37.24700	
	73	17.8387	17.1169	17.1110	37.8730	37.2470	37.5600	37.56000	36.93400	37.24700	
	74	17.4517	16.9576	17.0085	39.1250	38.1860	38.4990	38.81200	38.18600	38.49900	
	75	17.4542	16.7597	16.9579	38.4990	37.8730	38.1860	38.18600	37.56000	37.87300	
	76	17.5467	16.9771	17.2444	38.8120	38.1860	38.4990	38.18600	37.56000	38.18600	
	Min	17.1969	16.7597	16.9579	37.5600	37.2470	37.2470	37.56000	36.93400	37.24700	
	Max	17.8403	17.1169	17.2554	39.1250	38.1860	38.4990	38.81200	38.18600	38.49900	
	Mean	17.6092	16.9339	17.0999	38.4208	37.6904	38.0295	38.00342	37.37742	37.71650	
	Std. Dev	0.1980	0.1065	0.1080	0.5191	0.3645	0.4114	0.36449	0.36449	0.41136	
	Mean - 3 Sigma	17.0151	16.6145	16.7758	36.8636	36.5970	36.7954	36.90995	36.28395	36.48241	
Mean + 3 Sigma	18.2033	17.2534	17.4241	39.9779	38.7839	39.2636	39.09688	38.47088	38.95059		
Limit: <20mA				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHC Prop Delay Rise 5.5V, 5.5V			CHD Prop Delay Rise 5.5V, 5.5V			CHA Prop Delay Fall 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	37.5600	37.5600	37.5600	37.8730	37.8730	37.8730	37.87300	38.18600	38.18600	
	68	38.1860	38.1860	38.1860	37.8730	38.1860	38.1860	39.12500	39.43800	39.43800	
	11	38.4990	37.5600	37.8730	38.4990	37.5600	37.8730	38.81200	38.18600	38.49900	
	12	38.4990	37.8730	37.8730	38.1860	37.5600	37.5600	39.12500	38.18600	38.49900	
	13	38.1860	37.5600	38.1860	38.1860	37.5600	38.1860	39.43800	38.49900	38.81200	
	14	37.8730	37.2470	37.5600	37.5600	37.2470	37.5600	38.49900	37.87300	38.18600	
	15	37.8730	37.5600	37.8730	37.5600	37.8730	37.5600	39.43800	38.49900	38.81200	
	16	37.2470	36.9340	37.2470	37.8730	37.2470	37.5600	38.81200	37.56000	38.18600	
	71	37.8730	37.5600	37.5600	37.5600	37.2470	37.5600	39.75100	38.81200	39.12500	
	72	38.1860	37.5600	37.5600	38.1860	37.5600	37.5600	39.43800	38.49900	38.49900	
	73	37.5600	36.9340	37.2470	37.5600	36.9340	37.2470	38.49900	37.56000	37.87300	
	74	37.8730	36.6210	37.5600	37.5600	36.6210	37.2470	39.43800	38.81200	39.12500	
	75	37.5600	36.9340	37.2470	37.5600	36.9340	37.2470	39.12500	38.18600	38.81200	
	76	38.1860	37.5600	37.8730	37.8730	37.2470	37.5600	39.12500	38.49900	38.81200	
	Min	37.2470	36.6210	37.2470	37.5600	36.6210	37.2470	38.49900	37.56000	37.87300	
	Max	38.4990	37.8730	38.1860	38.4990	37.8730	38.1860	39.75100	38.81200	39.12500	
	Mean	37.9513	37.3253	37.6383	37.8730	37.2992	37.5600	39.12500	38.26425	38.60333	
	Std. Dev	0.3804	0.3804	0.3021	0.3269	0.3489	0.2669	0.40039	0.42468	0.38528	
	Mean - 3 Sigma	36.8100	36.1840	36.7318	36.8922	36.2525	36.7592	37.92383	36.99021	37.44750	
Mean + 3 Sigma	39.0925	38.4665	38.5447	38.8538	38.3458	38.3608	40.32617	39.53829	39.75916		
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHB Prop Delay Fall 5.5V, 5.5V			CHC Prop Delay Fall 5.5V, 5.5V			CHD Prop Delay Fall 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	38.1860	38.1860	38.4990	38.8120	38.4990	38.4990	40.06400	40.06400	40.06400	
	68	38.8120	39.1250	39.1250	38.8120	38.8120	39.1250	40.37700	40.37700	40.37700	
	11	39.1250	38.4990	39.1250	39.1250	38.4990	38.8120	40.37700	39.75100	40.06400	
	12	39.1250	37.8730	38.4990	39.1250	38.4990	38.8120	40.37700	39.75100	39.75100	
	13	39.1250	38.1860	38.8120	39.4380	38.8120	39.1250	40.69000	40.06400	40.37700	
	14	38.4990	37.8730	38.1860	39.1250	38.4990	38.8120	40.06400	39.43800	39.75100	
	15	38.8120	37.8730	38.1860	39.1250	38.4990	38.8120	40.06400	39.43800	39.75100	
	16	38.4990	37.8730	38.1860	38.4990	37.8730	38.1860	40.37700	39.43800	40.06400	
	71	38.8120	38.1860	38.4990	38.8120	38.1860	38.4990	39.75100	39.12500	39.12500	
	72	38.8120	37.8730	38.1860	39.1250	38.4990	38.4990	40.37700	39.75100	40.06400	
	73	38.4990	37.8730	38.1860	38.8120	37.8730	38.4990	39.75100	39.12500	39.43800	
	74	39.4380	38.4990	38.8120	38.4990	37.5600	38.1860	40.06400	38.81200	39.75100	
	75	38.8120	38.1860	38.4990	38.4990	37.8730	38.1860	39.75100	38.81200	39.12500	
	76	39.1250	38.4990	38.8120	39.1250	38.4990	38.8120	40.06400	39.43800	39.75100	
	Min	38.4990	37.8730	38.1860	38.4990	37.5600	38.1860	39.75100	38.81200	39.12500	
	Max	39.4380	38.4990	39.1250	39.4380	38.8120	39.1250	40.69000	40.06400	40.37700	
	Mean	38.8903	38.1078	38.4990	38.9424	38.2643	38.6033	40.14225	39.41192	39.75100	
	Std. Dev	0.3021	0.2711	0.3269	0.3118	0.3804	0.3082	0.30214	0.38816	0.37749	
	Mean - 3 Sigma	37.9838	37.2946	37.5182	38.0070	37.1230	37.6787	39.23583	38.24745	38.61852	
Mean + 3 Sigma	39.7967	38.9209	39.4798	39.8779	39.4055	39.5280	41.04867	40.57638	40.88348		
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHA PWD 5.5V, 5.5V			CHB PWD 5.5V, 5.5V			CHC PWD 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.3130	0.6260	0.6260	0.6260	0.6260	0.9390	1.25200	0.93900	0.93900	
	68	0.6260	0.9390	0.9390	0.6260	0.6260	0.6260	0.62600	0.62600	0.93900	
	11	0.9390	0.6260	0.6260	0.9390	0.9390	0.9390	0.62600	0.93900	0.93900	
	12	0.6260	0.6260	0.3130	0.9390	0.6260	0.9390	0.62600	0.62600	0.93900	
	13	0.9390	0.9390	0.6260	0.6260	0.9390	0.6260	1.25200	1.25200	0.93900	
	14	0.9390	0.6260	0.9390	0.9390	0.9390	0.9390	1.25200	1.25200	1.25200	
	15	0.9390	0.6260	0.6260	0.9390	0.6260	0.6260	1.25200	0.93900	0.93900	
	16	0.9390	0.3130	0.6260	0.6260	0.6260	0.6260	1.25200	0.93900	0.93900	
	71	0.6260	0.6260	0.6260	0.9390	0.6260	0.9390	0.93900	0.62600	0.93900	
	72	0.6260	0.9390	0.6260	1.2520	0.9390	0.9390	0.93900	0.93900	0.93900	
	73	0.6260	0.3130	0.3130	0.9390	0.9390	0.9390	1.25200	0.93900	1.25200	
	74	0.3130	0.6260	0.6260	0.6260	0.3130	0.3130	0.62600	0.93900	0.62600	
	75	0.6260	0.3130	0.6260	0.6260	0.6260	0.6260	0.93900	0.93900	0.93900	
	76	0.3130	0.3130	0.3130	0.9390	0.9390	0.6260	0.93900	0.93900	0.93900	
	Min	0.3130	0.3130	0.3130	0.6260	0.3130	0.3130	0.62600	0.62600	0.62600	
	Max	0.9390	0.9390	0.9390	1.2520	0.9390	0.9390	1.25200	1.25200	1.25200	
	Mean	0.7043	0.5738	0.5738	0.8868	0.7303	0.7825	0.99117	0.93900	0.96508	
	Std. Dev	0.2359	0.2247	0.1807	0.1807	0.2039	0.2110	0.26131	0.18875	0.16117	
Mean - 3 Sigma	-0.0035	-0.1001	0.0317	0.3447	0.1187	0.1494	0.20725	0.37276	0.48157		
Mean + 3 Sigma	1.4120	1.2478	1.1160	1.4290	1.3419	1.4156	1.77509	1.50524	1.44860		
Limit: <5ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	CHD PWD 5.5V, 5.5V			AB_Rise Match Codirection 5.5V, 5.5V			AB_Fall Match Codirection 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.1910	2.1910	2.1910	0.0000	0.0000	0.0000	0.31300	0.00000	0.31300	
	68	2.5040	2.1910	2.1910	0.3130	0.0000	0.0000	0.31300	0.31300	0.31300	
	11	1.8780	2.1910	2.1910	0.3130	0.0000	0.3130	0.31300	0.31300	0.62600	
	12	2.1910	1.8780	2.1910	0.3130	0.3130	0.6260	0.00000	0.31300	0.00000	
	13	2.5040	2.5040	2.1910	0.3130	0.0000	0.3130	0.31300	0.31300	0.00000	
	14	2.5040	2.1910	2.1910	0.0000	0.3130	0.0000	0.00000	0.00000	0.00000	
	15	2.1910	1.8780	2.1910	0.6260	0.6260	0.6260	0.62600	0.62600	0.62600	
	16	2.5040	2.1910	2.5040	0.0000	0.0000	0.0000	0.31300	0.31300	0.00000	
	71	2.1910	1.8780	1.5650	1.2520	0.6260	0.9390	0.93900	0.62600	0.62600	
	72	2.1910	2.1910	2.5040	1.2520	0.6260	0.6260	0.62600	0.62600	0.31300	
	73	2.1910	2.1910	2.1910	0.3130	0.3130	0.3130	0.00000	0.31300	0.31300	
	74	2.5040	2.1910	2.5040	0.3130	0.0000	0.0000	0.00000	0.31300	0.31300	
	75	2.1910	1.8780	1.8780	0.3130	0.3130	0.3130	0.31300	0.00000	0.31300	
	76	2.1910	2.1910	2.1910	0.6260	0.6260	0.3130	0.00000	0.00000	0.00000	
	Min	1.8780	1.8780	1.5650	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000	
	Max	2.5040	2.5040	2.5040	1.2520	0.6260	0.9390	0.93900	0.62600	0.62600	
	Mean	2.2693	2.1128	2.1910	0.4695	0.3130	0.3652	0.28692	0.31300	0.26083	
	Std. Dev	0.1946	0.1946	0.2669	0.4114	0.2669	0.2934	0.31181	0.23117	0.26131	
Mean - 3 Sigma	1.6856	1.5291	1.3902	-0.7646	-0.4878	-0.5151	-0.64852	-0.38050	-0.52309		
Mean + 3 Sigma	2.8529	2.6964	2.9918	1.7036	1.1138	1.2454	1.22235	1.00650	1.04475		
Limit: <5ns				Limit: <4ns				Limit: <4ns			

Wafer #	SN	CD_Rise Match Codirection 5.5V, 5.5V			CD_Fall Match Codirection 5.5V, 5.5V			AB_C Rise Match Opposing 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.3130	0.3130	0.3130	1.2520	1.5650	1.5650	0.00000	0.00000	0.00000	
	68	0.3130	0.0000	0.0000	1.5650	1.5650	1.2520	0.31300	0.31300	0.31300	
	11	0.0000	0.0000	0.0000	1.2520	1.2520	1.2520	0.62600	0.00000	0.31300	
	12	0.3130	0.0000	0.3130	1.2520	1.2520	0.9390	0.31300	0.62600	0.31300	
	13	0.0000	0.0000	0.0000	1.2520	1.2520	1.2520	0.31300	0.00000	0.31300	
	14	0.3130	0.0000	0.0000	0.9390	0.9390	0.9390	0.31300	0.31300	0.31300	
	15	0.0000	0.0000	0.3130	0.9390	0.9390	0.9390	0.62600	0.31300	0.31300	
	16	0.6260	0.3130	0.3130	1.8780	1.5650	1.8780	0.62600	0.31300	0.31300	
	71	0.3130	0.3130	0.0000	0.9390	0.9390	0.6260	1.25200	0.62600	0.93900	
	72	0.0000	0.0000	0.0000	1.2520	1.2520	1.5650	0.62600	0.62600	0.31300	
	73	0.0000	0.0000	0.0000	0.9390	1.2520	0.9390	0.31300	0.31300	0.31300	
	74	0.3130	0.0000	0.3130	1.5650	1.2520	1.5650	1.25200	1.56500	0.93900	
	75	0.0000	0.0000	0.0000	1.2520	0.9390	0.9390	0.93900	0.93900	0.93900	
	76	0.3130	0.3130	0.3130	0.9390	0.9390	0.9390	0.62600	0.62600	0.62600	
	Min	0.0000	0.0000	0.0000	0.9390	0.9390	0.6260	0.31300	0.00000	0.31300	
	Max	0.6260	0.3130	0.3130	1.8780	1.5650	1.8780	1.25200	1.56500	0.93900	
	Mean	0.1826	0.0783	0.1304	1.1998	1.1477	1.1477	0.65208	0.52167	0.49558	
	Std. Dev	0.2093	0.1416	0.1612	0.2934	0.2039	0.3614	0.33917	0.42903	0.28181	
Mean - 3 Sigma	-0.4452	-0.3464	-0.3531	0.3196	0.5361	0.0634	-0.36544	-0.76541	-0.34983		
Mean + 3 Sigma	0.8104	0.5029	0.6139	2.0801	1.7593	2.2319	1.66961	1.80874	1.34100		
Limit: <4ns				Limit: <4ns				Limit: <4ns			

Wafer #	SN	AB_C Fall Match Opposing 5.5V, 5.5V			ns			AB_D Rise Match Opposing 5.5V, 5.5V			ns			AB_D Fall Match Opposing 5.5V, 5.5V			ns		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal			
CTRL	9	0.9390	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	2.19100	1.87800	1.87800				
	68	0.3130	0.6260	0.3130	0.6260	0.3130	0.6260	0.3130	0.6260	0.3130	0.6260	0.3130	1.56500	1.25200	1.25200				
	11	0.3130	0.3130	0.3130	0.3130	0.6260	0.0000	0.3130	1.56500	1.56500	1.56500	1.56500	1.56500	1.87800	1.56500				
	12	0.0000	0.6260	0.3130	0.3130	0.6260	0.6260	0.6260	1.25200	1.87800	1.25200	1.87800	1.87800	1.25200	1.56500				
	13	0.3130	0.6260	0.3130	0.3130	0.6260	0.0000	0.3130	1.56500	1.87800	1.56500	1.87800	1.56500	1.56500	1.56500				
	14	0.6260	0.6260	0.6260	0.6260	0.0000	0.3130	0.3130	1.56500	1.56500	1.56500	1.56500	1.56500	1.56500	1.56500				
	15	0.3130	0.6260	0.6260	0.6260	0.6260	0.6260	0.3130	0.6260	1.25200	1.56500	1.56500	1.56500	1.56500	1.56500				
	16	0.3130	0.3130	0.0000	0.0000	0.0000	0.0000	0.0000	1.87800	1.87800	1.87800	1.87800	1.87800	1.87800	1.87800				
	71	0.9390	0.6260	0.6260	0.6260	1.5650	0.9390	0.9390	0.9390	0.93900	0.93900	0.62600	0.93900	1.87800	1.87800				
	72	0.3130	0.6260	0.3130	0.6260	0.6260	0.6260	0.3130	1.56500	1.87800	1.56500	1.87800	1.87800	1.87800	1.87800				
	73	0.3130	0.3130	0.6260	0.6260	0.3130	0.3130	0.3130	1.25200	1.56500	1.56500	1.56500	1.56500	1.56500	1.56500				
	74	0.9390	1.2520	0.9390	1.5650	1.5650	1.2520	0.6260	0.62600	0.31300	0.93900	0.93900	0.62600	0.31300	0.93900				
	75	0.6260	0.3130	0.6260	0.9390	0.9390	0.9390	0.9390	0.93900	0.62600	0.62600	0.62600	0.62600	0.62600	0.62600				
	76	0.0000	0.0000	0.0000	0.9390	0.9390	0.9390	0.9390	0.93900	0.93900	0.93900	0.93900	0.93900	0.93900	0.93900				
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.62600	0.31300	0.62600	0.31300	0.62600	0.31300	0.62600				
	Max	0.9390	1.2520	0.9390	1.5650	1.5650	1.2520	1.87800	1.87800	1.87800	1.87800	1.87800	1.87800	1.87800	1.87800				
	Mean	0.4173	0.5217	0.4434	0.6521	0.5478	0.5738	1.27808	1.38242	1.33025	1.33025	1.33025	1.33025	1.33025	1.33025				
	Std. Dev	0.3082	0.3082	0.2818	0.5247	0.4835	0.3735	0.36449	0.54145	0.44516	0.44516	0.44516	0.44516	0.44516	0.44516				
Mean - 3 Sigma	-0.5073	-0.4030	-0.4020	-0.9221	-0.9028	-0.5468	0.18462	-0.24192	-0.00522	-0.00522	-0.00522	-0.00522	-0.00522	-0.00522					
Mean + 3 Sigma	1.3420	1.4463	1.2888	2.2263	1.9983	1.6945	2.37155	3.00676	2.66572	2.66572	2.66572	2.66572	2.66572	2.66572					
				Limit: <6ns						Limit: <6ns									

Wafer #	SN	ChA Input Leakage @ 0V 5.5V, 5.5V			uA			ChB Input Leakage @ 0V 5.5V, 5.5V			uA			ChC Input Leakage @ 0V 5.5V, 5.5V			uA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal			
CTRL	9	0.0005	0.0011	0.0003	-0.0015	0.0000	0.0004	-0.0002	0.00075	0.00028									
	68	0.0000	0.0007	-0.0008	0.0007	0.0005	-0.0013	-0.00225	-0.00002	-0.00033									
	11	-0.0015	0.0005	0.0001	-0.0006	-0.0016	-0.0003	0.00037	-0.00117	-0.00186									
	12	-0.0009	-0.0003	-0.0003	-0.0008	-0.0002	-0.0006	-0.00010	-0.00025	0.00043									
	13	0.0003	0.0000	-0.0005	0.0003	-0.0002	-0.0011	-0.00079	-0.00071	0.00013									
	14	0.0004	-0.0002	0.0007	0.0001	0.0001	-0.0006	0.00029	0.00037	-0.00140									
	15	0.0005	-0.0008	0.0004	-0.0002	0.0003	0.00021	-0.00102	-0.00003	-0.00003									
	16	0.0004	-0.0007	0.0007	-0.0006	-0.0010	-0.0006	0.00021	-0.00102	-0.00110									
	71	0.0005	-0.0012	-0.0003	0.0004	-0.0010	-0.0009	-0.00063	-0.00025	-0.00308									
	72	0.0005	-0.0001	0.0003	-0.0012	-0.0001	0.0000	0.00106	0.00021	-0.00094									
	73	-0.0005	-0.0007	-0.0006	0.0008	-0.0010	0.0009	-0.00225	-0.00156	0.00082									
	74	-0.0011	-0.7155	-0.0007	0.0014	-0.4139	-0.0014	-0.00086	-0.00032	-0.00110									
	75	-0.0003	0.0002	-0.0016	-0.0001	-0.0002	-0.0003	-0.00156	-0.00117	0.00013									
	76	0.0001	-0.0002	0.0016	-0.0004	0.0000	-0.0007	-0.00033	-0.00140	0.00028									
	Min	-0.0015	-0.7155	-0.0016	-0.0012	-0.4139	-0.0014	-0.00225	-0.00156	-0.00308									
	Max	0.0005	0.0005	0.0016	0.0014	0.0001	0.0009	0.00106	0.00037	0.00082									
	Mean	-0.0001	-0.0599	0.0000	0.0001	-0.0349	-0.0004	-0.00036	-0.00069	-0.00064									
	Std. Dev	0.0007	0.2064	0.0008	0.0007	0.1193	0.0006	0.00092	0.00063	0.00114									
Mean - 3 Sigma	-0.0023	-0.6793	-0.0024	-0.0021	-0.3930	-0.0023	-0.00311	-0.00259	-0.00405										
Mean + 3 Sigma	0.0020	0.5594	0.0024	0.0022	0.3231	0.0014	0.00238	0.00121	0.00277										
				Limit: +/-10uA						Limit: +/-10uA									

Wafer #	SN	ChD Input Leakage @ 0V 5.5V, 5.5V			uA			ChA Input Leakage @ Vdd1 5.5V, 5.5V			uA			ChB Input Leakage @ Vdd1 5.5V, 5.5V			uA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal			
CTRL	9	-0.0012	-0.0003	-0.0002	0.0218	0.0350	0.0198	0.01979	0.02704	0.01824									
	68	-0.0009	-0.0012	-0.0023	0.0448	0.0192	0.0374	0.04040	0.01777	0.03067									
	11	-0.0002	-0.0004	-0.0009	0.0374	0.0333	0.0207	0.03171	0.02681	0.01755									
	12	0.0009	-0.0005	-0.0011	0.0268	0.0208	0.0294	0.02463	0.01692	0.02238									
	13	0.0011	-0.0005	-0.0004	0.0389	0.0414	0.0296	0.03171	0.03348	0.02545									
	14	-0.0007	-0.0018	-0.0003	0.0366	0.0310	0.0325	0.02971	0.02397	0.02676									
	15	0.0007	-0.0003	0.0000	0.0231	0.0225	0.0213	0.01763	0.01524	0.01563									
	16	-0.0003	-0.0004	-0.0007	0.0074	0.0255	0.0360	0.00433	0.02121	0.02875									
	71	0.0004	-0.0006	-0.0003	0.0234	0.0172	0.0117	0.01679	0.01156	0.00865									
	72	-0.0004	-0.0001	-0.0007	0.0178	0.0170	0.0169	0.01556	0.01225	0.01195									
	73	-0.0004	-0.0006	-0.0019	0.0426	0.0207	0.0246	0.03640	0.01746	0.01955									
	74	0.0005	-0.0013	-0.0010	0.0336	0.0061	0.0360	0.03340	0.00466	0.02829									
	75	-0.0017	0.0002	-0.0009	0.0473	0.0092	0.0334	0.04133	0.00972	0.02446									
	76	-0.0006	0.0002	-0.0010	0.0516	0.0449	0.0441	0.04771	0.03991	0.03742									
	Min	-0.0017	-0.0018	-0.0019	0.0074	0.0061	0.0117	0.00433	0.00466	0.00865									
	Max	0.0011	0.0002	0.0000	0.0516	0.0449	0.0441	0.04771	0.03991	0.03742									
	Mean	0.0000	-0.0005	-0.0008	0.0322	0.0241	0.0280	0.02758	0.01943	0.02224									
	Std. Dev	0.0008	0.0006	0.0005	0.0128	0.0118	0.0093	0.01226	0.01022	0.00803									
Mean - 3 Sigma	-0.0025	-0.0023	-0.0022	-0.0063	-0.0113	0.0002	-0.00921	-0.01122	-0.00184										
Mean + 3 Sigma	0.0024	0.0013	0.0007	0.0707	0.0595	0.0558	0.06436	0.05009	0.04631										
				Limit: +/-10uA						Limit: +/-10uA									

Wafer #	SN	ChC Input Leakage @ Vdd2 5.5V, 5.5V			ChD Input Leakage @ Vdd2 5.5V, 5.5V			Voh_A @ -20uA 5.5V, 5.5V			V
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0000	0.0008	0.0020	0.0007	0.0006	0.0010	5.49562	5.49210	5.49057	
	68	-0.0006	0.0004	0.0011	-0.0006	0.0013	0.0007	5.49630	5.49456	5.49069	
	11	0.0005	0.0020	0.0002	0.0007	0.0000	0.0016	5.49355	5.49310	5.49083	
	12	-0.0004	0.0015	0.0000	0.0011	0.0016	0.0007	5.49647	5.49395	5.49065	
	13	0.0010	0.0019	0.0010	0.0001	0.0019	0.0012	5.49647	5.49169	5.49096	
	14	0.0027	0.0015	0.0017	0.0018	0.0007	0.0003	5.49641	5.49437	5.49072	
	15	-0.0009	-0.0001	0.0012	0.0006	-0.0009	0.0008	5.49650	5.49416	5.49075	
	16	0.0011	0.0008	-0.0009	0.0017	0.0008	0.0006	5.49634	5.49418	5.48766	
	71	0.0018	0.0010	0.0001	0.0005	0.0021	0.0013	5.49653	5.49496	5.48931	
	72	0.0014	0.0021	0.0011	0.0019	0.0009	0.0006	5.49642	5.49419	5.49077	
	73	0.0001	0.0015	-0.0001	0.0002	0.0014	-0.0006	5.49634	5.49480	5.48905	
	74	0.0017	0.0011	0.0010	0.0023	0.0014	0.0007	5.49641	5.49413	5.49005	
	75	0.0006	0.0005	0.0014	0.0007	0.0015	-0.0012	5.49443	5.49399	5.49066	
	76	0.0024	0.0017	0.0008	0.0007	0.0018	0.0007	5.49563	5.49419	5.49080	
	Min	-0.0009	-0.0001	-0.0009	0.0001	-0.0009	-0.0012	5.49355	5.49169	5.48766	
	Max	0.0027	0.0021	0.0017	0.0023	0.0021	0.0016	5.49653	5.49496	5.49096	
	Mean	0.0010	0.0013	0.0006	0.0010	0.0011	0.0006	5.49596	5.49398	5.49018	
	Std. Dev	0.0011	0.0006	0.0008	0.0007	0.0009	0.0008	0.00097	0.00085	0.00101	
Mean - 3 Sigma	-0.0022	-0.0007	-0.0017	-0.0011	-0.0015	-0.0018	5.49306	5.49142	5.48715		
Mean + 3 Sigma	0.0042	0.0032	0.0029	0.0032	0.0037	0.0029	5.49886	5.49653	5.49322		
Limit: +/-10uA				Limit: +/-10uA				Limit: >-5.4V			

Wafer #	SN	Voh_B @ -20uA 5.5V, 5.5V			Voh_C @ -20uA 5.5V, 5.5V			Voh_D @ -20uA 5.5V, 5.5V			V
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.4955	5.4925	5.4930	5.4944	5.4940	5.4935	5.49419	5.49327	5.49321	
	68	5.4958	5.4949	5.4927	5.4955	5.4925	5.4934	5.49518	5.49164	5.49310	
	11	5.4934	5.4933	5.4931	5.4883	5.4929	5.4934	5.48831	5.49196	5.49339	
	12	5.4962	5.4947	5.4931	5.4956	5.4919	5.4934	5.49529	5.49133	5.49295	
	13	5.4961	5.4925	5.4932	5.4954	5.4921	5.4933	5.49543	5.49183	5.49302	
	14	5.4960	5.4947	5.4928	5.4955	5.4923	5.4933	5.49546	5.49198	5.49272	
	15	5.4962	5.4949	5.4932	5.4956	5.4927	5.4931	5.49544	5.49204	5.49297	
	16	5.4961	5.4943	5.4899	5.4954	5.4904	5.4934	5.49530	5.48970	5.49317	
	71	5.4962	5.4942	5.4919	5.4955	5.4909	5.4931	5.49523	5.49032	5.49310	
	72	5.4962	5.4943	5.4931	5.4951	5.4866	5.4936	5.49513	5.48655	5.49311	
	73	5.4961	5.4947	5.4915	5.4956	5.4925	5.4935	5.49528	5.49145	5.49320	
	74	5.4964	5.4945	5.4926	5.4956	5.4922	5.4931	5.49541	5.49179	5.49304	
	75	5.4940	5.4945	5.4930	5.4955	5.4926	5.4910	5.49524	5.49175	5.49040	
	76	5.4956	5.4947	5.4932	5.4954	5.4924	5.4935	5.49550	5.49160	5.49315	
	Min	5.4934	5.4925	5.4899	5.4883	5.4866	5.4910	5.48831	5.48655	5.49040	
	Max	5.4964	5.4949	5.4932	5.4956	5.4929	5.4936	5.49550	5.49204	5.49339	
	Mean	5.4957	5.4943	5.4925	5.4949	5.4916	5.4931	5.49475	5.49103	5.49285	
	Std. Dev	0.0010	0.0007	0.0010	0.0021	0.0017	0.0007	0.00203	0.00158	0.00079	
Mean - 3 Sigma	5.4928	5.4922	5.4895	5.4886	5.4864	5.4911	5.48866	5.48629	5.49049		
Mean + 3 Sigma	5.4986	5.4964	5.4956	5.5011	5.4969	5.4952	5.50085	5.49576	5.49522		
Limit: >-5.4V				Limit: >-5.4V				Limit: >-5.4V			

Wafer #	SN	Voh_A @ -4mA 5.5V, 5.5V			Voh_B @ -4mA 5.5V, 5.5V			Voh_C @ -4mA 5.5V, 5.5V			V
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.2895	5.2829	5.2862	5.2721	5.2779	5.2790	5.28497	5.28821	5.28832	
	68	5.2912	5.2882	5.2835	5.2829	5.2531	5.2516	5.28906	5.28221	5.27496	
	11	5.2716	5.2818	5.2833	5.2547	5.2749	5.2697	5.24785	5.28103	5.26455	
	12	5.2749	5.2872	5.2806	5.2711	5.2805	5.2788	5.28928	5.28056	5.28208	
	13	5.2937	5.2778	5.2832	5.2885	5.2651	5.2659	5.28856	5.27925	5.28176	
	14	5.2932	5.2875	5.2829	5.2685	5.2638	5.2662	5.28456	5.28137	5.28276	
	15	5.2932	5.2881	5.2830	5.2852	5.2666	5.2643	5.28578	5.28103	5.28323	
	16	5.2932	5.2883	5.2710	5.2869	5.2724	5.2498	5.28737	5.27416	5.28258	
	71	5.2921	5.2845	5.2753	5.2849	5.2554	5.2722	5.28815	5.27104	5.28014	
	72	5.2915	5.2863	5.2816	5.2757	5.2605	5.2611	5.28665	5.25795	5.28304	
	73	5.2941	5.2888	5.2771	5.2703	5.2652	5.2589	5.29012	5.28256	5.28242	
	74	5.2938	5.2867	5.2796	5.2753	5.2798	5.2679	5.28665	5.27819	5.28023	
	75	5.2869	5.2856	5.2820	5.2657	5.2627	5.2690	5.28750	5.27856	5.26880	
	76	5.2894	5.2857	5.2818	5.2759	5.2674	5.2624	5.28756	5.28028	5.28076	
	Min	5.2716	5.2778	5.2710	5.2547	5.2554	5.2498	5.24785	5.25795	5.26455	
	Max	5.2941	5.2888	5.2833	5.2885	5.2805	5.2788	5.29012	5.28256	5.28323	
	Mean	5.2890	5.2857	5.2801	5.2752	5.2679	5.2655	5.28417	5.27717	5.27936	
	Std. Dev	0.0077	0.0031	0.0038	0.0100	0.0076	0.0072	0.01154	0.00688	0.00608	
Mean - 3 Sigma	5.2660	5.2763	5.2687	5.2452	5.2449	5.2438	5.24956	5.25652	5.26111		
Mean + 3 Sigma	5.3120	5.2951	5.2916	5.3053	5.2908	5.2872	5.31877	5.29781	5.29761		
Limit: >-5.1V				Limit: >-5.1V				Limit: >-5.1V			

Wafer #	SN	Voh_D @ -4mA 5.5V, 5.5V			V			Vol_CHA @ 20uA 5.5V, 5.5V			Vol_CHB @ 20uA 5.5V, 5.5V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	5.2808	5.2672	5.2831	7.6473	1.1022	0.4520	7.69428	1.09761	1.15000			
	68	5.2844	5.2710	5.2818	1.5944	1.4830	0.7642	1.56970	1.54728	1.37000			
	11	5.2564	5.2696	5.2786	3.1139	1.9636	0.9203	3.18834	1.99069	1.52000			
	12	5.2823	5.2698	5.2793	0.9816	2.1009	0.7080	1.10724	1.97196	1.28000			
	13	5.2848	5.2734	5.2790	0.9379	1.8388	1.0400	1.05099	1.78460	1.62000			
	14	5.2854	5.2722	5.2792	1.0379	1.2770	2.1500	1.19473	1.49731	2.83000			
	15	5.2847	5.2749	5.2783	0.9879	1.2645	0.3708	1.08849	1.34743	1.00000			
	16	5.2781	5.2691	5.2789	1.3005	1.3005	0.5769	1.36347	1.09137	1.32000			
	71	5.2849	5.2675	5.2763	1.3568	1.2770	0.5519	1.40722	1.53479	1.08000			
	72	5.2833	5.2539	5.2800	0.9379	1.3394	0.2334	1.08224	1.42237	0.91600			
	73	5.2860	5.2779	5.2798	0.9816	1.2457	0.3957	1.19473	1.44111	1.15000			
	74	5.2830	5.2723	5.2724	2.1447	1.3331	0.3583	2.23841	1.37865	1.15000			
	75	5.2839	5.2693	5.2613	1.2818	1.2707	0.4769	1.38847	1.43486	1.05000			
	76	5.2838	5.2734	5.2722	1.3756	2.1134	0.4832	1.52596	2.18430	1.13000			
	Min	5.2564	5.2539	5.2613	0.9379	1.0210	0.2334	1.05099	1.09137	0.91600			
	Max	5.2860	5.2779	5.2800	3.1139	2.1134	2.1500	3.18834	2.18430	2.83000			
	Mean	5.2814	5.2703	5.2763	1.3698	1.5038	0.6888	1.48586	1.58995	1.33717			
	Std. Dev	0.0081	0.0059	0.0054	0.6456	0.3840	0.5165	0.62667	0.32131	0.51329			
	Mean - 3 Sigma	5.2570	5.2525	5.2600	-0.5671	0.3517	-0.8607	-0.39414	0.62603	-0.20272			
Mean + 3 Sigma	5.3057	5.2880	5.2926	3.3068	2.6558	2.2383	3.36586	2.55388	2.87705				
Limit: >5.1V				Limit: <100mV				Limit: <100mV					

Wafer #	SN	Vol_CHC @ 20uA 5.5V, 5.5V			Vol_CHD @ 20uA 5.5V, 5.5V			Vol_CHA @ 400uA 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.6192	2.0338	2.3100	2.5311	1.8934	2.2400	24.73025	17.30723	16.68000	
	68	2.0382	2.6955	1.7700	1.9060	2.3303	1.5500	17.78325	17.77541	17.28000	
	11	2.2381	2.2024	2.2100	1.9623	1.7811	1.8200	20.61583	17.90650	17.00000	
	12	3.3876	2.5332	1.9000	3.1999	2.1306	1.7000	18.79623	18.09376	16.76000	
	13	2.9753	2.4646	1.8100	2.7998	1.9808	1.6500	17.12044	17.91898	17.11000	
	14	2.9815	1.9964	1.8400	2.8499	1.6874	1.6600	17.06417	17.21984	18.29000	
	15	2.7066	2.3085	1.9300	2.5623	1.8809	1.6800	17.15796	16.92021	16.51000	
	16	2.2818	2.2960	1.6900	2.1311	1.9620	1.3600	17.52688	16.77664	16.76000	
	71	1.9944	2.3397	1.7800	1.7998	1.7561	1.6000	17.48311	17.66304	16.64000	
	72	2.0444	2.2523	1.6900	1.9123	1.8435	1.4300	17.28302	17.21984	16.30000	
	73	1.9819	2.4646	1.5100	1.8185	1.8996	1.3500	17.12670	17.10124	16.43000	
	74	2.2943	2.5270	1.5600	2.0748	2.2366	1.5700	18.53360	17.33220	16.56000	
	75	2.0444	3.9253	1.6100	1.7998	3.4474	1.5300	17.37681	17.21360	16.38000	
	76	2.5192	2.6269	1.6700	2.3186	2.2554	1.5700	17.63318	18.35594	16.51000	
	Min	1.9819	1.9964	1.5100	1.7998	1.6874	1.3500	17.06417	16.77664	16.30000	
	Max	3.3876	3.9253	2.2100	3.1999	3.4474	1.8200	20.61583	18.35594	18.29000	
	Mean	2.4541	2.4947	1.7667	2.2691	2.0718	1.5767	17.80983	17.47682	16.77083	
	Std. Dev	0.4633	0.4823	0.1913	0.4763	0.4697	0.1414	1.04344	0.49833	0.53631	
	Mean - 3 Sigma	1.0642	1.0478	1.1927	0.8402	0.6626	1.1523	14.67952	15.98182	15.16191	
Mean + 3 Sigma	3.8440	3.9417	2.3406	3.6980	3.4810	2.0010	20.94014	18.97181	18.37976		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHB @ 400uA 5.5V, 5.5V			Vol_CHC @ 400uA 5.5V, 5.5V			Vol_CHD @ 400uA 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	26.4430	17.8663	18.2800	19.5810	19.2314	19.4800	19.90725	20.83497	19.85000	
	68	18.8873	21.1951	23.5700	19.1686	19.8431	20.1000	19.40721	20.76008	19.30000	
	11	22.3620	18.7344	19.3000	20.2432	18.9068	20.7300	19.47596	19.64293	19.23000	
	12	19.5247	18.8531	18.1200	20.5556	19.2064	18.8300	21.12608	19.96747	19.05000	
	13	17.7498	19.4152	19.7100	20.1245	19.3125	18.5700	20.34477	19.31216	18.75000	
	14	19.7372	19.6962	20.9700	20.6680	18.8693	18.6400	20.45728	19.43074	18.98000	
	15	18.2498	19.3028	19.3300	20.1995	19.0816	18.7500	20.15726	19.41201	19.11000	
	16	18.3186	18.5721	19.5800	19.4185	18.8381	18.4500	20.24477	19.16237	18.72000	
	71	18.4248	20.4581	17.7800	19.0749	19.3063	18.8100	19.38846	19.23726	19.07000	
	72	18.9685	20.0834	19.4400	19.1811	18.9505	18.6200	19.39471	18.93145	18.55000	
	73	19.4560	19.6525	19.1400	19.0375	19.0504	18.4400	19.23845	19.13117	18.62000	
	74	20.9184	18.1599	18.6000	19.5185	19.5622	18.6300	19.80724	19.91754	19.24000	
	75	19.6622	19.7087	18.5500	19.0812	20.9605	18.6400	19.33221	21.40291	19.22000	
	76	19.1810	20.1584	19.1900	19.7309	19.5373	18.8700	20.01350	19.78648	19.46000	
	Min	17.7498	18.1599	17.7800	19.0375	18.8381	18.4400	19.23845	18.93145	18.55000	
	Max	22.3620	20.4581	20.9700	20.6680	20.9605	20.7300	21.12608	21.40291	19.46000	
	Mean	19.3794	19.3996	19.1425	19.7361	19.2985	18.8317	19.91506	19.61121	19.00000	
	Std. Dev	1.2676	0.6996	0.8285	0.6010	0.5782	0.6134	0.57779	0.65075	0.28255	
	Mean - 3 Sigma	15.5765	17.3008	16.6569	17.9331	17.5640	16.9915	18.18168	17.65897	18.15234	
Mean + 3 Sigma	23.1823	21.4984	21.6281	21.5391	21.0330	20.6718	21.64844	21.56344	19.84766		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHA @ 4mA 5.5V, 5.5V			Vol_CHB @ 4mA 5.5V, 5.5V			Vol_CHC @ 4mA 5.5V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	187.1063	170.8371	170.5600	204.4933	177.2721	181.8700	181.50212	180.27672	181.35000	
	68	172.7808	174.4514	173.0700	182.8948	207.1248	244.5900	181.72078	184.10325	192.49000	
	11	186.7811	170.4001	170.1300	204.6558	178.7773	188.9600	191.93535	177.66744	197.53000	
	12	187.3126	170.9620	170.2400	195.5752	179.8515	177.8800	184.40093	178.57881	180.40000	
	13	170.3359	170.7559	169.9600	176.8702	185.5285	192.5100	182.97027	179.39655	179.56000	
	14	170.5610	169.3265	172.7300	196.3376	194.5904	193.4800	187.21852	177.42398	178.63000	
	15	170.4172	168.0842	168.2700	180.7324	191.7800	192.3300	185.68166	178.84098	179.14000	
	16	171.7241	167.7846	169.0500	179.3325	185.1288	194.3100	183.05148	177.51762	178.83000	
	71	171.9241	171.5612	170.1900	180.8449	200.8920	177.5000	181.39590	180.87597	180.86000	
	72	172.4306	169.9694	168.8300	189.7755	199.1745	194.0800	182.47671	178.87220	179.11000	
	73	169.7231	168.1154	168.1100	194.4190	193.4538	191.4600	180.39007	177.52385	178.85000	
	74	173.6312	168.8645	169.3000	195.8251	177.3471	185.5700	183.42632	181.03828	180.56000	
	75	171.0362	168.7522	168.4200	192.8003	193.7785	185.8300	181.36467	183.06078	181.44000	
	76	172.2118	171.7547	168.9400	187.4820	191.7113	192.4400	183.03898	180.18308	180.39000	
	Min	169.7231	167.7846	168.1100	176.8702	177.3471	177.5000	180.39007	177.42398	178.63000	
	Max	187.3126	171.7547	172.7300	204.6558	200.8920	194.3100	191.93535	183.06078	197.53000	
	Mean	174.0074	169.6942	169.5142	189.5542	189.3345	188.8625	183.94591	179.24830	181.27500	
	Std. Dev	6.1867	1.3975	1.2678	8.5665	7.8834	5.9762	3.14170	1.75385	5.20075	
	Mean - 3 Sigma	155.4472	165.5018	165.7109	163.8546	165.6842	170.9340	174.52080	173.98674	165.67275	
	Mean + 3 Sigma	192.5676	173.8866	173.3174	215.2538	212.9847	206.7910	193.37101	184.50985	196.87725	
Limit: <400mV				Limit: <400mV				Limit: <400mV			

Wafer #	SN	Vol_CHD @ 4mA 5.5V, 5.5V			Idd1 Quiescent 5V, 5V			Idd2 Quiescent 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	186.2372	200.7332	186.8500	2.6078	2.5956	2.6074	2.59562	2.58016	2.57690	
	68	185.6497	195.3223	186.2200	2.5566	2.4605	2.5610	2.59969	2.58301	2.59603	
	11	184.8496	187.8018	184.4200	2.5842	2.5440	2.5798	2.58219	2.63062	2.61027	
	12	191.9438	189.4182	183.1700	2.5631	2.5248	2.5964	2.53459	2.60539	2.57080	
	13	187.6373	184.0010	183.1600	2.5391	2.5163	2.5765	2.55656	2.62329	2.57568	
	14	186.8497	186.1229	182.9800	2.5981	2.5505	2.6200	2.56917	2.61515	2.60539	
	15	186.7310	184.6563	183.7000	2.5859	2.5240	2.6091	2.57731	2.63021	2.59684	
	16	192.4376	182.9338	183.6700	2.5867	2.5269	2.5916	2.62207	2.67171	2.59115	
	71	185.0621	184.5377	185.3900	2.5476	2.5049	2.5671	2.64242	2.65218	2.62777	
	72	186.0372	182.9026	182.6800	2.5834	2.5252	2.6001	2.59196	2.61963	2.59115	
	73	184.6996	181.8853	182.8400	2.5924	2.5614	2.6213	2.63428	2.62085	2.63509	
	74	187.6748	186.8219	189.0800	2.5627	2.5220	2.5928	2.60132	2.66317	2.61800	
	75	185.5059	192.0270	190.1000	2.5956	2.5338	2.6013	2.61800	2.67782	2.64242	
	76	187.7685	187.0029	188.9700	2.5509	2.4963	2.5781	2.58341	2.65422	2.60376	
	Min	184.6996	181.8853	182.6800	2.5391	2.4963	2.5671	2.53459	2.60539	2.57080	
	Max	192.4376	192.0270	190.1000	2.5981	2.5614	2.6213	2.64242	2.67782	2.64242	
	Mean	187.2664	185.8426	185.0133	2.5741	2.5275	2.5945	2.59277	2.63869	2.60569	
	Std. Dev	2.5501	2.9622	2.7506	0.0204	0.0182	0.0171	0.03226	0.02405	0.02235	
	Mean - 3 Sigma	179.6160	176.9560	176.7615	2.5129	2.4728	2.5431	2.49599	2.56654	2.53863	
	Mean + 3 Sigma	194.9168	194.7292	193.2652	2.6353	2.5822	2.6459	2.68956	2.71084	2.67275	
Limit: <400mV				Limit: <3.8mA				Limit: <3.4mA			

Wafer #	SN	Idd1 Dynamic 2MBPS 5V, 5V			Idd2 Dynamic 2MBPS 5V, 5V			Idd1 Dynamic 10MBPS 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.4049	3.3929	3.3875	3.3745	3.3730	3.3667	7.98499	7.96330	7.89779	
	68	3.4146	3.2852	3.3720	3.4349	3.3812	3.3889	8.20223	7.98348	8.04501	
	11	3.4235	3.3046	3.4058	3.3910	3.3738	3.3654	8.24602	7.87305	8.06287	
	12	3.3823	3.2632	3.3685	3.3865	3.3535	3.3562	7.99854	7.64828	7.85657	
	13	3.4097	3.2679	3.3669	3.4106	3.3696	3.3733	8.17383	7.79554	7.98658	
	14	3.4069	3.2848	3.3734	3.3907	3.3728	3.3621	7.99524	7.68681	7.86780	
	15	3.4146	3.2880	3.3891	3.4248	3.3881	3.3891	8.14580	7.77885	7.97278	
	16	3.4132	3.2720	3.3764	3.4144	3.3783	3.3739	8.10087	7.70113	7.91032	
	71	3.4077	3.2573	3.3597	3.4409	3.3877	3.3907	8.15935	7.72250	7.89836	
	72	3.4331	3.2839	3.3871	3.4014	3.3649	3.3505	8.13509	7.73397	7.90467	
	73	3.4373	3.2991	3.3944	3.4578	3.4215	3.4056	8.15308	7.73609	7.91769	
	74	3.4354	3.2807	3.3951	3.4270	3.4020	3.3931	8.26612	7.76038	8.03793	
	75	3.4275	3.2851	3.3893	3.4331	3.3903	3.3936	8.20862	7.71627	7.97628	
	76	3.4235	3.2920	3.3804	3.4191	3.3994	3.4038	8.26172	7.86752	8.03748	
	Min	3.3823	3.2573	3.3597	3.3865	3.3535	3.3505	7.99524	7.64828	7.85657	
	Max	3.4373	3.3046	3.4058	3.4578	3.4215	3.4056	8.26612	7.87305	8.06287	
	Mean	3.4179	3.2815	3.3822	3.4164	3.3835	3.3798	8.15369	7.75170	7.95244	
	Std. Dev	0.0156	0.0142	0.0136	0.0218	0.0186	0.0186	0.08978	0.06809	0.06969	
	Mean - 3 Sigma	3.3712	3.2389	3.3414	3.3510	3.3277	3.3239	7.88435	7.54742	7.74337	
	Mean + 3 Sigma	3.4646	3.3242	3.4230	3.4819	3.4393	3.4357	8.42303	7.95598	8.16152	
Limit: <20mA				Limit: <20mA				Limit: <20mA			

Wafer #	SN	Idd2 Dynamic 10MBPS 5V, 5V			Idd1 Dynamic 25MBPS 5V, 5V			Idd2 Dynamic 25MBPS 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	7.6320	7.6386	7.6777	16.1563	16.1224	15.9436	15.51070	15.50004	15.62830	
	68	7.8661	7.7404	7.7529	16.7795	16.3693	16.4100	16.13167	15.81457	15.89803	
	11	7.7461	7.6134	7.6710	16.7872	16.0189	16.3914	15.79561	15.41097	15.64750	
	12	7.8489	7.5859	7.6928	16.2285	15.4458	15.8932	16.07361	15.38180	15.65780	
	13	7.9268	7.6398	7.6991	16.6611	15.8485	16.1852	16.31010	15.50586	15.73844	
	14	7.8272	7.6364	7.6674	16.2244	15.5618	15.9047	15.94645	15.47140	15.57719	
	15	7.9555	7.6880	7.7433	16.5770	15.8378	16.2061	16.29215	15.58053	15.77433	
	16	7.9030	7.5892	7.6650	16.4821	15.5850	16.17619	15.37231	15.59094	15.59094	
	71	7.8887	7.6236	7.6522	16.7088	15.7351	16.0669	16.12740	15.41793	15.54814	
	72	7.8964	7.6490	7.6351	16.5880	15.6718	16.0260	16.20288	15.51176	15.52246	
	73	7.9588	7.7165	7.7126	16.5883	15.6357	15.9867	16.29708	15.63607	15.64893	
	74	7.8230	7.6386	7.6570	16.9581	15.9493	16.3803	15.98043	15.46554	15.54382	
	75	7.8210	7.5678	7.6201	16.8075	15.6762	16.2028	15.92269	15.28060	15.44930	
	76	7.8657	7.6564	7.7291	16.9170	16.0588	16.3658	16.11288	15.52767	15.77096	
	Min	7.7461	7.5678	7.6201	16.2244	15.4458	15.8932	15.79561	15.28060	15.44930	
	Max	7.9588	7.7165	7.7433	16.9581	16.9581	16.3914	16.31010	15.63607	15.77433	
	Mean	7.8718	7.6337	7.6787	16.6273	15.7521	16.1329	16.10312	15.46354	15.62248	
	Std. Dev	0.0624	0.0424	0.0374	0.2349	0.1918	0.1818	0.16508	0.09773	0.10264	
	Mean - 3 Sigma	7.6847	7.5066	7.5666	15.9225	15.1766	15.5875	15.60789	15.17035	15.31457	
Mean + 3 Sigma	8.0588	7.7608	7.7908	17.3321	16.3276	16.6783	16.59835	15.75673	15.93040		
Limit: <20mA				Limit: <20mA				Limit: <20mA			

Wafer #	SN	CHA Prop Delay Rise 5V, 5V			CHB Prop Delay Rise 5V, 5V			CHC Prop Delay Rise 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	38.4990	38.8120	38.8120	38.4990	38.8120	38.8120	38.81200	38.81200	38.81200	
	68	39.7510	40.0640	39.7510	39.4380	39.7510	39.7510	39.43800	39.43800	39.43800	
	11	39.1250	38.8120	39.1250	39.4380	38.8120	39.4380	39.43800	38.49900	39.12500	
	12	39.7510	38.8120	39.1250	39.4380	38.8120	39.4380	39.75100	38.81200	39.12500	
	13	40.0640	38.8120	39.4380	39.4380	38.8120	39.1250	39.43800	38.81200	39.12500	
	14	38.8120	38.1860	38.4990	38.8120	37.8730	38.1860	39.12500	38.49900	38.81200	
	15	39.7510	39.1250	39.4380	39.1250	38.1860	38.4990	39.43800	38.81200	39.12500	
	16	39.1250	38.1860	38.8120	39.1250	38.4990	38.8120	38.49900	37.87300	38.18600	
	71	40.0640	39.4380	39.7510	39.1250	38.4990	38.8120	39.12500	38.49900	38.81200	
	72	40.0640	38.8120	39.1250	38.8120	38.1860	39.4380	39.43800	38.49900	38.81200	
	73	39.1250	38.4990	38.4990	38.8120	37.8730	38.4990	38.81200	38.18600	38.49900	
	74	40.0640	39.4380	39.7510	40.0640	39.4380	39.7510	39.12500	37.87300	38.49900	
	75	39.7510	39.1250	39.4380	39.4380	38.8120	39.1250	38.81200	37.87300	38.49900	
	76	40.0640	39.4380	39.7510	39.7510	38.8120	39.4380	39.43800	38.81200	39.12500	
	Min	38.8120	38.1860	38.4990	38.8120	37.8730	38.1860	38.49900	37.87300	38.18600	
	Max	40.0640	39.4380	39.7510	40.0640	39.4380	39.7510	39.75100	38.81200	39.12500	
	Mean	39.6467	38.8903	39.2293	39.2815	38.5251	38.8903	39.20325	38.42075	38.81200	
	Std. Dev	0.4687	0.4452	0.4493	0.3891	0.4518	0.5016	0.35625	0.38043	0.32692	
	Mean - 3 Sigma	38.2405	37.5548	37.8814	38.1142	37.1698	37.3855	38.13450	37.27946	37.83125	
Mean + 3 Sigma	41.0528	40.2257	40.5773	40.4488	39.8804	40.3950	40.27200	39.56204	39.79275		
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHD Prop Delay Rise 5V, 5V			CHA Prop Delay Fall 5V, 5V			CHB Prop Delay Fall 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	39.1250	39.1250	39.1250	39.1250	39.1250	39.4380	39.43800	39.43800	39.75100	
	68	39.1250	39.4380	39.4380	40.3770	40.3770	40.3770	40.06400	40.37700	40.37700	
	11	39.7510	38.8120	39.1250	40.0640	39.4380	39.7510	40.37700	39.75100	40.06400	
	12	39.4380	38.8120	38.8120	40.3770	39.4380	39.7510	40.06400	39.12500	39.43800	
	13	39.4380	38.8120	39.4380	40.6900	39.4380	40.0640	40.37700	39.43800	40.06400	
	14	39.1250	38.4990	38.4990	39.7510	39.1250	39.1250	39.75100	38.81200	39.12500	
	15	39.1250	38.4990	38.8120	40.3770	39.7510	40.0640	40.06400	39.12500	39.43800	
	16	39.1250	38.4990	38.8120	40.0640	38.8120	39.4380	39.75100	38.81200	39.43800	
	71	38.8120	38.4990	38.8120	40.6900	40.0640	40.3770	40.06400	39.43800	39.75100	
	72	39.4380	38.4990	38.8120	40.6900	39.7510	39.7510	40.06400	39.12500	39.43800	
	73	38.8120	38.1860	38.4990	39.7510	38.8120	39.1250	39.75100	38.81200	39.43800	
	74	38.8120	37.8730	38.4990	40.6900	39.7510	40.0640	40.69000	39.75100	40.06400	
	75	38.4990	37.8730	38.1860	40.0640	39.4380	40.0640	40.06400	39.12500	39.43800	
	76	39.1250	38.4990	38.8120	40.3770	39.7510	40.0640	40.37700	39.75100	40.06400	
	Min	38.4990	37.8730	38.1860	39.7510	38.8120	39.1250	39.75100	38.81200	39.12500	
	Max	39.7510	38.8120	39.4380	40.6900	40.0640	40.3770	40.69000	39.75100	40.06400	
	Mean	39.1250	38.4468	38.7598	40.2988	39.4641	39.8032	40.11617	39.25542	39.64667	
	Std. Dev	0.3531	0.3223	0.3223	0.3563	0.3882	0.3967	0.29342	0.36449	0.33588	
	Mean - 3 Sigma	38.0657	37.4798	37.7928	39.2300	38.2996	38.6132	39.23591	38.16195	38.63904	
Mean + 3 Sigma	40.1843	39.4139	39.7269	41.3675	40.6285	40.9932	40.99642	40.34888	40.65430		
Limit: 29<x<50ns				Limit: 29<x<50ns				Limit: 29<x<50ns			

Wafer #	SN	CHC Prop Delay Fall 5V, 5V			CHD Prop Delay Fall 5V, 5V			CHA PWD 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	39.7510	39.7510	39.7510	41.3160	41.3160	41.0030	0.62600	0.31300	0.62600	
	68	40.0640	40.0640	40.0640	41.6290	41.6290	41.6290	0.62600	0.31300	0.62600	
	11	40.3770	39.7510	40.0640	41.6290	40.6900	41.0030	0.93900	0.62600	0.62600	
	12	40.3770	39.4380	39.7510	41.3160	40.6900	40.6900	0.62600	0.62600	0.62600	
	13	40.3770	40.0640	40.3770	41.6290	41.0030	41.3160	0.62600	0.62600	0.62600	
	14	40.0640	39.4380	39.7510	41.0030	40.3770	40.6900	0.93900	0.93900	0.62600	
	15	40.3770	39.7510	40.0640	41.0030	40.3770	40.6900	0.62600	0.62600	0.62600	
	16	39.7510	39.1250	39.4380	41.6290	40.3770	41.0030	0.93900	0.62600	0.62600	
	71	39.7510	39.4380	39.7510	40.6900	40.0640	40.3770	0.62600	0.62600	0.62600	
	72	40.0640	39.4380	39.7510	41.6290	40.6900	41.0030	0.62600	0.93900	0.62600	
	73	39.7510	39.1250	39.4380	40.6900	40.0640	40.3770	0.62600	0.31300	0.62600	
	74	39.7510	38.4990	39.4380	41.0030	40.0640	40.6900	0.62600	0.31300	0.31300	
	75	39.4380	38.8120	39.4380	40.6900	40.0640	40.3770	0.31300	0.31300	0.62600	
	76	40.0640	39.7510	40.0640	41.3160	40.3770	40.6900	0.31300	0.31300	0.31300	
	Min	39.4380	38.4990	39.4380	40.6900	40.0640	40.3770	0.31300	0.31300	0.31300	
	Max	40.3770	40.0640	40.3770	41.6290	41.0030	41.3160	0.93900	0.93900	0.62600	
	Mean	40.0118	39.3858	39.7771	41.1856	40.4031	40.7422	0.65208	0.57383	0.57383	
	Std. Dev	0.3223	0.4393	0.3118	0.3882	0.3118	0.2934	0.20926	0.22465	0.12184	
Mean - 3 Sigma	39.0448	38.0680	38.8416	40.0211	39.4676	39.8619	0.02431	-0.10013	0.20833		
Mean + 3 Sigma	40.9789	40.7037	40.7125	42.3500	41.3385	41.6224	1.27986	1.24779	0.93934		
Limit: 29-x-50ns				Limit: 29-x-50ns				Limit: <5ns			

Wafer #	SN	CHB PWD 5V, 5V			CHC PWD 5V, 5V			CHD PWD 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.9390	0.6260	0.9390	0.9390	0.9390	2.19100	2.19100	2.19100	1.87800	
	68	0.6260	0.6260	0.6260	0.6260	0.6260	2.50400	2.19100	2.19100	2.19100	
	11	0.9390	0.9390	0.6260	0.9390	1.2520	0.9390	1.87800	1.87800	1.87800	
	12	0.6260	0.6260	0.6260	0.6260	0.6260	0.6260	1.87800	1.87800	1.87800	
	13	0.9390	0.6260	0.9390	0.9390	1.2520	1.2520	2.19100	2.19100	1.87800	
	14	0.9390	0.9390	0.9390	0.9390	0.9390	0.9390	1.87800	1.87800	2.19100	
	15	0.9390	0.9390	0.9390	0.9390	0.9390	0.9390	1.87800	1.87800	1.87800	
	16	0.6260	0.3130	0.6260	1.2520	1.2520	1.2520	2.50400	1.87800	2.19100	
	71	0.9390	0.9390	0.9390	0.6260	0.9390	0.9390	1.87800	1.56500	1.56500	
	72	1.2520	0.9390	1.2520	0.6260	0.9390	0.9390	2.19100	2.19100	2.19100	
	73	0.9390	0.9390	0.9390	0.9390	0.9390	0.9390	1.87800	1.87800	1.87800	
	74	0.6260	0.3130	0.3130	0.6260	0.6260	0.9390	2.19100	2.19100	2.19100	
	75	0.6260	0.3130	0.3130	0.6260	0.9390	0.9390	2.19100	2.19100	2.19100	
	76	0.6260	0.9390	0.6260	0.6260	0.9390	0.9390	2.19100	1.87800	1.87800	
	Min	0.6260	0.3130	0.3130	0.6260	0.6260	0.6260	1.87800	1.56500	1.56500	
	Max	1.2520	0.9390	1.2520	1.2520	1.2520	1.2520	2.50400	2.19100	2.19100	
	Mean	0.8347	0.7303	0.7564	0.8086	0.9651	0.9651	2.06058	1.95625	1.98233	
	Std. Dev	0.2039	0.2778	0.2818	0.2093	0.2093	0.1612	0.20926	0.19456	0.20387	
Mean - 3 Sigma	0.2231	-0.1031	-0.0890	0.1808	0.3373	0.4816	1.43281	1.37258	1.37073		
Mean + 3 Sigma	1.4463	1.5638	1.6018	1.4364	1.5929	1.4486	2.68836	2.53992	2.59394		
Limit: <5ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	AB_Rise Match Codirection 5V, 5V			AB_Fall Match Codirection 5V, 5V			CD_Rise Match Codirection 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0000	0.0000	0.0000	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300	
	68	0.3130	0.3130	0.0000	0.3130	0.0000	0.0000	0.31300	0.00000	0.00000	
	11	0.3130	0.0000	0.3130	0.3130	0.3130	0.3130	0.31300	0.31300	0.00000	
	12	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.31300	0.00000	0.31300	
	13	0.6260	0.0000	0.3130	0.3130	0.0000	0.0000	0.00000	0.00000	0.31300	
	14	0.0000	0.3130	0.3130	0.0000	0.3130	0.0000	0.00000	0.00000	0.31300	
	15	0.6260	0.9390	0.9390	0.3130	0.6260	0.6260	0.31300	0.31300	0.31300	
	16	0.0000	0.3130	0.0000	0.3130	0.0000	0.0000	0.62600	0.62600	0.62600	
	71	0.9390	0.9390	0.9390	0.6260	0.6260	0.6260	0.31300	0.00000	0.00000	
	72	1.2520	0.6260	0.9390	0.6260	0.6260	0.3130	0.00000	0.00000	0.00000	
	73	0.3130	0.6260	0.0000	0.0000	0.0000	0.3130	0.00000	0.00000	0.00000	
	74	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.31300	0.00000	0.00000	
	75	0.3130	0.3130	0.3130	0.0000	0.3130	0.6260	0.31300	0.00000	0.31300	
	76	0.3130	0.6260	0.3130	0.0000	0.0000	0.0000	0.31300	0.31300	0.31300	
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000	
	Max	1.2520	0.9390	0.9390	0.6260	0.6260	0.6260	0.62600	0.62600	0.62600	
	Mean	0.4173	0.4173	0.3913	0.2348	0.2608	0.2608	0.23475	0.13042	0.20867	
	Std. Dev	0.3853	0.3359	0.3563	0.2359	0.2613	0.2613	0.19456	0.20926	0.20387	
Mean - 3 Sigma	-0.7385	-0.5903	-0.6775	-0.4730	-0.5231	-0.5231	-0.34892	-0.49736	-0.40294		
Mean + 3 Sigma	1.5732	1.4250	1.4600	0.9425	1.0448	1.0448	0.81842	0.75819	0.82027		
Limit: <4ns				Limit: <4ns				Limit: <4ns			

Wafer #	SN	CD_Fall Match Codirection 5V, 5V			AB_C Rise Match Opposing 5V, 5V			AB_C Fall Match Opposing 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.5650	1.5650	1.2520	0.3130	0.0000	0.0000	0.62600	0.62600	0.31300	
	68	1.5650	1.5650	1.5650	0.3130	0.6260	0.3130	0.31300	0.31300	0.31300	
	11	1.2520	0.9390	0.9390	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300	
	12	0.9390	1.2520	0.9390	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300	
	13	1.2520	0.9390	0.9390	0.6260	0.0000	0.3130	0.31300	0.62600	0.31300	
	14	0.9390	0.9390	0.9390	0.3130	0.6260	0.6260	0.31300	0.62600	0.62600	
	15	0.6260	0.6260	0.6260	0.3130	0.6260	0.6260	0.31300	0.62600	0.62600	
	16	1.8780	1.2520	1.5650	0.6260	0.6260	0.6260	0.31300	0.31300	0.00000	
	71	0.9390	0.6260	0.6260	0.9390	0.9390	0.9390	0.93900	0.62600	0.62600	
	72	1.5650	1.2520	1.2520	0.6260	0.3130	0.6260	0.62600	0.31300	0.31300	
	73	0.9390	0.9390	0.9390	0.3130	0.3130	0.0000	0.00000	0.31300	0.31300	
	74	1.2520	1.5650	1.2520	0.9390	1.5650	1.2520	0.93900	1.25200	0.62600	
	75	1.2520	1.2520	0.9390	0.9390	1.2520	0.9390	0.62600	0.62600	0.62600	
	76	1.2520	0.6260	0.6260	0.6260	0.6260	0.6260	0.31300	0.00000	0.00000	
	Min	0.6260	0.6260	0.6260	0.3130	0.0000	0.0000	0.00000	0.00000	0.00000	
	Max	1.8780	1.5650	1.5650	0.9390	1.5650	1.2520	0.93900	1.25200	0.62600	
	Mean	1.1738	1.0173	0.9651	0.5738	0.6260	0.5999	0.44342	0.49558	0.39125	
	Std. Dev	0.3303	0.3021	0.2818	0.2613	0.4426	0.3392	0.28181	0.31181	0.23593	
Mean - 3 Sigma	0.1828	0.1108	0.1197	-0.2101	-0.4176	-0.7019	-0.40200	-0.43985	-0.31655		
Mean + 3 Sigma	2.1647	1.9237	1.8105	1.3578	1.9539	1.6174	1.28883	1.43102	1.09905		
Limit:<4ns				Limit:<6ns				Limit:<6ns			

Wafer #	SN	AB_D Rise Match Opposing 5V, 5V			AB_D Fall Match Opposing 5V, 5V			ChA Input Leakage @ 0V 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.6260	0.3130	0.3130	2.1910	2.1910	1.5650	-0.00070	0.00015	0.00066	
	68	0.6260	0.6260	0.3130	1.5650	1.2520	1.2520	-0.00062	0.00038	-0.00056	
	11	0.6260	0.0000	0.3130	1.5650	1.2520	1.2520	0.00130	-0.00046	0.00089	
	12	0.3130	0.3130	0.3130	1.2520	1.5650	1.2520	-0.00054	0.00053	0.00074	
	13	0.6260	0.0000	0.3130	1.2520	1.5650	1.2520	0.00053	0.00161	0.00013	
	14	0.3130	0.6260	0.3130	1.2520	1.5650	1.5650	0.00061	0.00115	0.00112	
	15	0.6260	0.6260	0.6260	0.9390	1.2520	1.2520	-0.00001	-0.00153	0.00013	
	16	0.0000	0.3130	0.0000	1.8780	1.5650	1.5650	-0.00039	0.00000	0.00021	
	71	1.2520	0.9390	0.9390	0.6260	0.6260	0.6260	-0.00162	-0.00146	0.00005	
	72	0.6260	0.3130	0.6260	1.5650	1.5650	1.5650	-0.00116	-0.00046	0.00044	
	73	0.3130	0.3130	0.0000	0.9390	1.2520	1.2520	-0.00139	0.00000	0.00005	
	74	1.2520	1.5650	1.2520	0.3130	0.3130	0.6260	0.00099	-0.25297	-0.00079	
	75	1.2520	1.2520	1.2520	0.6260	0.9390	0.9390	0.00030	-0.00008	0.00005	
	76	0.9390	0.9390	0.9390	0.9390	0.6260	0.6260	-0.00124	-0.00138	-0.00040	
	Min	0.0000	0.0000	0.0000	0.3130	0.3130	0.6260	-0.00162	-0.25297	-0.00079	
	Max	1.2520	1.5650	1.2520	1.8780	1.5650	1.5650	0.00130	0.00161	0.00112	
	Mean	0.6782	0.5999	0.5738	1.0955	1.1738	1.1477	-0.00022	-0.02125	0.00022	
	Std. Dev	0.4185	0.4896	0.4393	0.4526	0.4452	0.3614	0.00099	0.07298	0.00053	
Mean - 3 Sigma	-0.5774	-0.8689	-0.7440	-0.2623	-0.1617	0.0634	-0.00318	-0.24019	-0.00137		
Mean + 3 Sigma	1.9337	2.0688	1.8917	2.4533	2.5092	2.2319	0.00275	0.19768	0.00181		
Limit:<6ns				Limit:<6ns				Limit:±/10uA			

Wafer #	SN	ChB Input Leakage @ 0V 5V, 5V			ChC Input Leakage @ 0V 5V, 5V			ChD Input Leakage @ 0V 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0000	-0.0007	0.0011	-0.0004	-0.0001	-0.0013	0.00029	-0.00014	-0.00072	
	68	-0.0003	-0.0005	-0.0013	-0.0005	-0.0007	-0.0003	-0.00125	-0.00145	-0.00118	
	11	0.0006	0.0011	-0.0006	0.0005	0.0001	-0.0001	0.00006	0.00032	0.00043	
	12	0.0003	-0.0002	-0.0008	0.0005	0.0001	0.0003	0.00113	-0.00076	-0.00126	
	13	0.0006	0.0009	0.0013	-0.0006	0.0001	-0.0007	-0.00102	0.00193	-0.00057	
	14	-0.0002	-0.0002	0.0019	0.0002	-0.0009	-0.0006	-0.00002	-0.00068	-0.00026	
	15	0.0001	0.0008	-0.0007	0.0001	0.0010	0.0001	-0.00117	0.00055	-0.00042	
	16	-0.0003	-0.0002	0.0007	-0.0010	-0.0004	0.0007	-0.00002	-0.00107	0.00104	
	71	-0.0007	-0.0005	-0.0001	0.0010	0.0000	-0.0007	-0.00209	0.00047	-0.00126	
	72	-0.0005	0.0001	0.0001	0.0001	-0.0009	-0.0013	-0.00033	-0.00091	-0.00011	
	73	-0.0018	-0.0001	-0.0012	-0.0006	0.0008	-0.0006	-0.00094	-0.00030	-0.00034	
	74	0.0008	-0.1216	0.0007	0.0011	0.0003	-0.0004	-0.00179	-0.00207	0.00050	
	75	0.0001	0.0015	0.0011	-0.0009	-0.0002	0.0011	-0.00002	-0.00007	-0.00156	
	76	0.0000	-0.0009	-0.0006	0.0000	-0.0007	-0.0009	0.00059	0.00016	-0.00034	
	Min	-0.0018	-0.1216	-0.0012	-0.0010	-0.0009	-0.0013	-0.00209	-0.00207	-0.00156	
	Max	0.0008	0.0015	0.0019	0.0011	0.0010	0.0011	0.00113	0.00193	0.00104	
	Mean	-0.0001	-0.0099	0.0002	0.0000	-0.0001	-0.0002	-0.00047	-0.00020	-0.00035	
	Std. Dev	0.0007	0.0352	0.0010	0.0007	0.0006	0.0007	0.00095	0.00101	0.00077	
Mean - 3 Sigma	-0.0022	-0.1154	-0.0028	-0.0020	-0.0019	-0.0023	-0.00333	-0.00324	-0.00265		
Mean + 3 Sigma	0.0020	0.0956	0.0031	0.0021	0.0017	0.0018	0.00239	0.00284	0.00196		
Limit:±/10uA				Limit:±/10uA				Limit:±/10uA			

Wafer #	SN	ChA Input Leakage @ Vdd1 5V, 5V			ChB Input Leakage @ Vdd1 5V, 5V			ChC Input Leakage @ Vdd2 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0055	0.0057	0.0059	0.0024	0.0031	0.0034	-0.00017	0.00006	0.00066	
	68	0.0057	0.0060	0.0062	0.0050	0.0033	0.0051	0.00160	0.00044	0.00028	
	11	0.0056	0.0051	0.0044	0.0057	0.0032	0.0041	0.00006	0.00129	0.00158	
	12	0.0065	0.0053	0.0057	0.0052	0.0050	0.0044	0.00190	0.00160	0.00066	
	13	0.0092	0.0070	0.0041	0.0075	0.0041	0.0040	0.00021	0.00021	-0.00003	
	14	0.0057	0.0062	0.0069	0.0059	0.0037	0.0069	0.00152	-0.00002	0.00112	
	15	0.0051	0.0044	0.0046	0.0036	0.0042	0.0037	0.00106	-0.00056	0.00082	
	16	0.0070	0.0034	0.0053	0.0041	0.0032	0.0034	-0.00071	0.00168	0.00158	
	71	0.0074	0.0067	0.0051	0.0052	0.0064	0.0033	0.00021	0.00068	0.00120	
	72	0.0058	0.0048	0.0039	0.0037	0.0031	0.0028	0.00144	0.00121	0.00143	
	73	0.0062	0.0051	0.0043	0.0040	0.0033	0.0022	0.00098	0.00006	0.00089	
	74	0.0059	0.0044	0.0051	0.0056	0.0021	0.0040	0.00136	-0.00002	0.00120	
	75	0.0045	0.0044	0.0050	0.0052	0.0022	0.0036	0.00044	0.00121	0.00020	
	76	0.0080	0.0073	0.0062	0.0069	0.0054	0.0049	0.00113	0.00221	0.00120	
	Min	0.0045	0.0034	0.0039	0.0036	0.0021	0.0022	-0.00071	-0.00056	-0.00003	
	Max	0.0092	0.0073	0.0069	0.0075	0.0064	0.0069	0.00190	0.00221	0.00158	
	Mean	0.0064	0.0053	0.0051	0.0052	0.0038	0.0039	0.00080	0.00080	0.00099	
	Std. Dev	0.0013	0.0012	0.0009	0.0012	0.0013	0.0012	0.00076	0.00086	0.00051	
	Mean - 3 Sigma	0.0025	0.0017	0.0023	0.0016	0.0004	0.0004	-0.00147	-0.00177	-0.00053	
Mean + 3 Sigma	0.0103	0.0089	0.0078	0.0089	0.0076	0.0074	0.00307	0.00336	0.00251		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChD Input Leakage @ Vdd2 5V, 5V			Voh_A @ -20uA 5V, 5V			Voh_B @ -20uA 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0005	-0.0002	0.0003	4.9964	4.9939	4.9918	4.99619	4.99389	4.99406	
	68	0.0008	0.0012	0.0012	4.9971	4.9953	4.9919	4.99678	4.99577	4.99422	
	11	0.0016	0.0028	0.0004	4.9951	4.9938	4.9921	4.99503	4.99423	4.99433	
	12	0.0000	0.0029	0.0005	4.9973	4.9956	4.9918	4.99711	4.99547	4.99420	
	13	0.0001	0.0041	0.0013	4.9971	4.9929	4.9922	4.99705	4.99328	4.99458	
	14	0.0017	0.0012	0.0000	4.9971	4.9955	4.9919	4.99691	4.99543	4.99405	
	15	0.0012	0.0017	0.0012	4.9974	4.9952	4.9918	4.99709	4.99593	4.99431	
	16	0.0018	0.0032	-0.0001	4.9972	4.9960	4.9899	4.99714	4.99515	4.99221	
	71	-0.0007	0.0003	0.0003	4.9973	4.9956	4.9908	4.99711	4.99594	4.99304	
	72	0.0001	0.0019	0.0007	4.9975	4.9952	4.9919	4.99706	4.99550	4.99432	
	73	0.0002	0.0009	0.0009	4.9973	4.9953	4.9905	4.99686	4.99568	4.99276	
	74	0.0004	0.0002	0.0010	4.9971	4.9953	4.9915	4.99704	4.99571	4.99371	
	75	0.0011	0.0019	0.0007	4.9955	4.9956	4.9919	4.99553	4.99565	4.99440	
	76	-0.0007	-0.0007	0.0014	4.9966	4.9956	4.9919	4.99644	4.99524	4.99425	
	Min	-0.0007	-0.0007	-0.0001	4.9951	4.9929	4.9899	4.99503	4.99328	4.99221	
	Max	0.0018	0.0041	0.0014	4.9975	4.9960	4.9922	4.99714	4.99594	4.99458	
	Mean	0.0006	0.0017	0.0007	4.9969	4.9951	4.9915	4.99670	4.99527	4.99385	
	Std. Dev	0.0009	0.0014	0.0005	0.0008	0.0009	0.0007	0.00070	0.00077	0.00076	
	Mean - 3 Sigma	-0.0021	-0.0025	-0.0008	4.9946	4.9924	4.9893	4.99461	4.99295	4.99156	
Mean + 3 Sigma	0.0033	0.0059	0.0022	4.9992	4.9978	4.9937	4.99879	4.99759	4.99613		
Limit: +/-10uA				Limit: >4.9V				Limit: >4.9V			

Wafer #	SN	Voh_C @ -20uA 5V, 5V			Voh_D @ -20uA 5V, 5V			Voh_A @ -4mA 5V, 5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.9952	4.9953	4.9942	4.9953	4.9944	4.9939	4.78273	4.77615	4.78027	
	68	4.9962	4.9935	4.9941	4.9961	4.9925	4.9938	4.78426	4.78187	4.77699	
	11	4.9895	4.9935	4.9937	4.9893	4.9934	4.9935	4.76697	4.77390	4.77555	
	12	4.9964	4.9937	4.9940	4.9963	4.9930	4.9938	4.77082	4.77931	4.77337	
	13	4.9964	4.9938	4.9940	4.9962	4.9926	4.9938	4.78692	4.77019	4.77609	
	14	4.9961	4.9937	4.9939	4.9961	4.9927	4.9936	4.78608	4.78046	4.77449	
	15	4.9964	4.9938	4.9938	4.9962	4.9933	4.9939	4.78642	4.78071	4.77534	
	16	4.9963	4.9919	4.9940	4.9961	4.9911	4.9937	4.78636	4.78068	4.76634	
	71	4.9964	4.9921	4.9939	4.9960	4.9914	4.9936	4.78545	4.77693	4.76759	
	72	4.9962	4.9891	4.9942	4.9957	4.9885	4.9938	4.78483	4.77865	4.77418	
	73	4.9965	4.9943	4.9940	4.9963	4.9934	4.9939	4.78720	4.78165	4.77053	
	74	4.9963	4.9933	4.9938	4.9961	4.9928	4.9935	4.78642	4.77902	4.77293	
	75	4.9964	4.9934	4.9916	4.9961	4.9932	4.9915	4.78026	4.77856	4.77490	
	76	4.9964	4.9936	4.9936	4.9960	4.9934	4.9936	4.78333	4.77746	4.77393	
	Min	4.9895	4.9891	4.9916	4.9893	4.9885	4.9915	4.76697	4.77019	4.76634	
	Max	4.9965	4.9943	4.9942	4.9963	4.9934	4.9939	4.78720	4.78165	4.77609	
	Mean	4.9958	4.9930	4.9937	4.9955	4.9924	4.9935	4.78259	4.77813	4.77294	
	Std. Dev	0.0020	0.0014	0.0007	0.0020	0.0015	0.0006	0.00672	0.00325	0.00315	
	Mean - 3 Sigma	4.9898	4.9888	4.9917	4.9896	4.9880	4.9916	4.76241	4.76837	4.76347	
Mean + 3 Sigma	5.0017	4.9973	4.9957	5.0014	4.9968	4.9954	4.80276	4.78789	4.78240		
Limit: >4.9V				Limit: >4.9V				Limit: >4.6V			

Wafer #	SN	Voh_B @ -4mA 5V, 5V			V	Voh_C @ -4mA 5V, 5V			V	Voh_D @ -4mA 5V, 5V			V
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	4.7606	4.7708	4.7734	4.7786	4.7827	4.7804	4.77444	4.76115	4.77612			
	68	4.7764	4.7489	4.7405	4.7818	4.7764	4.7686	4.77813	4.76480	4.77472			
	11	4.7497	4.7676	4.7621	4.7412	4.7741	4.7568	4.74855	4.76467	4.77060			
	12	4.7610	4.7726	4.7715	4.7821	4.7745	4.7754	4.77566	4.76327	4.77225			
	13	4.7817	4.7572	4.7588	4.7820	4.7722	4.7745	4.77785	4.76795	4.77069			
	14	4.7620	4.7561	4.7591	4.7773	4.7754	4.7753	4.77888	4.76586	4.77122			
	15	4.7792	4.7591	4.7570	4.7788	4.7745	4.7751	4.77816	4.76876	4.77113			
	16	4.7799	4.7655	4.7459	4.7804	4.7677	4.7749	4.77187	4.76243	4.77107			
	71	4.7781	4.7497	4.7650	4.7810	4.7645	4.7731	4.77806	4.76105	4.76841			
	72	4.7692	4.7495	4.7539	4.7794	4.7541	4.7748	4.77628	4.74944	4.77197			
	73	4.7635	4.7574	4.7511	4.7828	4.7754	4.7751	4.77938	4.77104	4.77166			
	74	4.7680	4.7721	4.7617	4.7797	4.7716	4.7730	4.77650	4.76608	4.76476			
	75	4.7591	4.7549	4.7626	4.7806	4.7718	4.7610	4.77728	4.76308	4.75346			
	76	4.7691	4.7601	4.7547	4.7808	4.7737	4.7725	4.77725	4.76714	4.76576			
	Min	4.7497	4.7495	4.7459	4.7412	4.7541	4.7568	4.74855	4.74944	4.75346			
	Max	4.7817	4.7726	4.7715	4.7828	4.7754	4.7754	4.77938	4.77104	4.77225			
	Mean	4.7684	4.7602	4.7586	4.7772	4.7708	4.7718	4.77464	4.76423	4.76858			
	Std. Dev	0.0099	0.0078	0.0068	0.0114	0.0062	0.0062	0.00844	0.00548	0.00534			
Mean - 3 Sigma	4.7387	4.7368	4.7383	4.7429	4.7522	4.7533	4.74931	4.74780	4.75256				
Mean + 3 Sigma	4.7980	4.7835	4.7789	4.8115	4.7893	4.7903	4.79997	4.78066	4.78461				
Limit: >4.6V				Limit: >4.6V				Limit: >4.6V					

Wafer #	SN	Vol_CHA @ 20uA 5V, 5V			mV	Vol_CHB @ 20uA 5V, 5V			mV	Vol_CHC @ 20uA 5V, 5V			mV
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	6.8594	1.0335	0.4457	6.8131	1.0726	1.0800	2.35678	2.10250	2.14000			
	68	1.3943	1.2957	0.7330	1.4760	1.5785	1.3700	2.00068	2.57691	1.64000			
	11	2.7950	1.7826	0.8704	2.8634	1.7908	1.4400	1.98193	2.04632	2.03000			
	12	1.0629	1.9012	0.6393	1.0197	1.9595	1.2600	3.01901	2.27104	1.83000			
	13	0.8816	1.7077	0.8704	1.0322	1.7346	1.5100	2.70664	2.21486	1.59000			
	14	0.9754	1.2083	2.0900	1.1072	1.4099	2.6500	2.84408	2.09625	1.69000			
	15	0.9816	1.0959	0.4270	0.9947	1.2413	1.1600	2.51921	2.07129	1.76000			
	16	1.2130	1.0897	0.6081	1.2697	1.1039	1.0500	2.07565	2.18365	1.46000			
	71	1.1755	1.4268	0.4582	1.2135	1.4411	1.0400	1.83824	2.15244	1.65000			
	72	1.0067	1.2894	0.1709	0.9573	1.3474	0.9909	1.84449	2.09625	1.45000			
	73	0.9754	1.2145	0.3708	1.1135	1.3100	0.9472	1.92571	2.20237	1.56000			
	74	2.0009	1.2145	0.3021	2.1072	1.3037	1.0500	2.26307	2.41461	1.33000			
	75	1.1880	1.2083	0.4145	1.4197	1.3162	0.9160	1.91321	3.63811	1.46000			
	76	1.3068	1.7951	0.4769	1.3885	1.9969	1.1500	2.27556	2.50825	1.60000			
	Min	0.8816	1.0897	0.1709	0.9573	1.1039	0.9160	1.83824	2.04632	1.33000			
	Max	2.7950	1.9012	2.0900	2.8634	1.9969	2.6500	3.01901	3.63811	2.03000			
	Mean	1.2969	1.4112	0.6415	1.3739	1.4963	1.2637	2.26723	2.32462	1.61750			
	Std. Dev	0.5552	0.3001	0.5020	0.5636	0.2961	0.4743	0.41313	0.43614	0.19227			
Mean - 3 Sigma	-0.3688	0.5108	-0.8645	-0.3168	0.6078	-0.1591	1.02786	1.01620	1.04070				
Mean + 3 Sigma	2.9626	2.3115	2.1476	3.0646	2.3847	2.6864	3.50661	3.63304	2.19430				
Limit: <100mV				Limit: <100mV				Limit: <100mV					

Wafer #	SN	Vol_CHD @ 20uA 5V, 5V			mV	Vol_CHA @ 400uA 5V, 5V			mV	Vol_CHB @ 400uA 5V, 5V			mV
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	2.2936	1.9246	2.1200	24.1175	17.5257	17.0400	26.23676	18.15361	18.66000			
	68	1.7810	2.2304	1.5200	18.0271	18.0938	17.4500	19.08100	21.50111	24.02000			
	11	1.8748	1.6063	1.6600	20.6971	18.1312	17.3600	22.36828	19.07167	19.51000			
	12	2.9186	2.0931	1.5200	18.9275	18.2249	17.2000	19.88095	19.04669	18.29000			
	13	2.7186	1.7873	1.3700	17.4268	18.0001	17.4600	18.13732	19.49011	20.05000			
	14	2.6123	1.7062	1.5000	17.4519	17.5444	18.5800	19.96219	19.98349	20.95000			
	15	2.3498	1.7498	1.6800	17.5019	17.3010	16.6400	18.35605	19.64000	19.45000			
	16	1.9685	1.9308	1.3200	17.8520	17.0888	16.8500	18.48104	18.80312	19.83000			
	71	1.7810	1.6375	1.3500	17.7395	17.8191	16.9500	18.74353	20.62676	18.04000			
	72	1.8060	1.6125	1.3000	17.5832	17.5507	16.5500	19.28099	21.08891	19.52000			
	73	1.7560	1.7748	1.2500	17.4143	17.4134	16.5300	19.78721	19.93977	19.61000			
	74	2.0435	2.0869	1.3200	18.7212	17.4258	16.8600	20.37467	18.38469	18.83000			
	75	1.7935	3.3538	1.2800	17.6707	17.4695	16.6000	19.83095	19.99598	18.76000			
	76	2.1498	2.1118	1.3900	17.9646	18.4933	16.7100	19.38098	20.47687	19.42000			
	Min	1.7560	1.6063	1.2500	17.4143	17.0888	16.5300	18.13732	18.38469	18.04000			
	Max	2.9186	3.3538	1.6800	20.6971	18.4933	18.5800	22.36828	21.08891	20.95000			
	Mean	2.1477	1.9542	1.4117	18.0792	17.7052	17.0242	19.54868	19.71234	19.35500			
	Std. Dev	0.4073	0.4789	0.1452	0.9617	0.4247	0.5809	1.14227	0.79880	0.79059			
Mean - 3 Sigma	0.9260	0.5176	0.9760	15.1942	16.4310	15.2814	16.12187	17.31595	16.98324				
Mean + 3 Sigma	3.3695	3.3909	1.8473	20.9642	18.9794	18.7670	22.97549	22.10872	21.72676				
Limit: <100mV				Limit: <100mV				Limit: <100mV					

Wafer #	SN	Vol_CHC @ 400uA 5V, 5V			mV			Vol_CHD @ 400uA 5V, 5V			mV			Vol_CHA @ 4mA 5V, 5V			mV		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal			
CTRL	9	19.6809	19.1939	19.3900	20.0573	20.9286	19.9500	189.48862	174.26413	174.12000									
	68	19.4123	19.9555	20.2500	19.6385	20.7913	19.4500	176.28869	177.75358	176.54000									
	11	20.4494	18.9443	20.9400	19.6010	19.5680	19.4400	189.96383	173.49008	173.47000									
	12	20.6368	19.4062	18.9800	21.1386	20.0548	19.1000	190.10140	174.12679	173.57000									
	13	20.0745	19.3500	18.7100	20.5323	19.4120	19.0000	173.86255	174.09559	173.15000									
	14	20.6868	18.9630	18.9900	20.5198	19.5556	19.1600	174.02513	172.55998	175.82000									
	15	20.2994	19.2564	18.8900	20.2698	19.4245	19.1800	173.85004	171.51751	171.78000									
	16	19.7122	19.1066	18.7400	20.4010	19.4994	19.0500	174.96307	171.04308	172.43000									
	71	19.2374	19.3687	19.0300	19.5510	19.4807	19.3500	175.40078	174.59497	173.44000									
	72	19.3873	19.0566	18.6200	19.6697	19.2185	18.8800	175.73218	173.07809	172.29000									
	73	19.2124	19.2439	18.7300	19.4822	19.1936	18.8800	173.22475	171.23660	171.69000									
	74	19.7746	19.8806	18.7800	20.0760	20.0174	19.5200	176.95776	172.21041	172.74000									
	75	19.3498	20.9668	18.8500	19.4947	21.5527	19.6000	174.40656	171.90453	171.72000									
	76	19.8559	19.6996	19.0100	20.1510	20.0299	19.5000	175.54459	174.73854	172.23000									
	Min	19.2124	18.9443	18.6200	19.4822	19.1936	18.8800	173.22475	171.04308	171.69000									
	Max	20.6868	20.9668	20.9400	21.1386	21.5527	19.6000	190.10140	174.73854	175.82000									
	Mean	19.8897	19.4369	19.0225	20.0739	19.7506	19.2217	177.33605	172.88301	172.86083									
	Std. Dev	0.5379	0.5568	0.6187	0.5257	0.6404	0.2534	6.01801	1.32365	1.16050									
	Mean - 3 Sigma	18.2760	17.7665	17.1663	18.4969	17.8295	18.4616	159.28203	168.91207	169.37934									
	Mean + 3 Sigma	21.5034	21.1073	20.8787	21.6510	21.6717	19.9818	195.39007	176.85396	176.34232									
		Limit: <100mV			Limit: <100mV			Limit: <400mV											

Wafer #	SN	Vol_CHB @ 4mA 5V, 5V			mV			Vol_CHC @ 4mA 5V, 5V			mV			Vol_CHD @ 4mA 5V, 5V			mV		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal						
CTRL	9	211.3678	180.8320	185.8300	184.6321	183.4728	184.4200	189.34989	204.19079	189.83000									
	68	186.0883	211.6215	250.4500	185.1506	187.2931	195.3700	189.14363	198.48022	189.46000									
	11	207.2806	181.9561	192.1400	195.3777	180.9759	200.8400	188.29358	190.97848	187.65000									
	12	201.1435	183.1053	181.0400	187.3935	181.6063	183.4800	194.91276	192.40144	186.42000									
	13	180.3075	188.7011	195.7700	186.2127	182.3617	182.7100	190.87499	187.03414	186.45000									
	14	199.6999	198.0004	196.5500	190.2923	180.5826	181.9000	189.98744	189.12490	186.34000									
	15	183.2010	195.0401	195.6600	188.7866	181.8373	182.2900	189.92493	187.84547	187.06000									
	16	182.7635	187.8642	197.6900	186.2564	180.8760	182.0500	195.46281	186.12294	187.00000									
	71	184.1634	203.8460	180.8600	184.8945	183.9160	184.2300	188.57484	187.66448	188.60000									
	72	192.9566	209.7791	197.3800	185.7566	182.2181	182.5000	189.26863	186.05429	186.07000									
	73	197.9250	196.8762	194.7200	183.6887	180.5139	182.2500	188.14357	184.97460	186.17000									
	74	194.2440	180.5572	188.6500	186.7687	184.1969	183.8900	191.04376	189.96744	192.52000									
	75	196.3939	197.1385	188.3000	185.0194	186.1445	184.7900	188.90612	195.11005	193.38000									
	76	190.8192	194.7029	195.5000	186.4813	183.4291	183.7200	191.10002	189.94246	192.24000									
	Min	180.3075	180.5572	180.8600	183.6887	180.5139	181.9000	188.14357	184.97460	186.07000									
	Max	207.2806	209.7791	197.6900	195.3777	186.1445	200.8400	195.46281	195.11005	193.38000									
	Mean	192.5748	193.1306	192.0217	187.2440	182.3882	184.5542	190.54112	188.93506	188.32500									
	Std. Dev	8.5016	8.9582	6.0184	3.1045	1.7267	5.2141	2.40559	2.93483	2.74986									
	Mean - 3 Sigma	167.0702	166.2560	173.9666	177.9305	177.2080	168.9120	183.32436	180.13056	180.07541									
	Mean + 3 Sigma	218.0795	220.0052	210.0768	196.5576	187.5684	200.1964	197.75789	197.73956	196.57459									
		Limit: <400mV			Limit: <400mV			Limit: <400mV											

Wafer #	SN	Idd1 Quiescent 5.5V, 3V			mA			Idd2 Quiescent 5.5V, 3V			mA			Idd1 Dynamic 2MBPS 5.5V, 3V			mA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal						
CTRL	9	2.8658	2.8491	2.8516	1.6321	1.6260	1.6406	3.70754	3.68691	3.67458									
	68	2.8259	2.7226	2.7991	1.6606	1.6626	1.6797	3.71596	3.58238	3.66897									
	11	2.8438	2.7909	2.8455	1.6419	1.6516	1.6557	3.72955	3.61147	3.70241									
	12	2.8076	2.7804	2.8349	1.6223	1.6178	1.6341	3.66803	3.56297	3.66632									
	13	2.8088	2.7543	2.8353	1.6398	1.6679	1.6406	3.68939	3.56944	3.66359									
	14	2.8475	2.7771	2.8984	1.6244	1.6455	1.6480	3.70555	3.58612	3.67808									
	15	2.8292	2.7665	2.8337	1.6423	1.6964	1.6736	3.70367	3.59214	3.68247									
	16	2.8463	2.7938	2.8735	1.6284	1.6655	1.6577	3.70091	3.57876	3.66408									
	71	2.8145	2.7584	2.8280	1.5808	1.6777	1.6528	3.70559	3.57489	3.66123									
	72	2.8495	2.7999	2.8849	1.6541	1.6659	1.6577	3.72369	3.59300	3.68581									
	73	2.8467	2.8174	2.8837	1.6655	1.6964	1.6736	3.73269	3.60520	3.68939									
	74	2.8231	2.7905	2.8792	1.6785	1.6972	1.6614	3.73895	3.59312	3.70355									
	75	2.8524	2.7950	2.8821	1.6646	1.7171	1.6915	3.72951	3.60158	3.69472									
	76	2.8080	2.7743	2.8512	1.6484	1.6947	1.6614	3.72634	3.59483	3.67605									
	Min	2.8076	2.7543	2.8280	1.5808	1.6178	1.6341	3.66803	3.56297	3.66123									
	Max	2.8524	2.8174	2.8984	1.6785	1.7171	1.6915	3.73895	3.61147	3.70355									
	Mean	2.8315	2.7832	2.8609	1.6409	1.6745	1.6590	3.71282	3.58863	3.68064									
	Std. Dev	0.0181	0.0183	0.0251	0.0256	0.0278	0.0154	0.02094	0.01469	0.01501									
	Mean - 3 Sigma	2.7771	2.7284	2.7855	1.5642	1.5912	1.6127	3.65001	3.54456	3.63561									
	Mean + 3 Sigma	2.8859	2.8380	2.9362	1.7176	1.7578	1.7053	3.77564	3.63269	3.72567									
		Limit: <3.8mA			Limit: <2.3mA			Limit: <20mA											

Wafer #	SN	Idd2 Dynamic 2MBPS 5.5V, 3V			mA	Idd1 Dynamic 10MBPS 5.5V, 3V			mA	Idd2 Dynamic 10MBPS 5.5V, 3V			mA
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	2.2814	2.2642	2.2633	8.6349	8.6062	8.5251	5.34273	5.29508	5.31892			
	68	2.3070	2.2860	2.2781	8.8756	8.6521	8.7116	5.45296	5.35310	5.36144			
	11	2.2799	2.2625	2.2666	8.9080	8.5520	8.7308	5.36690	5.16125	5.27351			
	12	2.2725	2.2426	2.2488	8.6323	8.2917	8.4985	5.41239	5.18583	5.28047			
	13	2.2822	2.2533	2.2569	8.8377	8.4723	8.6241	5.43859	5.20202	5.31620			
	14	2.2648	2.2499	2.2455	8.6603	8.3442	8.5090	5.36645	5.18395	5.26647			
	15	2.3032	2.2709	2.2746	8.7772	8.4478	8.6278	5.47660	5.25337	5.36165			
	16	2.2889	2.2611	2.2696	8.7239	8.3723	8.5504	5.40250	5.18672	5.28246			
	71	2.2268	2.2828	2.2867	8.8485	8.4200	8.5769	5.34895	5.20910	5.29748			
	72	2.2857	2.2596	2.2556	8.8053	8.3996	8.5589	5.40311	5.20353	5.25683			
	73	2.3301	2.3063	2.3091	8.7978	8.3888	8.5553	5.52241	5.32914	5.38232			
	74	2.2923	2.2731	2.2703	8.9851	8.4910	8.7522	5.38574	5.19046	5.26204			
	75	2.3021	2.2764	2.2825	8.9244	8.4676	8.6780	5.34834	5.13350	5.21651			
	76	2.3026	2.2862	2.2972	8.9672	8.5811	8.7197	5.45902	5.26346	5.35912			
	Min	2.2268	2.2426	2.2455	8.6323	8.2917	8.4985	5.34834	5.13350	5.21651			
	Max	2.3301	2.3063	2.3091	8.9851	8.5811	8.7522	5.52241	5.32914	5.38232			
	Mean	2.2859	2.2687	2.2719	8.8223	8.4357	8.6151	5.41092	5.20853	5.29626			
	Std. Dev	0.0253	0.0177	0.0194	0.1133	0.0837	0.0877	0.05416	0.05172	0.04951			
	Mean - 3 Sigma	2.2101	2.2156	2.2139	8.4825	8.1845	8.3521	5.24844	5.05336	5.14773			
	Mean + 3 Sigma	2.3617	2.3219	2.3300	9.1622	8.6869	8.8781	5.57339	5.36370	5.44478			
Limit:<12mA				Limit:<20mA				Limit:<12mA					

Wafer #	SN	Idd1 Dynamic 25MBPS 5.5V, 3V			mA	Idd2 Dynamic 25MBPS 5.5V, 3V			mA	CHA Prop Delay Rise 5.5V, 3V			ns
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	17.3993	17.3336	17.1244	10.7722	10.7192	10.7673	44.75900	43.50700	43.50700			
	68	18.0410	17.6652	17.6811	11.1066	10.8740	10.9085	44.13300	44.13300	44.13300			
	11	18.0934	17.3364	17.6596	10.8277	10.3722	10.6456	44.75900	43.82000	44.13300			
	12	17.4482	16.7201	17.1215	11.0173	10.4566	10.7088	44.13300	43.82000	44.13300			
	13	17.9550	17.1563	17.4634	11.0991	10.4419	10.7377	44.13300	44.13300	44.13300			
	14	17.4863	16.8485	17.1440	10.9336	10.4339	10.6412	43.50700	43.19400	44.13300			
	15	17.8411	17.1306	17.4774	11.1917	10.6070	10.8743	44.13300	43.50700	44.13300			
	16	17.7109	16.8997	17.2371	11.0044	10.4201	10.6247	43.82000	42.88100	43.82000			
	71	17.9888	17.0578	17.3643	10.9417	10.4342	10.6506	44.13300	43.82000	44.44600			
	72	17.8426	16.9620	17.2661	11.0083	10.4565	10.5984	44.13300	43.50700	43.82000			
	73	17.7754	16.8820	17.1815	11.2597	10.7354	10.8703	43.50700	43.19400	43.82000			
	74	18.2719	17.1997	17.7091	11.0931	10.4648	10.6187	44.44600	43.82000	44.44600			
	75	18.1124	17.0897	17.5027	10.8285	10.2633	10.4856	44.13300	43.50700	43.82000			
	76	18.2201	17.3990	17.6621	11.1209	10.5877	10.8422	44.44600	44.13300	44.44600			
	Min	17.4482	16.7201	17.1215	10.8277	10.2633	10.4856	43.50700	42.88100	43.82000			
	Max	18.2719	17.3990	17.7091	11.2597	10.7354	10.8743	44.75900	44.13300	44.44600			
	Mean	17.8955	17.0568	17.3990	11.0139	10.4728	10.6915	44.10692	43.61133	44.10692			
	Std. Dev	0.2641	0.2030	0.2108	0.1341	0.1212	0.1197	0.36449	0.38528	0.24820			
	Mean - 3 Sigma	17.1032	16.4480	16.7667	10.6115	10.1093	10.3323	43.01345	42.45550	43.36233			
	Mean + 3 Sigma	18.6878	17.6657	18.0314	11.4163	10.8363	11.0507	45.20038	44.76716	44.85151			
Limit:<20mA				Limit:<12mA				Limit: 30<x<55ns					

Wafer #	SN	CHB Prop Delay Rise 5.5V, 3V			ns	CHC Prop Delay Rise 5.5V, 3V			ns	CHD Prop Delay Rise 5.5V, 3V			ns
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	45.0720	43.8200	43.5070	47.5760	47.2630	47.2630	47.57600	47.57600	47.57600			
	68	44.1330	44.1330	44.1330	47.5760	47.5760	47.5760	47.57600	47.57600	47.57600			
	11	44.7590	43.8200	44.1330	47.8890	46.9500	47.8890	47.88900	47.57600	47.88900			
	12	44.1330	43.8200	44.1330	48.2020	47.2630	47.8890	47.88900	47.57600	47.88900			
	13	44.1330	44.1330	44.1330	47.8890	47.5760	48.2020	47.88900	47.57600	48.20200			
	14	43.8200	43.1940	44.1330	47.5760	47.2630	47.8890	47.57600	47.26300	47.88900			
	15	44.1330	43.5070	44.1330	47.8890	47.5760	48.2020	47.88900	47.26300	48.20200			
	16	43.8200	43.1940	44.4460	47.2630	46.6370	47.5760	47.57600	47.26300	47.57600			
	71	43.8200	43.5070	44.1330	47.5760	47.2630	47.5760	47.57600	47.26300	47.88900			
	72	43.8200	43.5070	43.8200	47.5760	47.5760	47.8890	47.57600	47.26300	47.57600			
	73	43.8200	43.1940	43.8200	47.5760	46.9500	47.5760	47.26300	46.95000	47.57600			
	74	44.4460	43.8200	44.4460	47.5760	46.9500	47.5760	47.57600	46.95000	47.88900			
	75	44.1330	43.5070	43.8200	46.9500	46.6370	47.2630	46.95000	46.95000	47.26300			
	76	44.4460	44.1330	44.4460	48.2020	47.8890	48.5150	47.57600	47.26300	47.57600			
	Min	43.8200	43.1940	43.8200	46.9500	46.6370	47.2630	46.95000	46.95000	47.26300			
	Max	44.7590	44.1330	44.4460	48.2020	47.8890	48.5150	47.88900	47.57600	48.20200			
	Mean	44.1069	43.6113	44.1330	47.6803	47.2108	47.8368	47.60208	47.26300	47.78467			
	Std. Dev	0.3118	0.3359	0.2312	0.3614	0.3967	0.3489	0.28181	0.23117	0.27783			
	Mean - 3 Sigma	43.1715	42.6037	43.4395	46.5961	46.0208	46.7902	46.75667	46.56950	46.95119			
	Mean + 3 Sigma	45.0424	44.6190	44.8265	48.7646	48.4008	48.8835	48.44750	47.95650	48.61815			
Limit: 30<x<55ns				Limit: 30<x<55ns				Limit: 30<x<55ns					

Wafer #	SN	CHA Prop Delay Fall 5.5V, 3V			CHB Prop Delay Fall 5.5V, 3V			CHC Prop Delay Fall 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	43.1940	42.5680	42.8810	43.5070	43.1940	43.1940	48.51500	48.51500	48.51500	
	68	43.8200	43.8200	43.8200	43.8200	43.8200	43.8200	48.51500	48.82800	48.82800	
	11	43.8200	42.8810	43.5070	44.4460	43.1940	43.8200	49.14100	48.20200	49.14100	
	12	43.8200	42.8810	43.5070	43.8200	42.8810	43.5070	49.14100	48.51500	48.82800	
	13	43.8200	43.1940	43.5070	43.8200	43.1940	43.8200	49.14100	48.51500	49.45400	
	14	43.1940	42.5680	42.8810	43.1940	42.5680	43.1940	48.82800	48.20200	48.82800	
	15	43.8200	43.1940	43.5070	43.5070	42.8810	43.1940	49.14100	48.51500	49.14100	
	16	43.1940	42.5680	42.8810	43.5070	42.5680	43.1940	48.51500	47.88900	48.51500	
	71	44.1330	43.5070	43.8200	43.5070	43.1940	43.5070	48.82800	48.20200	48.82800	
	72	43.8200	43.1940	43.1940	43.5070	42.8810	43.1940	48.82800	48.20200	48.82800	
	73	43.1940	42.5680	42.8810	43.5070	42.5680	43.1940	48.51500	47.88900	48.51500	
	74	44.1330	43.5070	43.8200	44.1330	43.5070	43.8200	48.51500	47.88900	48.51500	
	75	43.5070	42.8810	43.5070	43.5070	42.8810	43.1940	48.20200	47.88900	48.51500	
	76	43.8200	43.1940	43.8200	44.1330	43.5070	43.8200	49.14100	48.82800	49.45400	
	Min	43.1940	42.5680	42.8810	43.1940	42.5680	43.1940	48.20200	47.88900	48.51500	
	Max	44.1330	43.5070	43.8200	44.4460	43.8200	43.8200	49.14100	48.82800	49.45400	
	Mean	43.6896	43.0114	43.4027	43.7157	42.9853	43.4548	48.82800	48.22808	48.88017	
	Std. Dev	0.3392	0.3392	0.3614	0.3614	0.3359	0.2934	0.32692	0.31181	0.34888	
Mean - 3 Sigma	42.6721	41.9939	42.3184	42.6314	41.9777	42.5746	47.84725	47.29265	47.83352		
Mean + 3 Sigma	44.7071	44.0289	44.4869	44.7999	43.9930	44.3351	49.80875	49.16352	49.92681		
Limit: 30<-x<-55ns				Limit: 30<-x<-55ns				Limit: 30<-x<-55ns			

Wafer #	SN	CHD Prop Delay Fall 5.5V, 3V			CHA PWD 5.5V, 3V			CHB PWD 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	50.0800	50.0800	49.7670	1.5650	0.9390	0.6260	1.56500	0.62600	0.31300	
	68	50.0800	50.0800	50.0800	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300	
	11	50.0800	49.1410	49.7670	0.9390	0.9390	0.6260	0.31300	0.62600	0.31300	
	12	49.7670	49.1410	49.7670	0.3130	0.9390	0.6260	0.31300	0.93900	0.62600	
	13	50.0800	49.4540	50.0800	0.3130	0.9390	0.6260	0.31300	0.93900	0.31300	
	14	49.7670	49.1410	50.0800	0.3130	0.6260	1.2520	0.62600	0.62600	0.93900	
	15	49.7670	48.8280	49.7670	0.3130	0.3130	0.6260	0.62600	0.62600	0.93900	
	16	50.0800	49.1410	49.7670	0.6260	0.3130	0.9390	0.31300	0.62600	1.25200	
	71	49.1410	48.5150	49.1410	0.0000	0.3130	0.6260	0.31300	0.31300	0.62600	
	72	50.0800	49.1410	49.7670	0.3130	0.3130	0.6260	0.31300	0.62600	0.62600	
	73	48.8280	48.2020	48.8280	0.3130	0.6260	0.9390	0.31300	0.62600	0.62600	
	74	49.7670	49.4540	50.0800	0.3130	0.3130	0.6260	0.31300	0.31300	0.62600	
	75	48.8280	48.5150	48.8280	0.6260	0.6260	0.3130	0.62600	0.62600	0.62600	
	76	49.7670	49.1410	49.7670	0.6260	0.9390	0.6260	0.31300	0.62600	0.62600	
	Min	48.8280	48.2020	48.8280	0.0000	0.3130	0.3130	0.31300	0.31300	0.31300	
	Max	50.0800	49.4540	50.0800	0.9390	0.9390	1.2520	0.62600	0.93900	1.25200	
	Mean	49.6627	48.9845	49.6366	0.4173	0.5999	0.7043	0.39125	0.62600	0.67817	
	Std. Dev	0.4687	0.3891	0.4518	0.2437	0.2818	0.2359	0.14156	0.18875	0.26131	
Mean - 3 Sigma	48.2565	47.8172	48.2813	-0.3137	-0.2455	-0.0035	-0.03343	0.05976	-0.10575		
Mean + 3 Sigma	51.0688	50.1518	50.9919	1.1483	1.4453	1.4120	0.81593	1.19224	1.46209		
Limit: 30<-x<-55ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	CHC PWD 5.5V, 3V			CHD PWD 5.5V, 3V			AB_Rise Match Codirection 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.9390	1.2520	1.2520	2.5040	2.5040	2.1910	0.31300	0.31300	0.00000	
	68	0.9390	1.2520	1.2520	2.5040	2.5040	2.5040	0.00000	0.00000	0.00000	
	11	1.2520	1.2520	1.2520	2.1910	1.5650	1.8780	0.00000	0.00000	0.00000	
	12	0.9390	1.2520	0.9390	1.8780	1.5650	1.8780	0.00000	0.00000	0.00000	
	13	1.2520	0.9390	1.2520	2.1910	1.8780	1.8780	0.00000	0.00000	0.00000	
	14	1.2520	0.9390	0.9390	2.1910	1.8780	2.1910	0.31300	0.00000	0.00000	
	15	1.2520	0.9390	0.9390	1.8780	1.5650	1.5650	0.00000	0.00000	0.00000	
	16	1.2520	1.2520	0.9390	2.5040	1.8780	2.1910	0.00000	0.31300	0.62600	
	71	1.2520	0.9390	1.2520	1.5650	1.2520	1.2520	0.31300	0.31300	0.31300	
	72	1.2520	0.6260	0.9390	2.5040	1.8780	2.1910	0.31300	0.00000	0.00000	
	73	0.9390	0.9390	0.9390	1.5650	1.2520	1.2520	0.31300	0.00000	0.00000	
	74	0.9390	0.9390	0.9390	2.1910	2.5040	2.1910	0.00000	0.00000	0.00000	
	75	1.2520	1.2520	1.2520	1.8780	1.5650	1.5650	0.00000	0.00000	0.00000	
	76	0.9390	0.9390	0.9390	2.1910	1.8780	2.1910	0.00000	0.00000	0.00000	
	Min	0.9390	0.6260	0.9390	1.5650	1.2520	1.2520	0.00000	0.00000	0.00000	
	Max	1.2520	1.2520	1.2520	2.5040	2.5040	2.1910	0.31300	0.31300	0.62600	
	Mean	1.1477	1.0173	1.0433	2.0606	1.7215	1.8519	0.10433	0.05217	0.07825	
	Std. Dev	0.1541	0.1946	0.1541	0.3118	0.3403	0.3645	0.15411	0.12184	0.19456	
Mean - 3 Sigma	0.6853	0.4336	0.5810	1.1251	0.7007	0.7585	-0.35800	-0.31334	-0.50542		
Mean + 3 Sigma	1.6100	1.6009	1.5057	2.9960	2.7423	2.9454	0.56666	0.41767	0.66192		
Limit: <5ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	AB_Fall Match Codirection 5.5V, 3V			CD_Rise Match Codirection 5.5V, 3V			CD_Fall Match Codirection 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.3130	0.6260	0.3130	0.0000	0.3130	0.3130	1.56500	1.56500	1.25200	
	68	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	1.56500	1.25200	1.25200	
	11	0.6260	0.3130	0.3130	0.0000	0.6260	0.0000	0.93900	0.93900	0.62600	
	12	0.0000	0.0000	0.0000	0.3130	0.3130	0.0000	0.62600	0.62600	0.93900	
	13	0.0000	0.0000	0.3130	0.0000	0.0000	0.0000	0.93900	0.93900	0.62600	
	14	0.0000	0.0000	0.3130	0.0000	0.0000	0.0000	0.93900	0.93900	1.25200	
	15	0.3130	0.3130	0.3130	0.0000	0.3130	0.0000	0.62600	0.31300	0.62600	
	16	0.3130	0.0000	0.3130	0.3130	0.3130	0.0000	1.56500	1.25200	1.25200	
	71	0.6260	0.3130	0.3130	0.0000	0.0000	0.3130	0.31300	0.31300	0.31300	
	72	0.3130	0.3130	0.0000	0.0000	0.3130	0.3130	1.25200	0.93900	0.93900	
	73	0.3130	0.0000	0.3130	0.3130	0.0000	0.0000	0.31300	0.31300	0.31300	
	74	0.0000	0.0000	0.0000	0.0000	0.0000	0.3130	1.25200	1.56500	1.56500	
	75	0.0000	0.0000	0.3130	0.0000	0.3130	0.0000	0.62600	0.62600	0.31300	
	76	0.3130	0.3130	0.0000	0.0000	0.6260	0.9390	0.62600	0.31300	0.31300	
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.31300	0.31300	0.31300	
	Max	0.6260	0.3130	0.3130	0.6260	0.6260	0.9390	1.56500	1.56500	1.56500	
	Mean	0.2348	0.1304	0.2087	0.1304	0.2608	0.1565	0.83467	0.75642	0.75642	
	Std. Dev	0.2359	0.1612	0.1541	0.2093	0.2613	0.2831	0.38528	0.41046	0.43161	
Mean - 3 Sigma	-0.4730	-0.3531	-0.2537	-0.4974	-0.5231	-0.6929	-0.32116	-0.47496	-0.53842		
Mean + 3 Sigma	0.9425	0.6139	0.6710	0.7582	1.0448	1.0059	1.99050	1.98780	2.05125		
Limit: <5ns				Limit: <5ns				Limit: <5ns			

Wafer #	SN	AB_C Rise Match Opposing 5.5V, 3V			AB_C Fall Match Opposing 5.5V, 3V			AB_D Rise Match Opposing 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.8170	3.7560	3.7560	5.3210	5.9470	5.6340	2.81700	4.06900	4.06900	
	68	3.4430	3.4430	3.4430	4.6950	5.0080	5.0080	3.44300	3.44300	3.44300	
	11	3.1300	3.1300	3.7560	5.3210	5.3210	5.6340	3.13000	3.75600	3.75600	
	12	4.0690	3.4430	3.7560	5.3210	5.6340	5.3210	3.75600	3.75600	3.75600	
	13	3.7560	3.4430	4.0690	5.3210	5.3210	5.9470	3.75600	3.44300	4.06900	
	14	4.0690	4.0690	3.7560	5.6340	5.6340	5.9470	4.06900	4.06900	3.75600	
	15	3.7560	4.0690	4.0690	5.6340	5.6340	5.9470	3.75600	3.75600	4.06900	
	16	3.4430	3.7560	3.7560	5.3210	5.3210	5.6340	3.75600	4.38200	3.75600	
	71	3.7560	3.7560	3.4430	5.3210	5.0080	5.3210	3.75600	3.75600	3.75600	
	72	3.7560	4.0690	4.0690	5.3210	5.3210	5.6340	3.75600	3.75600	3.75600	
	73	4.0690	3.7560	3.7560	5.3210	5.3210	5.6340	3.75600	3.75600	3.75600	
	74	3.1300	3.1300	3.1300	4.3820	4.3820	4.6950	3.13000	3.13000	3.44300	
	75	2.8170	3.1300	3.4430	4.6950	5.0080	5.3210	2.81700	3.44300	3.44300	
	76	3.7560	3.7560	4.0690	5.3210	5.6340	5.6340	3.13000	3.13000	3.13000	
	Min	2.8170	3.1300	3.1300	4.3820	4.3820	4.6950	2.81700	3.13000	3.13000	
	Max	4.0690	4.0690	4.0690	5.6340	5.6340	5.9470	4.06900	4.38200	4.06900	
	Mean	3.6256	3.6256	3.7560	5.2428	5.2949	5.5558	3.54733	3.67775	3.70383	
	Std. Dev	0.4105	0.3645	0.2984	0.3563	0.3645	0.3563	0.38528	0.35625	0.26131	
Mean - 3 Sigma	2.3942	2.5321	2.8607	4.1740	4.2015	4.4870	2.39150	2.60900	2.91991		
Mean + 3 Sigma	4.8570	4.7190	4.6513	6.3115	6.3884	6.6245	4.70316	4.74650	4.48775		
Limit: <9ns				Limit: <9ns				Limit: <9ns			

Wafer #	SN	AB_D Fall Match Opposing 5.5V, 3V			ChA Input Leakage @ 0V 5.5V, 3V			ChB Input Leakage @ 0V 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	6.8860	7.5120	6.8860	-0.0002	-0.0012	0.0005	-0.00044	-0.00132	-0.00086	
	68	6.2600	6.2600	6.2600	0.0005	-0.0017	-0.0005	-0.00090	-0.00032	-0.00010	
	11	6.2600	6.2600	6.2600	0.0004	-0.0005	-0.0006	-0.00167	0.00045	0.00105	
	12	5.9470	6.2600	6.2600	0.0001	-0.0017	-0.0006	-0.00067	0.00083	0.00075	
	13	6.2600	6.2600	6.5730	0.0016	-0.0003	-0.0002	-0.00067	-0.00109	-0.00048	
	14	6.5730	6.5730	7.1990	-0.0001	-0.0008	0.0004	-0.00083	-0.00063	-0.00109	
	15	6.2600	5.9470	6.5730	0.0005	-0.0006	0.0005	-0.00036	-0.00055	-0.00056	
	16	6.8860	6.5730	6.8860	0.0008	0.0007	-0.0003	-0.00044	0.00083	-0.00056	
	71	5.6340	5.3210	5.6340	-0.0017	-0.0003	0.0007	-0.00029	-0.00086	-0.00133	
	72	6.5730	6.2600	6.5730	0.0008	0.0000	-0.0011	0.00025	-0.00063	-0.00109	
	73	5.6340	5.6340	5.9470	-0.0011	0.0000	-0.0019	-0.00121	-0.00070	-0.00071	
	74	5.6340	5.9470	6.2600	-0.0022	-0.6991	-0.0008	-0.00036	-0.40740	-0.00010	
	75	5.3210	5.6340	5.6340	-0.0017	0.0002	0.0000	0.00025	0.00083	-0.00017	
	76	5.9470	5.9470	5.9470	-0.0015	0.0004	0.0014	-0.00021	-0.00124	-0.00056	
	Min	5.3210	5.3210	5.6340	-0.0022	-0.6991	-0.0019	-0.00167	-0.40740	-0.00133	
	Max	6.8860	6.5730	7.1990	0.0016	0.0007	0.0014	0.00025	0.00083	0.00105	
	Mean	6.0774	6.0513	6.3122	-0.0004	-0.0585	-0.0002	-0.00052	-0.03418	-0.00040	
	Std. Dev	0.4711	0.3853	0.4781	0.0012	0.2017	0.0009	0.00055	0.11754	0.00071	
Mean - 3 Sigma	4.6642	4.8955	4.8778	-0.0041	-0.6637	-0.0029	-0.00217	-0.38679	-0.00254		
Mean + 3 Sigma	7.4907	7.2072	7.7465	0.0034	0.5467	0.0024	0.00113	0.31843	0.00173		
Limit: <9ns				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChC Input Leakage @ 0V 5.5V, 3V			ChD Input Leakage @ 0V 5.5V, 3V			ChA Input Leakage @ Vdd1 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	-0.0003	0.0005	0.0006	0.0002	0.0002	0.0007	0.02020	0.03355	0.02076	
	68	0.0002	-0.0006	0.0003	-0.0003	-0.0001	0.0011	0.04516	0.02045	0.03826	
	11	0.0002	-0.0002	0.0006	-0.0005	0.0010	-0.0008	0.03556	0.03340	0.02168	
	12	-0.0012	-0.0009	-0.0001	0.0003	0.0005	0.0004	0.02772	0.01923	0.02886	
	13	-0.0007	0.0020	0.0009	0.0011	-0.0004	-0.0003	0.03863	0.03968	0.02924	
	14	-0.0008	-0.0006	0.0003	0.0011	-0.0007	0.0000	0.03710	0.03110	0.03191	
	15	-0.0002	0.0007	-0.0003	0.0004	0.0002	-0.0001	0.02150	0.02099	0.01992	
	16	-0.0005	-0.0011	0.0000	0.0004	-0.0010	0.0006	0.00791	0.02505	0.03566	
	71	0.0007	-0.0001	0.0002	0.0007	0.0012	-0.0012	0.02265	0.01616	0.01144	
	72	-0.0008	0.0014	-0.0013	0.0002	0.0006	-0.0002	0.01997	0.01838	0.01640	
	73	-0.0006	-0.0002	0.0007	0.0011	-0.0005	0.0002	0.04416	0.01992	0.02504	
	74	-0.0019	-0.0006	0.0008	0.0011	0.0012	0.0014	0.03364	0.00498	0.03596	
	75	0.0002	0.0008	0.0005	-0.0008	-0.0009	-0.0003	0.04616	0.00973	0.03069	
	76	0.0006	-0.0003	0.0001	-0.0015	0.0014	0.0000	0.05269	0.04719	0.04559	
	Min	-0.0019	-0.0011	-0.0013	-0.0015	-0.0010	-0.0012	0.00791	0.00498	0.01144	
	Max	0.0007	0.0020	0.0009	0.0011	0.0014	0.0014	0.05269	0.04719	0.04559	
	Mean	-0.0004	0.0001	0.0002	0.0003	0.0002	0.0000	0.03231	0.02382	0.02770	
	Std. Dev	0.0008	0.0009	0.0006	0.0008	0.0009	0.0007	0.01280	0.01217	0.00943	
	Mean - 3 Sigma	-0.0027	-0.0028	-0.0016	-0.0022	-0.0024	-0.0020	-0.00609	-0.01270	-0.00059	
Mean + 3 Sigma	0.0019	0.0029	0.0020	0.0028	0.0028	0.0020	0.07070	0.06034	0.05599		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChB Input Leakage @ Vdd1 5.5V, 3V			ChC Input Leakage @ Vdd2 5.5V, 3V			ChD Input Leakage @ Vdd2 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0185	0.0280	0.0179	0.0013	0.0004	0.0001	0.00067	0.00063	0.00043	
	68	0.0405	0.0168	0.0297	0.0018	-0.0003	0.0009	-0.00033	0.00093	0.00004	
	11	0.0309	0.0270	0.0181	0.0002	0.0012	0.0004	-0.00148	-0.00030	0.00134	
	12	0.0240	0.0175	0.0225	0.0007	0.0000	0.0015	0.00013	0.00093	0.00073	
	13	0.0323	0.0356	0.0262	0.0021	0.0013	0.0009	-0.00017	0.00009	0.00020	
	14	0.0298	0.0236	0.0258	0.0007	0.0000	0.0010	0.00266	0.00155	0.00111	
	15	0.0174	0.0152	0.0143	0.0019	-0.0001	0.0021	-0.00017	0.00139	0.00035	
	16	0.0049	0.0197	0.0305	-0.0001	-0.0009	-0.0001	0.00044	0.00001	-0.00095	
	71	0.0174	0.0109	0.0084	0.0004	0.0015	0.0005	0.00174	0.00124	-0.00049	
	72	0.0158	0.0144	0.0123	0.0012	0.0013	-0.0002	0.00067	0.00055	0.00096	
	73	0.0357	0.0162	0.0209	0.0010	0.0008	-0.0003	0.00036	0.00239	0.00111	
	74	0.0327	0.0039	0.0280	0.0014	0.0004	0.0012	0.00098	0.00109	0.00150	
	75	0.0425	0.0094	0.0231	0.0018	0.0003	0.0004	0.00105	-0.00014	-0.00011	
	76	0.0467	0.0394	0.0392	-0.0015	0.0014	0.0011	0.00167	-0.00014	0.00066	
	Min	0.0049	0.0039	0.0084	-0.0015	-0.0009	-0.0003	-0.00148	-0.00030	-0.00095	
	Max	0.0467	0.0394	0.0392	0.0021	0.0015	0.0021	0.00266	0.00239	0.00150	
	Mean	0.0275	0.0194	0.0224	0.0008	0.0006	0.0007	0.00066	0.00072	0.00053	
	Std. Dev	0.0120	0.0105	0.0085	0.0010	0.0008	0.0007	0.00108	0.00084	0.00076	
	Mean - 3 Sigma	-0.0086	-0.0120	-0.0030	-0.0022	-0.0017	-0.0014	-0.00258	-0.00181	-0.00174	
Mean + 3 Sigma	0.0636	0.0508	0.0479	0.0038	0.0028	0.0029	0.00390	0.00325	0.00280		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	Voh_A @ -20uA 5.5V, 3V			Voh_B @ -20uA 5.5V, 3V			Voh_C @ -20uA 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.9978	2.9960	2.9924	2.9972	2.9959	2.9949	5.49441	5.49375	5.49342	
	68	2.9977	2.9967	2.9923	2.9979	2.9971	2.9946	5.49547	5.49200	5.49334	
	11	2.9968	2.9961	2.9927	2.9965	2.9962	2.9951	5.48837	5.49227	5.49319	
	12	2.9980	2.9965	2.9925	2.9978	2.9973	2.9949	5.49560	5.49193	5.49323	
	13	2.9981	2.9957	2.9928	2.9980	2.9954	2.9951	5.49552	5.49280	5.49341	
	14	2.9977	2.9965	2.9926	2.9978	2.9967	2.9947	5.49556	5.49242	5.49340	
	15	2.9979	2.9971	2.9925	2.9978	2.9965	2.9949	5.49559	5.49305	5.49322	
	16	2.9980	2.9969	2.9914	2.9977	2.9973	2.9938	5.49547	5.49028	5.49351	
	71	2.9980	2.9966	2.9918	2.9978	2.9971	2.9943	5.49539	5.49122	5.49330	
	72	2.9979	2.9966	2.9925	2.9976	2.9963	2.9950	5.49520	5.48736	5.49347	
	73	2.9978	2.9965	2.9917	2.9978	2.9972	2.9941	5.49561	5.49223	5.49326	
	74	2.9978	2.9963	2.9924	2.9977	2.9964	2.9948	5.49550	5.49214	5.49352	
	75	2.9971	2.9972	2.9924	2.9967	2.9964	2.9949	5.49545	5.49217	5.49081	
	76	2.9975	2.9962	2.9923	2.9974	2.9965	2.9949	5.49554	5.49230	5.49302	
	Min	2.9968	2.9957	2.9914	2.9965	2.9954	2.9938	5.48837	5.48736	5.49081	
	Max	2.9981	2.9972	2.9928	2.9980	2.9973	2.9951	5.49561	5.49305	5.49352	
	Mean	2.9977	2.9965	2.9923	2.9976	2.9966	2.9947	5.49490	5.49168	5.49311	
	Std. Dev	0.0004	0.0004	0.0004	0.0005	0.0006	0.0004	0.00206	0.00154	0.00074	
	Mean - 3 Sigma	2.9965	2.9953	2.9910	2.9962	2.9949	2.9935	5.48872	5.48706	5.49089	
Mean + 3 Sigma	2.9989	2.9978	2.9935	2.9989	2.9982	2.9959	5.50108	5.49630	5.49533		
Limit: >2.9V				Limit: >2.9V				Limit: >5.4V			

Wafer #	SN	Voh_D @ -20uA 5.5V, 3V			Voh_A @ -4mA 5.5V, 3V			Voh_B @ -4mA 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.4943	5.4936	5.4930	2.7078	2.7018	2.7043	2.67525	2.69643	2.69800	
	68	5.4953	5.4913	5.4932	2.7073	2.7048	2.7001	2.69958	2.67295	2.62789	
	11	5.4884	5.4921	5.4933	2.6917	2.6906	2.6906	2.67491	2.68528	2.67839	
	12	5.4954	5.4917	5.4931	2.6944	2.6945	2.6876	2.68257	2.68866	2.68523	
	13	5.4954	5.4914	5.4932	2.7114	2.6863	2.6902	2.70718	2.67417	2.67367	
	14	5.4953	5.4923	5.4930	2.7109	2.6966	2.6897	2.68711	2.67326	2.67430	
	15	5.4954	5.4918	5.4929	2.7104	2.6972	2.6904	2.70346	2.67673	2.67220	
	16	5.4953	5.4898	5.4932	2.7114	2.6969	2.6821	2.70434	2.68141	2.66093	
	71	5.4952	5.4897	5.4928	2.7082	2.6886	2.6781	2.70143	2.66255	2.67601	
	72	5.4951	5.4865	5.4930	2.7090	2.6943	2.6878	2.69299	2.65174	2.66743	
	73	5.4952	5.4917	5.4931	2.7108	2.6968	2.6829	2.68786	2.67354	2.66571	
	74	5.4953	5.4914	5.4930	2.7106	2.6920	2.6830	2.69164	2.68538	2.67224	
	75	5.4953	5.4911	5.4905	2.7046	2.6915	2.6851	2.68360	2.66726	2.67352	
	76	5.4955	5.4917	5.4929	2.7058	2.6895	2.6836	2.69267	2.67282	2.66705	
	Min	5.4884	5.4865	5.4905	2.6917	2.6863	2.6781	2.67491	2.65174	2.66093	
	Max	5.4955	5.4923	5.4933	2.7114	2.6972	2.6906	2.70718	2.68866	2.68523	
	Mean	5.4947	5.4909	5.4928	2.7066	2.6929	2.6859	2.69248	2.67440	2.67222	
	Std. Dev	0.0020	0.0016	0.0007	0.0067	0.0037	0.0041	0.00997	0.01047	0.00639	
	Mean - 3 Sigma	5.4888	5.4861	5.4906	2.6864	2.6818	2.6738	2.66256	2.64298	2.65305	
Mean + 3 Sigma	5.5007	5.4958	5.4951	2.7267	2.7040	2.6981	2.72240	2.70582	2.69140		
Limit: >5.4V				Limit: >2.6V				Limit: >2.6V			

Wafer #	SN	Voh_C @ -4mA 5.5V, 3V			Voh_D @ -4mA 5.5V, 3V			Vol_CHA @ 20uA 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.2856	5.2884	5.2882	5.2812	5.2676	5.2832	4.60208	1.00229	0.34578	
	68	5.2888	5.2832	5.2767	5.2847	5.2711	5.2814	1.26302	1.23326	0.38325	
	11	5.2485	5.2811	5.2642	5.2562	5.2684	5.2779	2.11342	1.53289	0.60808	
	12	5.2895	5.2808	5.2822	5.2826	5.2699	5.2798	1.05042	1.47047	0.49566	
	13	5.2889	5.2792	5.2816	5.2848	5.2740	5.2792	0.96288	1.28320	0.60808	
	14	5.2847	5.2812	5.2832	5.2854	5.2725	5.2783	0.90035	1.13338	1.26000	
	15	5.2858	5.2812	5.2829	5.2850	5.2756	5.2789	0.86909	0.95860	0.22712	
	16	5.2874	5.2752	5.2821	5.2787	5.2694	5.2782	1.16297	0.99605	0.33953	
	71	5.2882	5.2714	5.2806	5.2848	5.2676	5.2765	1.10670	1.13338	0.40199	
	72	5.2866	5.2617	5.2819	5.2836	5.2571	5.2796	0.96288	1.12714	0.21463	
	73	5.2896	5.2827	5.2829	5.2860	5.2778	5.2792	0.90035	1.04599	0.37700	
	74	5.2867	5.2784	5.2807	5.2833	5.2730	5.2728	1.68822	1.11465	0.29582	
	75	5.2876	5.2789	5.2690	5.2845	5.2701	5.2609	1.14422	0.94611	0.23961	
	76	5.2876	5.2803	5.2808	5.2834	5.2739	5.2733	1.16923	1.57034	0.38950	
	Min	5.2485	5.2617	5.2642	5.2562	5.2571	5.2609	0.86909	0.94611	0.21463	
	Max	5.2896	5.2827	5.2832	5.2860	5.2778	5.2798	2.11342	1.57034	1.26000	
	Mean	5.2843	5.2777	5.2793	5.2815	5.2708	5.2762	1.16923	1.19268	0.45475	
	Std. Dev	0.0114	0.0059	0.0061	0.0082	0.0053	0.0054	0.36920	0.22093	0.28675	
	Mean - 3 Sigma	5.2502	5.2599	5.2610	5.2569	5.2549	5.2602	0.06164	0.52989	-0.40551	
Mean + 3 Sigma	5.3183	5.2954	5.2977	5.3061	5.2858	5.2923	2.27682	1.85548	1.31501		
Limit: >5.1V				Limit: >5.1V				Limit: <100mV			

Wafer #	SN	Vol_CHB @ 20uA 5.5V, 3V			Vol_CHC @ 20uA 5.5V, 3V			Vol_CHD @ 20uA 5.5V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.7132	0.9727	0.7849	2.5505	2.0588	2.2300	2.44357	1.89963	2.14000	
	68	1.3322	1.2038	1.1900	2.1319	2.7392	1.7700	1.85603	2.34898	1.56000	
	11	2.2697	1.5285	1.3100	2.2131	2.1587	2.0400	1.97479	1.86218	1.91000	
	12	1.0197	1.5161	0.9847	3.2502	2.4521	1.8600	3.09361	2.13679	1.69000	
	13	0.9822	1.3787	1.3600	2.8628	2.3023	1.7100	2.81234	1.99948	1.49000	
	14	1.0510	1.1226	1.9600	2.9503	2.2523	1.9300	2.78109	1.89963	1.64000	
	15	0.9698	1.0414	0.9722	2.7879	2.2461	1.6900	2.61858	1.97452	1.71000	
	16	1.2385	0.8728	0.9784	2.3006	2.3647	1.5800	2.15605	1.89339	1.55000	
	71	1.1322	1.3724	0.9347	1.9382	2.2523	1.5900	1.86228	1.86842	1.52000	
	72	1.0260	1.0914	0.8473	2.1256	2.1150	1.6200	1.88103	1.75608	1.49000	
	73	1.1260	1.2288	0.9223	2.0444	2.2898	1.6300	1.83103	1.99324	1.50000	
	74	1.7822	1.0164	0.8973	2.3505	2.6643	1.7600	2.17480	2.29906	1.38000	
	75	1.2322	1.2912	0.7912	2.0694	3.9690	1.4900	1.83103	3.54727	1.37000	
	76	1.3197	1.5223	1.1000	2.5942	2.7392	1.6100	2.34981	2.26161	1.60000	
	Min	0.9698	0.8728	0.7912	1.9382	2.1150	1.4900	1.83103	1.75608	1.37000	
	Max	2.2697	1.5285	1.9600	3.2502	3.9690	2.0400	3.09361	3.54727	1.91000	
	Mean	1.2624	1.2485	1.0882	2.4573	2.4838	1.7092	2.28054	2.12431	1.57083	
	Std. Dev	0.3871	0.2211	0.3238	0.4221	0.5040	0.1615	0.44439	0.47703	0.15139	
	Mean - 3 Sigma	0.1010	0.5851	0.1168	1.1909	0.9717	1.2247	0.94736	0.69321	1.11668	
Mean + 3 Sigma	2.4239	1.9120	2.0595	3.7237	3.9959	2.1937	3.61372	3.55540	2.02499		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHA @ 400uA 5.5V, 3V			Vol_CHB @ 400uA 5.5V, 3V			Vol_CHC @ 400uA 5.5V, 3V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	24.8616	20.3847	19.7500	27.8679	20.8641	21.2000	19.51225	19.15648	19.40000
	68	20.8472	20.8716	20.2400	21.7746	24.2865	27.8800	19.19988	19.76199	19.98000
	11	22.8231	20.5720	20.0100	24.6431	21.3700	22.1900	20.18073	18.83813	20.66000
	12	21.8789	20.5220	19.8400	23.3682	21.5074	20.9600	20.53058	19.33751	18.82000
	13	20.3032	20.5033	19.9000	20.9934	21.9633	22.5400	20.07452	19.20018	18.65000
	14	20.2657	20.2474	20.6300	22.8620	22.6877	23.2200	20.59306	18.79443	18.75000
	15	20.2532	20.1475	19.6300	21.2184	22.3317	22.3400	20.00580	19.01915	18.67000
	16	20.4595	19.9040	19.5900	21.4012	22.5600	22.5600	19.43105	19.10030	18.55000
	71	20.6721	20.4908	19.8500	21.5183	23.2061	21.0200	19.05619	19.26885	18.80000
	72	20.4157	20.2973	19.3100	22.1995	24.9111	22.3100	19.16239	18.85061	18.62000
	73	20.1969	20.0102	19.3700	22.7808	22.5316	22.3100	19.06244	19.04412	18.45000
	74	21.0723	20.1350	19.5200	23.1370	20.8578	21.5000	19.52475	19.68084	18.67000
	75	20.4908	20.1225	19.4400	22.6308	22.4629	21.5000	19.09367	20.91057	18.61000
	76	20.6033	20.8154	19.5600	22.0621	22.7939	22.3800	19.68093	19.59345	18.80000
	Min	20.1969	19.9040	19.3100	20.9934	20.8578	20.9600	19.05619	18.79443	18.45000
	Max	22.8231	20.8154	20.6300	24.6431	24.9111	23.2200	20.59306	20.91057	20.66000
	Mean	20.7862	20.3140	19.7208	22.3933	22.3354	22.0692	19.69967	19.30318	18.83750
	Std. Dev	0.7942	0.2680	0.3612	1.0629	1.0700	0.6787	0.56676	0.57937	0.58430
	Mean - 3 Sigma	18.4036	19.5100	18.6371	19.2046	19.1254	20.0332	17.99940	17.56506	17.08461
	Mean + 3 Sigma	23.1688	21.1179	20.8046	25.5819	25.5453	24.1052	21.39995	21.04130	20.59039
		Limit: <100mV			Limit: <100mV			Limit: <100mV		

Wafer #	SN	Vol_CHD @ 400uA 5.5V, 3V			Vol_CHA @ 4mA 5.5V, 3V			Vol_CHB @ 4mA 5.5V, 3V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	19.9073	20.7164	19.8000	220.1905	206.9302	206.8600	252.61497	213.84479	217.91000
	68	19.5260	20.7102	19.2700	209.2104	211.0189	209.7700	219.16103	245.32126	290.04000
	11	19.3447	19.7116	19.1700	222.2415	204.5456	205.0800	241.02204	213.03290	224.10000
	12	21.0761	19.8988	18.8700	223.5858	205.0512	205.4800	237.76600	214.44434	213.11000
	13	20.4073	19.2934	18.9500	206.9968	205.3072	205.2800	213.93014	220.13384	228.20000
	14	20.3698	19.3558	18.9700	206.9343	203.8153	207.1100	233.16006	229.52682	228.72000
	15	20.0135	19.3309	18.9800	207.4033	203.3409	204.1600	216.87993	227.01620	228.86000
	16	20.2573	19.1000	18.7200	207.7972	202.1673	204.2900	215.97375	218.61622	230.05000
	71	19.3447	19.1249	18.9300	208.2849	206.0625	205.5300	217.35491	235.75340	213.22000
	72	19.4447	19.0438	18.6400	208.8477	204.5082	204.2100	226.37303	266.03076	229.64000
	73	19.1822	19.0688	18.6700	206.4716	202.8290	203.5800	231.28520	228.34645	227.21000
	74	19.7697	19.8863	19.3500	209.2666	202.9289	204.2700	228.49163	211.72138	220.20000
	75	19.3072	21.3904	19.3900	206.8968	202.6292	203.1600	229.05411	228.25277	219.54000
	76	19.9823	19.7241	19.2400	208.3037	205.6318	203.9200	223.89196	225.85456	227.31000
	Min	19.1822	19.0438	18.6400	206.4716	202.1673	203.1600	213.93014	211.72138	213.11000
	Max	21.0761	21.3904	19.3900	223.5858	206.0625	207.1100	241.02204	266.03076	230.05000
	Mean	19.8750	19.5774	18.9900	210.2525	204.0681	204.6725	226.26523	226.56080	224.18000
	Std. Dev	0.5806	0.6531	0.2527	5.9806	1.2953	1.0743	8.87640	14.49884	6.19357
	Mean - 3 Sigma	18.1330	17.6181	18.2319	192.3108	200.1822	201.4495	199.63603	183.06429	205.59929
	Mean + 3 Sigma	21.6169	21.5367	19.7481	228.1942	207.9855	207.8955	252.89443	270.05732	242.76071
		Limit: <100mV			Limit: <400mV			Limit: <400mV		

Wafer #	SN	Vol_CHC @ 4mA 5.5V, 3V			Vol_CHD @ 4mA 5.5V, 3V			Idd1 Quiescent 3V, 5.5V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	181.1585	180.0582	181.1600	185.8497	200.3650	186.5300	1.67074	1.61173	1.64022
	68	181.7270	183.6725	191.1500	185.6996	194.7356	185.9200	1.62557	1.52954	1.61377
	11	191.7792	177.6862	197.2000	184.7996	189.4432	184.3500	1.65812	1.52262	1.65405
	12	184.1136	178.5726	180.1100	191.4313	189.1623	183.2200	1.62150	1.54907	1.64551
	13	182.8516	179.1406	179.4600	187.4998	184.0322	182.8900	1.61011	1.52710	1.62964
	14	186.7999	177.3990	178.6200	186.7372	185.9232	182.9200	1.66178	1.56901	1.63859
	15	185.1631	178.7598	179.0600	186.4685	184.7062	183.8400	1.64795	1.55111	1.65731
	16	182.7953	177.5800	178.7800	191.8563	182.9213	183.6900	1.63208	1.56698	1.65120
	71	181.2272	180.5514	180.6500	185.0809	184.4191	185.2400	1.60238	1.55070	1.63411
	72	182.1144	178.8098	179.2100	185.5746	182.8651	182.6100	1.65283	1.56616	1.67236
	73	180.2339	177.5488	178.9300	184.7308	181.8603	182.8200	1.62313	1.56820	1.69271
	74	183.2826	180.7199	180.4300	187.3873	186.6784	188.9500	1.61987	1.52873	1.65120
	75	181.5021	182.9359	181.3700	185.3996	191.8023	190.0300	1.62842	1.56372	1.64429
	76	182.9265	180.2268	180.0500	187.4935	186.7720	188.8300	1.62923	1.54175	1.61906
	Min	180.2339	177.3990	178.6200	184.7308	181.8603	182.6100	1.60238	1.52262	1.61906
	Max	191.7792	182.9359	197.2000	191.8563	191.8023	190.0300	1.66178	1.56901	1.69271
	Mean	183.7325	179.1609	181.1558	187.0383	185.8821	184.9492	1.63228	1.55043	1.64917
	Std. Dev	3.0891	1.6722	5.1225	2.3830	3.0287	2.7219	0.01900	0.01712	0.01953
	Mean - 3 Sigma	174.4653	174.1443	165.7882	179.8894	176.7959	176.7835	1.57529	1.49906	1.59058
	Mean + 3 Sigma	192.9996	184.1775	196.5235	194.1872	194.9684	193.1148	1.68927	1.60180	1.70776
		Limit: <400mV			Limit: <400mV			Limit: <2.4mA		

Wafer #	SN	Idd2 Quiescent 3V, 5.5V			Idd1 Dynamic 2MBPS 3V, 5.5V			Idd2 Dynamic 2MBPS 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.8573	2.8597	2.8414	2.2575	2.2425	2.2300	3.71320	3.66758	3.68195	
	68	2.8784	2.8390	2.8357	2.2563	2.1439	2.2310	3.73842	3.68919	3.68707	
	11	2.8524	2.8780	2.8373	2.2533	2.1011	2.2326	3.70636	3.68675	3.66848	
	12	2.8593	2.8674	2.8320	2.2527	2.0893	2.2218	3.70115	3.65892	3.65924	
	13	2.8385	2.8833	2.8398	2.2529	2.0808	2.2086	3.73037	3.68142	3.67971	
	14	2.8357	2.8971	2.8617	2.2711	2.1149	2.2378	3.70640	3.67413	3.68085	
	15	2.8524	2.8739	2.8483	2.2646	2.1170	2.2340	3.74005	3.69790	3.69322	
	16	2.8565	2.9016	2.8841	2.2594	2.0856	2.2139	3.73842	3.68638	3.68370	
	71	2.8801	2.9041	2.8874	2.2773	2.0904	2.2198	3.74030	3.70152	3.68911	
	72	2.8263	2.8683	2.8422	2.2912	2.1148	2.2384	3.72052	3.67503	3.65782	
	73	2.8691	2.9317	2.8776	2.2651	2.0922	2.2177	3.76845	3.73460	3.71177	
	74	2.8670	2.9110	2.8870	2.2658	2.0762	2.2203	3.73301	3.70229	3.69448	
	75	2.8813	2.9329	2.9232	2.2777	2.0891	2.2253	3.73912	3.69973	3.69322	
	76	2.8764	2.8967	2.8683	2.2562	2.0882	2.2157	3.73354	3.70563	3.69045	
	Min	2.8263	2.8674	2.8320	2.2527	2.0762	2.2086	3.70115	3.65892	3.65782	
	Max	2.8813	2.9329	2.9232	2.2912	2.1170	2.2384	3.76845	3.73460	3.71177	
	Mean	2.8579	2.8955	2.8657	2.2656	2.0950	2.2238	3.72981	3.69203	3.68350	
	Std. Dev	0.0179	0.0224	0.0273	0.0120	0.0138	0.0098	0.01883	0.01946	0.01562	
	Mean - 3 Sigma	2.8043	2.8283	2.7839	2.2297	2.0536	2.1944	3.67333	3.63365	3.63665	
Mean + 3 Sigma	2.9115	2.9627	2.9476	2.3015	2.1364	2.2533	3.78629	3.75040	3.73036		
Limit: <3.5mA				Limit: <13mA				Limit: <20mA			

Wafer #	SN	Idd1 Dynamic 10MBPS 3V, 5.5V			Idd2 Dynamic 10MBPS 3V, 5.5V			Idd1 Dynamic 25MBPS 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.3290	5.3433	5.2777	8.4552	8.3379	8.3737	10.86853	10.88684	10.77657	
	68	5.4462	5.2896	5.3719	8.5715	8.4181	8.4447	11.22717	11.00110	11.05713	
	11	5.4142	4.9515	5.2464	8.4406	8.3229	8.3565	11.05778	10.05607	10.69369	
	12	5.3815	4.9274	5.1884	8.5524	8.3036	8.3866	11.02645	9.96989	10.56213	
	13	5.4480	4.9517	5.2010	8.6494	8.3661	8.4098	11.26790	10.07446	10.63444	
	14	5.3963	5.0011	5.2290	8.5223	8.3556	8.3808	11.06628	10.17468	10.67513	
	15	5.4401	5.0638	5.2604	8.6693	8.3931	8.4340	11.23035	10.35327	10.77751	
	16	5.3670	4.8549	5.1402	8.6332	8.2995	8.3628	10.97941	9.83907	10.46623	
	71	5.5250	4.9186	5.2135	8.5943	8.3054	8.3258	11.42639	9.99141	10.66630	
	72	5.5302	4.9866	5.2440	8.6056	8.3414	8.3074	11.43131	10.11487	10.73539	
	73	5.3914	4.8966	5.1342	8.6696	8.4217	8.3965	11.03870	9.92757	10.42371	
	74	5.4876	4.8723	5.2224	8.5061	8.3128	8.3128	11.32450	9.92594	10.67399	
	75	5.5143	4.8867	5.2252	8.5034	8.2473	8.2890	11.35628	9.89254	10.64164	
	76	5.4937	4.9749	5.2582	8.5523	8.3570	8.4029	11.34302	10.12740	10.73600	
	Min	5.3670	4.8549	5.1342	8.4406	8.2473	8.2890	10.97941	9.83907	10.42371	
	Max	5.5302	5.0638	5.2604	8.6696	8.4217	8.4340	11.43131	10.35327	10.77751	
	Mean	5.4491	4.9405	5.2136	8.5749	8.3344	8.3637	11.21236	10.03726	10.64051	
	Std. Dev	0.0594	0.0602	0.0417	0.0737	0.0477	0.0461	0.16834	0.14331	0.10714	
	Mean - 3 Sigma	5.2709	4.7598	5.0885	8.3536	8.1912	8.2256	10.70733	9.60733	10.31911	
Mean + 3 Sigma	5.6273	5.1212	5.3386	8.7961	8.4775	8.5019	11.71740	10.46720	10.96192		
Limit: <13mA				Limit: <20mA				Limit: <13mA			

Wafer #	SN	Idd2 Dynamic 25MBPS 3V, 5.5V			CHA Prop Delay Rise 3V, 5.5V			CHB Prop Delay Rise 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	17.1248	16.8776	17.0182	46.6370	46.6370	46.9500	46.95000	46.63700	46.63700	
	68	17.4400	17.0467	17.1391	47.8890	47.8890	47.8890	47.57600	47.88900	47.57600	
	11	17.1101	16.7779	16.9462	47.5760	46.9500	47.2630	47.88900	46.95000	47.57600	
	12	17.4591	16.7842	17.0764	47.8890	46.9500	47.5760	46.95000	46.32400	46.63700	
	13	17.6873	16.9268	17.1303	48.2020	47.5760	47.8890	47.57600	46.63700	47.26300	
	14	17.3807	16.9166	17.0499	46.9500	46.3240	46.9500	46.32400	45.69800	46.32400	
	15	17.7179	16.9647	17.1625	47.8890	47.2630	47.5760	46.95000	46.32400	46.95000	
	16	17.6311	16.7203	16.9990	47.2630	46.3240	46.6370	47.26300	46.32400	46.95000	
	71	17.4321	16.6837	16.8240	48.5150	47.5760	47.8890	47.26300	46.95000	46.95000	
	72	17.5910	16.8767	16.8749	47.8890	46.9500	47.5760	46.95000	46.32400	46.32400	
	73	17.6928	16.9909	16.9961	47.2630	46.6370	46.6370	46.95000	46.32400	46.63700	
	74	17.2083	16.6189	16.7371	48.8280	47.8890	48.2020	48.20200	47.57600	47.88900	
	75	17.2155	16.5012	16.6932	47.8890	47.2630	47.8890	47.57600	46.95000	47.26300	
	76	17.3636	16.8005	17.0140	48.2020	47.5760	48.2020	47.57600	46.95000	47.26300	
	Min	17.1101	16.5012	16.6932	46.9500	46.3240	46.6370	46.32400	45.69800	46.32400	
	Max	17.7179	16.9909	17.1625	48.8280	47.8890	48.2020	48.20200	47.57600	47.88900	
	Mean	17.4574	16.7969	16.9586	47.8629	47.1065	47.5238	47.28908	46.61092	47.00217	
	Std. Dev	0.2090	0.1482	0.1490	0.5414	0.5082	0.5476	0.50748	0.48962	0.47812	
	Mean - 3 Sigma	16.8303	16.3521	16.5117	46.2386	45.5819	45.8811	45.76663	45.14206	45.56782	
Mean + 3 Sigma	18.0846	17.2416	17.4056	49.4873	48.6311	49.1666	48.81153	48.07977	48.43651		
Limit: <20mA				Limit: 31<x<60ns				Limit: 31<x<60ns			

Wafer #	SN	CHC Prop Delay Rise 3V, 5.5V			CHD Prop Delay Rise 3V, 5.5V			CHA Prop Delay Fall 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	43.1940	42.8810	43.1940	43.1940	43.5070	43.1940	47.26300	47.26300	47.26300	
	68	43.8200	44.4460	43.8200	43.8200	44.1330	43.8200	48.20200	48.51500	48.51500	
	11	46.6370	43.8200	44.1330	45.0720	43.8200	44.1330	48.51500	47.57600	47.88900	
	12	43.8200	43.5070	43.1940	43.5070	43.5070	43.1940	48.51500	47.57600	47.57600	
	13	43.8200	43.8200	43.8200	43.8200	43.8200	43.8200	48.82800	47.57600	47.88900	
	14	43.5070	43.1940	43.1940	43.1940	42.8810	42.8810	47.57600	46.95000	47.26300	
	15	43.8200	43.5070	43.8200	43.5070	43.1940	43.1940	48.51500	47.57600	47.88900	
	16	43.1940	43.1940	42.8810	43.5070	43.5070	43.5070	47.88900	46.63700	47.26300	
	71	43.8200	44.1330	43.8200	43.5070	43.8200	43.5070	49.14100	47.88900	48.51500	
	72	43.5070	46.6370	43.5070	43.5070	44.7590	43.5070	48.82800	47.57600	47.57600	
	73	42.8810	43.1940	42.8810	43.1940	42.8810	42.8810	47.57600	46.63700	46.95000	
	74	44.1330	44.1330	43.8200	44.1330	44.1330	43.8200	48.82800	48.20200	48.51500	
	75	43.8200	43.8200	43.8200	43.5070	43.8200	43.8200	48.51500	47.57600	48.20200	
	76	44.1330	44.1330	44.1330	43.8200	43.8200	43.5070	48.51500	47.57600	47.88900	
	Min	42.8810	43.1940	42.8810	43.1940	42.8810	42.8810	47.57600	46.63700	46.95000	
	Max	46.6370	46.6370	44.1330	45.0720	44.7590	44.1330	49.14100	48.20200	48.51500	
	Mean	43.9243	43.9243	43.5853	43.6896	43.6635	43.4809	48.43675	47.44558	47.78467	
	Std. Dev	0.9279	0.9279	0.4452	0.5075	0.5254	0.3882	0.50160	0.47108	0.48734	
	Mean - 3 Sigma	41.1407	41.1407	42.2498	42.1671	42.0872	42.3165	46.93195	46.03235	46.32265	
Mean + 3 Sigma	46.7079	46.7079	44.9207	45.2120	45.2398	44.6454	49.94155	48.85882	49.24669		
Limit: 31<-x<-60ns			Limit: 31<-x<-60ns			Limit: 31<-x<-60ns					

Wafer #	SN	CHB Prop Delay Fall 3V, 5.5V			CHC Prop Delay Fall 3V, 5.5V			CHD Prop Delay Fall 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	48.2020	47.5760	47.5760	42.5680	42.5680	42.5680	43.82000	44.13300	43.82000	
	68	48.5150	48.8280	48.5150	43.1940	43.5070	43.5070	44.44600	44.75900	44.44600	
	11	49.4540	47.8890	48.8280	43.5070	42.8810	43.1940	44.44600	43.82000	44.13300	
	12	48.2020	47.2630	47.5760	43.1940	42.5680	42.8810	44.13300	43.19400	43.50700	
	13	48.5150	47.5760	48.2020	43.5070	42.8810	43.5070	44.75900	43.82000	44.44600	
	14	47.5760	46.6370	47.2630	43.1940	42.5680	42.8810	43.82000	43.19400	43.50700	
	15	47.8890	47.2630	47.5760	43.5070	42.8810	43.1940	44.13300	43.19400	43.82000	
	16	47.8890	46.9500	47.5760	42.8810	42.2550	42.5680	44.44600	43.19400	43.82000	
	71	48.2020	47.5760	47.8890	43.1940	42.5680	42.8810	43.82000	43.19400	43.50700	
	72	48.2020	47.2630	47.5760	43.1940	42.5680	42.8810	44.44600	43.50700	43.82000	
	73	47.8890	47.2630	47.5760	42.8810	42.2550	42.5680	43.82000	42.88100	43.19400	
	74	48.8280	48.2020	48.5150	43.1940	42.5680	42.8810	44.44600	43.50700	43.82000	
	75	48.2020	47.5760	47.8890	43.1940	42.5680	42.8810	43.82000	43.19400	43.50700	
	76	49.1410	47.8890	48.5150	43.5070	42.8810	43.1940	44.13300	43.50700	43.82000	
	Min	47.5760	46.6370	47.2630	42.8810	42.2550	42.5680	43.82000	42.88100	43.19400	
	Max	49.4540	48.2020	48.8280	43.5070	42.8810	43.5070	44.75900	43.82000	44.44600	
	Mean	48.3324	47.4456	47.9151	43.2462	42.6202	42.9593	44.18517	43.35050	43.74175	
	Std. Dev	0.5577	0.4316	0.4896	0.2247	0.2247	0.2711	0.32235	0.28312	0.33031	
	Mean - 3 Sigma	46.6595	46.1507	46.4462	42.5722	41.9462	42.1461	43.21813	42.50114	42.75083	
Mean + 3 Sigma	50.0054	48.7404	49.3839	43.9201	43.2941	43.7724	45.15220	44.19986	44.73267		
Limit: 31<-x<-60ns			Limit: 31<-x<-60ns			Limit: 31<-x<-60ns					

Wafer #	SN	CHA PWD 3V, 5.5V			CHB PWD 3V, 5.5V			CHC PWD 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.6260	0.6260	0.3130	1.2520	0.9390	0.9390	0.62600	0.31300	0.62600	
	68	0.3130	0.6260	0.6260	0.9390	0.9390	0.9390	0.62600	0.93900	0.31300	
	11	0.9390	0.6260	0.6260	1.5650	0.9390	1.2520	3.13000	0.93900	0.93900	
	12	0.6260	0.6260	0.0000	1.2520	0.9390	0.9390	0.62600	0.93900	0.31300	
	13	0.6260	0.0000	0.0000	0.9390	0.9390	0.9390	0.31300	0.93900	0.31300	
	14	0.6260	0.6260	0.3130	1.2520	0.9390	0.9390	0.31300	0.62600	0.31300	
	15	0.6260	0.3130	0.3130	0.9390	0.9390	0.6260	0.31300	0.62600	0.62600	
	16	0.6260	0.3130	0.6260	0.6260	0.6260	0.6260	0.31300	0.93900	0.31300	
	71	0.6260	0.3130	0.6260	0.9390	0.6260	0.9390	0.62600	1.56500	0.93900	
	72	0.9390	0.6260	0.0000	1.2520	0.9390	1.2520	0.31300	4.06900	0.62600	
	73	0.3130	0.0000	0.3130	0.9390	0.9390	0.9390	0.00000	0.93900	0.31300	
	74	0.0000	0.3130	0.3130	0.6260	0.6260	0.6260	0.93900	1.56500	0.93900	
	75	0.6260	0.3130	0.3130	0.6260	0.6260	0.6260	0.62600	1.25200	0.93900	
	76	0.3130	0.0000	0.3130	1.5650	0.9390	1.2520	0.62600	1.25200	0.93900	
	Min	0.0000	0.0000	0.0000	0.6260	0.6260	0.6260	0.00000	0.62600	0.31300	
	Max	0.9390	0.6260	0.6260	1.5650	0.9390	1.2520	3.13000	4.06900	0.93900	
	Mean	0.5738	0.3391	0.3130	1.0433	0.8347	0.9129	0.67817	1.30417	0.62600	
	Std. Dev	0.2613	0.2482	0.2312	0.3359	0.1541	0.2482	0.81000	0.92306	0.29843	
	Mean - 3 Sigma	-0.2101	-0.4055	-0.3805	0.0357	0.3723	0.1683	-1.75182	-1.46500	-0.26930	
Mean + 3 Sigma	1.3578	1.0837	1.0065	2.0510	1.2970	1.6575	3.10816	4.07334	1.52130		
Limit: <5ns			Limit: <5ns			Limit: <5ns					

Wafer #	SN	CHD PWD 3V, 5.5V			AB_Rise Match Codirection 3V, 5.5V			AB_Fall Match Codirection 3V, 5.5V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	0.6260	0.6260	0.6260	0.3130	0.0000	0.3130	0.93900	0.31300	0.31300
	68	0.6260	0.6260	0.6260	0.3130	0.0000	0.3130	0.31300	0.31300	0.00000
	11	0.6260	0.0000	0.0000	0.3130	0.0000	0.3130	0.93900	0.31300	0.93900
	12	0.6260	0.3130	0.3130	0.9390	0.6260	0.9390	0.31300	0.31300	0.00000
	13	0.9390	0.0000	0.6260	0.6260	0.9390	0.6260	0.31300	0.00000	0.31300
	14	0.6260	0.3130	0.6260	0.6260	0.6260	0.6260	0.00000	0.31300	0.00000
	15	0.6260	0.0000	0.6260	0.9390	0.9390	0.6260	0.62600	0.31300	0.31300
	16	0.9390	0.3130	0.3130	0.0000	0.0000	0.3130	0.00000	0.31300	0.31300
	71	0.3130	0.6260	0.0000	1.2520	0.6260	0.9390	0.93900	0.31300	0.62600
	72	0.9390	1.2520	0.3130	0.9390	0.6260	1.2520	0.62600	0.31300	0.00000
	73	0.6260	0.0000	0.3130	0.3130	0.3130	0.0000	0.31300	0.62600	0.62600
	74	0.3130	0.6260	0.0000	0.6260	0.3130	0.3130	0.00000	0.00000	0.00000
	75	0.3130	0.6260	0.3130	0.3130	0.3130	0.6260	0.31300	0.00000	0.31300
	76	0.3130	0.3130	0.3130	0.6260	0.6260	0.9390	0.62600	0.31300	0.62600
	Min	0.3130	0.0000	0.0000	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000
	Max	0.9390	1.2520	0.6260	1.2520	0.9390	1.2520	0.93900	0.62600	0.93900
	Mean	0.5999	0.3652	0.3130	0.6260	0.4956	0.6260	0.41733	0.26083	0.33908
	Std. Dev	0.2482	0.3735	0.2312	0.3531	0.3118	0.3531	0.33588	0.18071	0.31181
	Mean - 3 Sigma	-0.1447	-0.7555	-0.3805	-0.4333	-0.4399	-0.4333	-0.59030	-0.28130	-0.59635
	Mean + 3 Sigma	1.3445	1.4858	1.0065	1.6853	1.4310	1.6853	1.42496	0.80297	1.27452
		Limit: <5ns			Limit: <5ns			Limit: <5ns		

Wafer #	SN	CD_Rise Match Codirection 3V, 5.5V			CD_Fall Match Codirection 3V, 5.5V			AB_C Rise Match Opposing 3V, 5.5V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	0.0000	0.6260	0.0000	1.2520	1.5650	1.2520	3.75600	3.75600	3.75600
	68	0.0000	0.3130	0.0000	1.2520	1.2520	0.9390	4.06900	3.44300	4.06900
	11	1.5650	0.0000	0.0000	0.9390	0.9390	0.9390	1.25200	3.13000	3.44300
	12	0.3130	0.0000	0.0000	0.9390	0.6260	0.6260	4.06900	3.44300	4.38200
	13	0.0000	0.0000	0.0000	1.2520	0.9390	0.9390	4.38200	3.75600	4.06900
	14	0.3130	0.3130	0.3130	0.6260	0.6260	0.6260	3.44300	3.13000	3.75600
	15	0.3130	0.3130	0.6260	0.6260	0.3130	0.6260	4.06900	3.75600	3.75600
	16	0.3130	0.3130	0.6260	1.5650	0.9390	1.2520	4.06900	3.13000	4.06900
	71	0.3130	0.3130	0.3130	0.6260	0.6260	0.6260	4.69500	3.44300	4.06900
	72	0.0000	1.8780	0.0000	1.2520	0.9390	0.9390	4.38200	0.31300	4.06900
	73	0.3130	0.3130	0.0000	0.9390	0.6260	0.6260	4.38200	3.44300	3.75600
	74	0.0000	0.0000	0.0000	1.2520	0.9390	0.9390	4.69500	3.75600	4.38200
	75	0.3130	0.0000	0.0000	0.6260	0.6260	0.6260	4.06900	3.44300	4.06900
	76	0.3130	0.3130	0.6260	0.6260	0.6260	0.6260	4.06900	3.44300	4.06900
	Min	0.0000	0.0000	0.0000	0.6260	0.3130	0.6260	1.25200	0.31300	3.44300
	Max	1.5650	1.8780	0.6260	1.5650	0.9390	1.2520	4.69500	3.75600	4.38200
	Mean	0.3391	0.3130	0.2087	0.9390	0.7303	0.7825	3.96467	3.18217	3.99075
	Std. Dev	0.4105	0.5169	0.2778	0.3269	0.2039	0.2110	0.91822	0.93266	0.27107
	Mean - 3 Sigma	-0.8923	-1.2377	-0.6248	-0.0418	0.1187	0.1494	1.21001	0.38420	3.17755
	Mean + 3 Sigma	1.5705	1.8637	1.0421	1.9198	1.3419	1.4156	6.71933	5.98013	4.80395
		Limit: <5ns			Limit: <5ns			Limit: <9ns		

Wafer #	SN	AB_C Fall Match Opposing 3V, 5.5V			AB_D Rise Match Opposing 3V, 5.5V			AB_D Fall Match Opposing 3V, 5.5V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	5.6340	5.0080	5.0080	3.7560	3.1300	3.7560	4.38200	3.44300	3.75600
	68	5.3210	5.3210	5.0080	4.0690	3.7560	4.0690	4.06900	4.06900	4.06900
	11	5.9470	5.0080	5.6340	2.8170	3.1300	3.4430	5.00800	4.06900	4.69500
	12	5.3210	5.0080	4.6950	4.3820	3.4430	4.3820	4.38200	4.38200	4.06900
	13	5.3210	4.6950	4.6950	4.3820	3.7560	4.0690	4.06900	3.75600	3.75600
	14	4.3820	4.3820	4.3820	3.7560	3.4430	4.0690	3.75600	3.75600	3.75600
	15	5.0080	4.6950	4.6950	4.3820	4.0690	4.3820	4.38200	4.38200	4.06900
	16	5.0080	4.6950	5.0080	3.7560	2.8170	3.4430	3.44300	3.75600	3.75600
	71	5.9470	5.3210	5.6340	5.0080	3.7560	4.3820	5.32100	4.69500	5.00800
	72	5.6340	5.0080	4.6950	4.3820	2.1910	4.0690	4.38200	4.06900	3.75600
	73	5.0080	5.0080	5.0080	4.0690	3.7560	3.7560	4.06900	4.38200	4.38200
	74	5.6340	5.6340	5.6340	4.6950	3.7560	4.3820	4.38200	4.69500	4.69500
	75	5.3210	5.0080	5.3210	4.3820	3.4430	4.0690	4.69500	4.38200	4.69500
	76	5.6340	5.0080	5.3210	4.3820	3.7560	4.6950	5.00800	4.38200	4.69500
	Min	4.3820	4.3820	4.3820	2.8170	2.1910	3.4430	3.44300	3.75600	3.75600
	Max	5.9470	5.6340	5.6340	5.0080	4.0690	4.6950	5.32100	4.69500	5.00800
	Mean	5.3471	4.9558	5.0602	4.1994	3.4430	4.0951	4.40808	4.22550	4.27767
	Std. Dev	0.4518	0.3223	0.4393	0.5577	0.5169	0.3882	0.54145	0.34027	0.46871
	Mean - 3 Sigma	3.9918	3.9888	3.7423	2.5265	1.8923	2.9306	2.78374	3.20470	2.87154
	Mean + 3 Sigma	6.7024	5.9229	6.3780	5.8724	4.9937	5.2595	6.03242	5.24630	5.68379
		Limit: <9ns			Limit: <9ns			Limit: <9ns		

Wafer #	SN	ChA Input Leakage @ 0V 3V, 5.5V			ChB Input Leakage @ 0V 3V, 5.5V			ChC Input Leakage @ 0V 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0007	-0.0002	0.0001	0.0003	0.0001	0.0002	0.00029	0.00021	-0.00125	
	68	-0.0015	-0.0008	-0.0005	0.0008	-0.0019	-0.0016	-0.00186	0.00014	-0.00087	
	11	-0.0005	-0.0010	0.0005	-0.0007	-0.0004	0.0007	-0.00156	-0.00009	-0.00163	
	12	-0.0001	-0.0011	-0.0004	-0.0007	0.0000	0.0004	0.00106	0.00037	-0.00171	
	13	0.0008	0.0002	0.0001	0.0004	0.0001	-0.0009	0.00044	-0.00102	-0.00087	
	14	-0.0011	0.0006	0.0007	-0.0018	0.0006	-0.0006	-0.00025	-0.00125	-0.00018	
	15	0.0001	-0.0002	0.0001	-0.0011	0.0001	0.0017	-0.00109	-0.00040	-0.00056	
	16	-0.0004	0.0004	-0.0001	-0.0004	-0.0009	0.0006	-0.00209	-0.00086	-0.00041	
	71	-0.0015	-0.0014	-0.0001	-0.0001	-0.0007	0.0007	-0.00071	-0.00025	-0.00064	
	72	-0.0015	0.0003	0.0000	-0.0001	-0.0002	-0.0003	-0.00094	0.00006	0.00020	
	73	-0.0009	0.0004	0.0001	-0.0002	-0.0002	-0.0007	0.00060	-0.00025	0.00005	
	74	-0.0022	0.0001	-0.0009	-0.0011	-0.0011	-0.0004	-0.00132	-0.00048	-0.00163	
	75	-0.0002	0.0004	-0.0001	0.0009	0.0000	0.0008	0.00021	0.00083	-0.00163	
	76	-0.0003	-0.0017	0.0016	0.0014	-0.0001	-0.0005	-0.00102	-0.00125	-0.00132	
	Min	-0.0022	-0.0017	-0.0009	-0.0018	-0.0011	-0.0009	-0.00209	-0.00125	-0.00171	
	Max	0.0008	0.0006	0.0016	0.0014	0.0006	0.0017	0.00006	0.00083	0.00020	
	Mean	-0.0006	-0.0002	0.0001	-0.0003	-0.0002	0.0001	-0.00056	-0.00038	-0.00086	
	Std. Dev	0.0008	0.0008	0.0006	0.0009	0.0005	0.0008	0.00096	0.00064	0.00071	
Mean - 3 Sigma	-0.0031	-0.0027	-0.0017	-0.0029	-0.0017	-0.0022	-0.00345	-0.00230	-0.00298		
Mean + 3 Sigma	0.0018	0.0022	0.0020	0.0024	0.0012	0.0025	0.00234	0.00154	0.00126		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChD Input Leakage @ 0V 3V, 5.5V			ChA Input Leakage @ Vdd1 3V, 5.5V			ChB Input Leakage @ Vdd1 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0015	0.0008	-0.0006	0.0008	0.0019	0.0021	0.00225	0.00175	0.00144	
	68	-0.0009	-0.0001	-0.0014	0.0006	0.0017	0.0012	0.00133	0.00198	0.00121	
	11	-0.0021	-0.0012	0.0000	0.0021	0.0021	0.0004	0.00156	0.00037	0.00197	
	12	-0.0001	-0.0008	0.0002	0.0019	0.0010	0.0030	0.00010	0.00183	0.00029	
	13	-0.0007	-0.0015	-0.0003	0.0014	0.0010	0.0004	0.00194	0.00083	0.00128	
	14	-0.0003	0.0009	-0.0010	0.0008	0.0027	0.0020	0.00102	0.00075	0.00121	
	15	0.0009	0.0000	-0.0012	0.0012	0.0014	0.0015	0.00140	0.00137	0.00228	
	16	-0.0007	-0.0013	-0.0012	0.0015	0.0023	0.0009	0.00233	0.00152	0.00090	
	71	-0.0002	-0.0006	0.0007	0.0008	0.0019	0.0021	-0.00006	0.00183	0.00067	
	72	-0.0009	-0.0011	-0.0011	0.0019	0.0014	0.0019	0.00033	0.00290	0.00136	
	73	-0.0016	0.0002	-0.0006	0.0012	0.0002	0.0008	0.00140	0.00144	0.00098	
	74	-0.0004	-0.0014	-0.0009	0.0012	0.0016	0.0004	0.00040	0.00198	0.00113	
	75	-0.0003	-0.0006	-0.0002	0.0015	0.0026	0.0007	0.00094	0.00236	-0.00063	
	76	-0.0013	-0.0002	0.0001	0.0018	0.0004	0.0028	0.00087	0.00183	0.00220	
	Min	-0.0021	-0.0015	-0.0012	0.0008	0.0002	0.0004	-0.00006	0.00037	-0.00063	
	Max	0.0009	0.0009	0.0007	0.0021	0.0027	0.0030	0.00233	0.00290	0.00228	
	Mean	-0.0006	-0.0006	-0.0004	0.0014	0.0015	0.0014	0.00102	0.00158	0.00114	
	Std. Dev	0.0008	0.0007	0.0006	0.0004	0.0005	0.0009	0.00074	0.00071	0.00082	
Mean - 3 Sigma	-0.0030	-0.0028	-0.0023	0.0001	-0.0009	-0.0014	-0.00120	-0.00053	-0.00132		
Mean + 3 Sigma	0.0017	0.0016	0.0014	0.0027	0.0040	0.0042	0.00324	0.00370	0.00359		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChC Input Leakage @ Vdd2 3V, 5.5V			ChD Input Leakage @ Vdd2 3V, 5.5V			Voh_A @ -20uA 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0015	0.0015	0.0011	-0.0001	0.0019	0.0002	5.49562	5.49266	5.49061	
	68	0.0008	0.0016	0.0011	0.0020	-0.0001	-0.0009	5.49648	5.49462	5.49044	
	11	0.0009	0.0019	-0.0005	0.0018	0.0000	0.0008	5.49410	5.49308	5.49076	
	12	-0.0003	-0.0003	0.0001	0.0012	0.0000	0.0010	5.49678	5.49412	5.49064	
	13	0.0001	0.0013	0.0009	-0.0004	0.0014	0.0013	5.49653	5.49217	5.49085	
	14	0.0005	0.0008	0.0010	0.0014	0.0013	0.0007	5.49655	5.49439	5.49069	
	15	0.0000	0.0011	-0.0002	0.0006	0.0009	0.0013	5.49659	5.49429	5.49033	
	16	0.0006	0.0001	0.0004	0.0000	0.0005	0.0012	5.49666	5.49416	5.48824	
	71	0.0023	0.0011	-0.0002	0.0009	0.0001	0.0007	5.49651	5.49423	5.48937	
	72	0.0008	0.0019	0.0002	0.0013	0.0016	0.0004	5.49657	5.49433	5.49066	
	73	-0.0002	0.0015	-0.0001	0.0001	0.0011	0.0000	5.49645	5.49483	5.48910	
	74	-0.0015	-0.0003	-0.0004	0.0013	0.0012	0.0010	5.49549	5.49429	5.49020	
	75	0.0011	0.0008	-0.0003	0.0012	0.0004	0.0015	5.49454	5.49456	5.49045	
	76	0.0000	0.0008	0.0014	0.0027	-0.0011	0.0003	5.49585	5.49414	5.49059	
	Min	-0.0015	-0.0003	-0.0005	-0.0004	-0.0011	0.0000	5.49410	5.49217	5.48824	
	Max	0.0023	0.0019	0.0014	0.0027	0.0016	0.0015	5.49678	5.49483	5.49085	
	Mean	0.0004	0.0009	0.0002	0.0010	0.0006	0.0009	5.49605	5.49405	5.49016	
	Std. Dev	0.0009	0.0008	0.0006	0.0009	0.0008	0.0004	0.00089	0.00072	0.00082	
Mean - 3 Sigma	-0.0024	-0.0014	-0.0016	-0.0016	-0.0018	-0.0005	5.49338	5.49189	5.48771		
Mean + 3 Sigma	0.0031	0.0032	0.0020	0.0036	0.0030	0.0022	5.49873	5.49621	5.49261		
Limit: +/-10uA				Limit: +/-10uA				Limit: >5.4V			

Wafer #	SN	Voh_B @ -20uA 3V, 5.5V			V	Voh_C @ -20uA 3V, 5.5V			V	Voh_D @ -20uA 3V, 5.5V			V
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	5.4954	5.4926	5.4930	2.9962	2.9963	2.9952	2.99610	2.99561	2.99513			
	68	5.4959	5.4950	5.4927	2.9969	2.9946	2.9951	2.99689	2.99316	2.99490			
	11	5.4936	5.4929	5.4932	2.9921	2.9952	2.9950	2.99186	2.99361	2.99478			
	12	5.4963	5.4943	5.4930	2.9969	2.9942	2.9952	2.99681	2.99375	2.99490			
	13	5.4963	5.4922	5.4931	2.9969	2.9948	2.9949	2.99683	2.99372	2.99494			
	14	5.4962	5.4949	5.4930	2.9969	2.9945	2.9949	2.99687	2.99364	2.99485			
	15	5.4962	5.4942	5.4929	2.9971	2.9947	2.9952	2.99702	2.99393	2.99504			
	16	5.4964	5.4941	5.4905	2.9970	2.9934	2.9949	2.99676	2.99286	2.99506			
	71	5.4962	5.4946	5.4919	2.9971	2.9935	2.9949	2.99690	2.99252	2.99479			
	72	5.4962	5.4946	5.4929	2.9969	2.9915	2.9950	2.99667	2.99031	2.99482			
	73	5.4961	5.4946	5.4915	2.9972	2.9949	2.9950	2.99674	2.99414	2.99498			
	74	5.4953	5.4946	5.4925	2.9971	2.9947	2.9950	2.99670	2.99379	2.99479			
	75	5.4942	5.4943	5.4930	2.9971	2.9942	2.9932	2.99684	2.99322	2.99315			
	76	5.4956	5.4949	5.4928	2.9971	2.9948	2.9949	2.99683	2.99350	2.99478			
	Min	5.4936	5.4922	5.4905	2.9921	2.9915	2.9932	2.99186	2.99031	2.99315			
	Max	5.4964	5.4949	5.4932	2.9972	2.9952	2.9952	2.99702	2.99414	2.99506			
	Mean	5.4957	5.4942	5.4925	2.9966	2.9942	2.9948	2.99640	2.99325	2.99474			
	Std. Dev	0.0009	0.0008	0.0008	0.0014	0.0010	0.0005	0.00143	0.00103	0.00051			
	Mean - 3 Sigma	5.4930	5.4917	5.4901	2.9924	2.9911	2.9933	2.99210	2.99016	2.99321			
	Mean + 3 Sigma	5.4984	5.4966	5.4950	3.0009	2.9973	2.9964	3.00070	2.99634	2.99627			
Limit: >5.4V				Limit: >2.8V				Limit: >2.8V					

Wafer #	SN	Voh_A @ -4mA 3V, 5.5V			V	Voh_B @ -4mA 3V, 5.5V			V	Voh_C @ -4mA 3V, 5.5V			V
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	5.2899	5.2835	5.2870	5.2692	5.2784	5.2802	2.70840	2.71123	2.71031			
	68	5.2913	5.2888	5.2837	5.2837	5.2554	5.2549	2.70974	2.70333	2.69751			
	11	5.2729	5.2817	5.2829	5.2572	5.2751	5.2708	2.66872	2.69265	2.67830			
	12	5.2801	5.2873	5.2815	5.2676	5.2797	5.2784	2.71190	2.69458	2.69907			
	13	5.2936	5.2777	5.2834	5.2889	5.2651	5.2671	2.71037	2.69206	2.69716			
	14	5.2931	5.2884	5.2825	5.2693	5.2647	5.2672	2.70721	2.69493	2.69841			
	15	5.2935	5.2887	5.2831	5.2859	5.2675	5.2651	2.70734	2.69443	2.69841			
	16	5.2935	5.2885	5.2733	5.2867	5.2737	5.2532	2.70934	2.68768	2.69832			
	71	5.2923	5.2853	5.2756	5.2849	5.2592	5.2725	2.70871	2.68238	2.69329			
	72	5.2921	5.2869	5.2819	5.2770	5.2628	5.2624	2.70702	2.67435	2.69701			
	73	5.2945	5.2885	5.2775	5.2708	5.2656	5.2587	2.71184	2.69474	2.69807			
	74	5.2940	5.2867	5.2795	5.2771	5.2804	5.2694	2.70680	2.68909	2.69432			
	75	5.2860	5.2860	5.2818	5.2675	5.2636	5.2711	2.70749	2.68937	2.68211			
	76	5.2897	5.2854	5.2814	5.2764	5.2685	5.2634	2.70790	2.69206	2.69435			
	Min	5.2729	5.2777	5.2733	5.2572	5.2592	5.2532	2.66872	2.67435	2.67830			
	Max	5.2945	5.2887	5.2834	5.2889	5.2804	5.2784	2.71190	2.69493	2.69907			
	Mean	5.2896	5.2859	5.2804	5.2758	5.2688	5.2666	2.70539	2.68986	2.69407			
	Std. Dev	0.0067	0.0033	0.0033	0.0097	0.0068	0.0067	0.01169	0.00612	0.00679			
	Mean - 3 Sigma	5.2694	5.2762	5.2706	5.2468	5.2483	5.2465	2.67032	2.67149	2.67369			
	Mean + 3 Sigma	5.3098	5.2957	5.2901	5.3048	5.2893	5.2867	2.74045	2.70823	2.71444			
Limit: >5.1V				Limit: >5.1V				Limit: >2.6V					

Wafer #	SN	Voh_D @ -4mA 3V, 5.5V			V	Vol_CHA @ 20uA 3V, 5.5V			mV	Vol_CHB @ 20uA 3V, 5.5V			mV
		PRE	100k	Anneal		PRE	100k	Anneal		PRE	100k	Anneal	
CTRL	9	2.7046	2.6911	2.7059	7.4534	1.1022	0.5206	7.53180	1.12884	1.12000			
	68	2.7067	2.6921	2.7037	1.4569	1.4393	0.7143	1.54471	1.63471	1.43000			
	11	2.6772	2.6827	2.6931	3.0013	1.9199	1.0800	3.11960	1.96571	1.48000			
	12	2.7056	2.6850	2.6972	0.9504	2.0073	0.7829	1.15723	1.98445	1.26000			
	13	2.7075	2.6873	2.6957	0.9191	1.8388	0.9516	1.00099	1.90326	1.56000			
	14	2.7090	2.6869	2.6964	0.9004	1.2021	2.2300	1.12598	1.33494	2.71000			
	15	2.7072	2.6892	2.6951	0.9504	1.1147	0.5144	1.06974	1.29746	1.01000			
	16	2.7020	2.6833	2.6956	1.2255	0.9336	0.5019	1.40722	1.01018	1.35000			
	71	2.7057	2.6784	2.6909	1.3381	1.4205	0.4520	1.23223	1.56601	1.13000			
	72	2.7053	2.6709	2.6951	0.9504	1.1334	0.2646	0.98849	1.32244	0.80989			
	73	2.7088	2.6913	2.6954	0.9754	1.2832	0.4020	1.15723	1.17880	1.21000			
	74	2.7037	2.6834	2.6874	2.1134	1.2520	0.4207	2.19466	1.37865	1.12000			
	75	2.7049	2.6805	2.6760	1.2755	1.3269	0.4145	1.39472	1.32869	1.07000			
	76	2.7045	2.6854	2.6872	1.4193	2.0510	0.4332	1.42596	2.13434	1.17000			
	Min	2.6772	2.6709	2.6760	0.9004	0.9336	0.2646	0.98849	1.01018	0.80989			
	Max	2.7090	2.6913	2.6972	3.0013	2.0510	2.2300	3.11960	2.13434	2.71000			
	Mean	2.7034	2.6837	2.6921	1.3349	1.4569	0.7040	1.43950	1.53374	1.32332			
	Std. Dev	0.0085	0.0054	0.0061	0.6281	0.3893	0.5393	0.62006	0.36856	0.48140			
	Mean - 3 Sigma	2.6779	2.6675	2.6739	-0.5495	0.2891	-0.9140	-0.42069	0.42805	-0.12087			
	Mean + 3 Sigma	2.7290	2.6999	2.7103	3.2194	2.6248	2.3220	3.29969	2.63944	2.76752			
Limit: >2.6V				Limit: <100mV				Limit: <100mV					

Wafer #	SN	Vol. CHC @ 20uA 3V, 5.5V			Vol. CHD @ 20uA 3V, 5.5V			Vol. CHA @ 400uA 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.8070	1.6905	1.3900	1.7185	1.3941	1.3100	24.51140	17.21360	16.64000	
	68	1.6196	2.1087	1.3300	1.5498	1.7935	1.1400	17.85203	17.72547	17.18000	
	11	1.5946	1.6406	1.4000	1.5248	1.4066	1.2500	20.44700	17.99389	17.08000	
	12	2.2506	1.7717	1.3400	2.1936	1.6812	1.2200	18.42105	18.07504	16.89000	
	13	2.1256	1.8653	1.3000	2.0123	1.5189	1.2700	17.09543	17.95019	16.99000	
	14	2.1569	1.5906	1.3400	2.0498	1.3255	0.9912	17.12044	17.23857	18.47000	
	15	1.9507	1.7529	1.3900	1.8560	1.4565	1.2400	17.14545	16.89524	16.43000	
	16	1.7071	1.8278	1.1900	1.6373	1.4253	1.1400	17.45185	16.85154	16.67000	
	71	1.6446	1.6905	1.1900	1.4560	1.3816	1.1700	17.49562	17.61935	16.55000	
	72	1.5071	1.5719	1.0100	1.4748	1.3130	0.9724	17.27677	17.39462	16.36000	
	73	1.4759	1.7717	1.1300	1.5248	1.3754	1.1300	17.01414	17.07627	16.28000	
	74	1.6946	1.8965	1.1400	1.6373	1.7686	0.9349	18.52110	17.19487	16.50000	
	75	1.6133	2.7205	1.1700	1.4873	2.3552	1.0500	17.38307	17.13245	16.33000	
	76	1.8008	1.9589	1.2800	1.7435	1.6625	1.1700	17.60192	18.19989	16.59000	
	Min	1.4759	1.5719	1.0100	1.4560	1.3130	0.9349	17.01414	16.85154	16.28000	
	Max	2.2506	2.7205	1.4000	2.1936	2.3552	1.2700	20.44700	18.19989	18.47000	
	Mean	1.7935	1.8382	1.2400	1.7164	1.5559	1.1282	17.74782	17.46849	16.76167	
	Std. Dev	0.2646	0.3025	0.1199	0.2543	0.2918	0.1152	0.98093	0.48080	0.59766	
	Mean - 3 Sigma	0.9996	0.9308	0.8802	0.9536	0.6803	0.7826	14.80502	16.02609	14.96869	
Mean + 3 Sigma	2.5874	2.7457	1.5998	2.4793	2.4314	1.4739	20.69062	18.91090	18.55465		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol. CHB @ 400uA 3V, 5.5V			Vol. CHC @ 400uA 3V, 5.5V			Vol. CHD @ 400uA 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	26.6055	17.8851	18.1700	21.9550	21.7470	21.6900	22.37616	23.40005	22.11000	
	68	18.8373	21.0452	21.5700	21.9675	22.5585	22.4700	22.24490	23.41253	22.01000	
	11	22.2558	18.6907	19.1200	22.9858	21.4849	23.3700	22.15740	22.33283	21.84000	
	12	20.0997	18.8843	18.0900	22.4860	21.6347	21.2700	23.18872	22.25169	21.66000	
	13	17.7436	19.3340	19.6900	22.4111	21.7221	21.2900	22.73869	21.81482	21.55000	
	14	19.6122	19.7337	20.7900	22.7359	21.2976	21.2600	22.55742	21.95212	21.55000	
	15	17.9436	19.3340	19.1800	22.4923	21.5223	21.2100	22.56367	21.88971	21.66000	
	16	18.2186	18.5658	19.5100	22.0924	21.5847	21.2300	22.86994	21.73368	21.55000	
	71	18.3686	20.2958	17.8900	21.8675	21.7658	21.5500	22.11989	21.95836	21.66000	
	72	18.8623	19.7649	19.2700	22.0237	21.6222	21.2600	22.14490	21.68376	21.31000	
	73	19.5247	19.4901	19.1600	21.7676	21.5286	21.3100	22.03239	21.68376	21.31000	
	74	20.3747	18.0974	18.5600	22.2112	22.0716	21.6200	22.65743	22.40772	22.04000	
	75	19.4497	19.4901	18.3300	21.9238	22.9768	21.4300	22.28240	23.53735	22.26000	
	76	19.0373	20.1271	19.0500	22.1987	21.9655	21.4300	22.60743	22.37651	22.07000	
	Min	17.7436	18.0974	17.8900	21.7676	21.2976	21.2100	22.03239	21.68376	21.31000	
	Max	22.2558	20.2958	20.7900	22.9858	22.9768	23.3700	23.18872	23.53735	22.26000	
	Mean	19.2909	19.3173	19.0533	22.2663	21.7647	21.5192	22.49336	22.13519	21.70500	
	Std. Dev	1.2527	0.6523	0.7837	0.3668	0.4355	0.5973	0.35230	0.51766	0.29571	
	Mean - 3 Sigma	15.5329	17.3605	16.7021	21.1658	20.4583	19.7273	21.43645	20.58222	20.81786	
Mean + 3 Sigma	23.0488	21.2742	21.4045	23.3669	23.0712	23.3111	23.55026	23.68817	22.59214		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol. CHA @ 4mA 3V, 5.5V			Vol. CHB @ 4mA 3V, 5.5V			Vol. CHC @ 4mA 3V, 5.5V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	186.4309	170.4688	170.4800	208.0118	177.0411	181.2900	215.46319	214.54075	215.37000	
	68	172.7745	173.8834	172.9100	182.3198	205.4885	217.2200	217.94343	220.15883	227.23000	
	11	186.4997	170.1817	170.2200	203.9371	178.5837	186.9500	227.75815	211.90025	232.49000	
	12	183.4108	170.7684	170.0800	202.0185	179.7390	177.0800	218.05589	211.11372	213.75000	
	13	170.2421	170.7747	169.8400	176.6202	185.1975	191.4400	217.52486	212.38716	214.02000	
	14	170.5423	169.1454	172.6000	195.7689	194.5655	192.5700	220.29246	209.94641	212.11000	
	15	170.1108	167.9594	168.3200	179.3200	190.4498	191.1600	220.02383	212.23734	213.35000	
	16	171.3989	167.9781	169.0400	179.0825	183.5362	193.9600	217.74976	210.78289	213.08000	
	71	171.8241	171.1679	169.9700	180.5949	198.4438	177.2000	217.69354	215.37721	216.58000	
	72	171.8304	169.7197	168.8900	188.3069	196.7763	192.9200	218.36200	213.37344	214.67000	
	73	169.7919	168.0280	168.0600	194.1315	193.0666	191.1300	215.48194	210.82033	213.48000	
	74	173.3436	168.7959	169.2300	192.2504	177.1660	184.8400	220.24249	215.77048	216.93000	
	75	171.0300	168.7522	168.2500	191.3692	193.0916	183.7100	218.29954	217.33731	217.55000	
	76	171.9241	171.4301	168.7000	187.0445	190.7308	191.0100	219.39908	214.78420	216.08000	
	Min	169.7919	167.9594	168.0600	176.6202	177.1660	177.0800	215.48194	209.94641	212.11000	
	Max	186.4997	171.4301	172.6000	203.9371	198.4438	193.9600	227.75815	217.33731	232.49000	
	Mean	173.4957	169.5585	169.4333	189.2037	188.4456	187.8308	219.24030	212.98590	216.17417	
	Std. Dev	5.4808	1.2881	1.2458	9.0488	7.3481	5.9262	3.01580	2.33890	5.41840	
	Mean - 3 Sigma	157.0532	165.6943	165.6958	162.0572	166.4013	170.0521	210.19288	205.96919	199.91897	
Mean + 3 Sigma	189.9382	173.4226	173.1708	216.3502	210.4898	205.6096	228.28771	220.00260	232.42936		
Limit: <400mV				Limit: <400mV				Limit: <400mV			

Wafer #	SN	Vol_CHD @ 4mA 3V, 5.5V			Idd1 Quiescent 3V, 3V			Idd2 Quiescent 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	220.2457	235.0777	220.9600	1.6569	1.6382	1.6549	1.65283	1.62964	1.63208	
	68	221.7208	230.6591	222.2300	1.6296	1.5007	1.6077	1.67318	1.64958	1.65649	
	11	220.9395	222.4958	219.8900	1.6435	1.5251	1.6345	1.64551	1.66341	1.65975	
	12	225.4648	221.8155	216.9600	1.6239	1.5405	1.6089	1.63045	1.64673	1.64266	
	13	222.4209	217.4281	217.6300	1.6113	1.5397	1.6158	1.62476	1.66178	1.63615	
	14	220.4207	218.5951	216.4700	1.6154	1.5531	1.6475	1.63900	1.65934	1.62516	
	15	221.4646	218.1083	218.4300	1.6345	1.5308	1.6256	1.63534	1.67521	1.66748	
	16	226.7899	216.1050	218.0500	1.6366	1.5588	1.6520	1.61947	1.67277	1.66260	
	71	221.1270	218.9322	220.6500	1.6154	1.5426	1.6284	1.68009	1.67196	1.70492	
	72	221.5708	217.0598	218.0000	1.6561	1.5283	1.6569	1.64266	1.66463	1.66911	
	73	220.0895	215.2499	217.3200	1.6390	1.5719	1.6549	1.66545	1.69678	1.70898	
	74	224.0647	221.2663	225.1300	1.6406	1.5304	1.6219	1.67074	1.67277	1.67765	
	75	221.9959	225.7661	226.0900	1.6614	1.5605	1.6272	1.66300	1.70858	1.70614	
	76	223.8585	221.1228	224.1400	1.5942	1.5304	1.6341	1.67643	1.68579	1.67318	
	Min	220.0895	215.2499	216.4700	1.5942	1.5251	1.6089	1.61947	1.64673	1.62516	
	Max	226.7899	225.7661	226.0900	1.6614	1.5719	1.6569	1.68009	1.70858	1.70898	
	Mean	222.5172	219.4954	219.8967	1.6310	1.5427	1.6340	1.64941	1.67331	1.66948	
	Std. Dev	2.0918	3.0491	3.3794	0.0195	0.0151	0.0157	0.02089	0.01696	0.02727	
	Mean - 3 Sigma	216.2417	210.3481	209.7586	1.5725	1.4973	1.5868	1.58675	1.62243	1.58767	
Mean + 3 Sigma	228.7928	228.6427	230.0347	1.6895	1.5880	1.6812	1.71207	1.72420	1.75130		
Limit: <400mV				Limit: <2.4mA				Limit: <2.3mA			

Wafer #	SN	Idd1 Dynamic 2MBPS 3V, 3V			Idd2 Dynamic 2MBPS 3V, 3V			Idd1 Dynamic 10MBPS 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.2379	2.2330	2.2203	2.2784	2.2642	2.2662	5.26322	5.25899	5.19897	
	68	2.2423	2.1250	2.2157	2.3056	2.2865	2.2774	5.37223	5.22965	5.29890	
	11	2.2409	2.0847	2.2141	2.2834	2.2546	2.2541	5.34737	4.87638	5.17573	
	12	2.2356	2.0731	2.2054	2.2728	2.2490	2.2449	5.30358	4.84753	5.11307	
	13	2.2425	2.0754	2.1867	2.2873	2.2641	2.2590	5.39534	4.87528	5.12943	
	14	2.2570	2.0950	2.2183	2.2730	2.2514	2.2508	5.32234	4.90999	5.14672	
	15	2.2516	2.1073	2.2189	2.2976	2.2694	2.2794	5.36962	4.99633	5.17822	
	16	2.2374	2.0663	2.1971	2.2813	2.2556	2.2598	5.29268	4.78283	5.07031	
	71	2.2528	2.0744	2.2041	2.3105	2.2811	2.2848	5.45251	4.85200	5.13138	
	72	2.2806	2.1020	2.2230	2.2860	2.2591	2.2596	5.44551	4.91585	5.16939	
	73	2.2389	2.0795	2.2036	2.3246	2.3093	2.3086	5.31815	4.81058	5.06156	
	74	2.2555	2.0760	2.2076	2.3032	2.2808	2.2740	5.41019	4.83422	5.15010	
	75	2.2660	2.0812	2.2137	2.3077	2.2816	2.2883	5.43371	4.82999	5.15771	
	76	2.2458	2.0806	2.2065	2.3049	2.2913	2.2943	5.43176	4.90515	5.18806	
	Min	2.2356	2.0663	2.1867	2.2728	2.2490	2.2449	5.29268	4.78283	5.06156	
	Max	2.2806	2.1073	2.2230	2.3246	2.3093	2.3086	5.45251	4.99633	5.18806	
	Mean	2.2504	2.0830	2.2082	2.2944	2.2706	2.2715	5.37690	4.86968	5.13931	
	Std. Dev	0.0132	0.0124	0.0102	0.0162	0.0185	0.0198	0.05872	0.05705	0.04077	
	Mean - 3 Sigma	2.2107	2.0459	2.1777	2.2458	2.2152	2.2121	5.20073	4.69852	5.01699	
Mean + 3 Sigma	2.2901	2.1200	2.2387	2.3429	2.3260	2.3308	5.55306	5.04083	5.26163		
Limit: <13mA				Limit: <13mA				Limit: <13mA			

Wafer #	SN	Idd2 Dynamic 10MBPS 3V, 3V			Idd1 Dynamic 25MBPS 3V, 3V			Idd2 Dynamic 25MBPS 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.3347	5.2812	5.2943	10.6853	10.6731	10.5813	10.72095	10.67830	10.73800	
	68	5.4451	5.3567	5.3795	11.0095	10.7865	10.8344	11.11544	10.88554	10.91496	
	11	5.3677	5.1761	5.2726	10.8608	9.8482	10.4967	10.86080	10.39868	10.66410	
	12	5.3953	5.1754	5.2753	10.8209	9.7793	10.3817	11.00098	10.44059	10.66825	
	13	5.4328	5.2093	5.3094	11.0732	9.8779	10.4410	11.12524	10.47327	10.74121	
	14	5.3674	5.1774	5.2569	10.8912	9.9665	10.4705	10.88953	10.41003	10.57275	
	15	5.4712	5.2465	5.3505	11.0207	10.1860	10.5730	11.18205	10.59574	10.83122	
	16	5.3984	5.1728	5.2664	10.7848	9.6065	10.2442	11.00614	10.38936	10.58879	
	71	5.4264	5.2216	5.2926	11.2146	9.7976	10.4780	11.05611	10.45760	10.67033	
	72	5.4014	5.1975	5.2377	11.2033	9.9203	10.5429	11.02808	10.45215	10.57174	
	73	5.5168	5.3208	5.3797	10.8153	9.6834	10.2226	11.27389	10.73596	10.84025	
	74	5.4021	5.2211	5.2784	11.0856	9.7536	10.4725	10.94674	10.40222	10.61292	
	75	5.3740	5.1527	5.2273	11.1265	9.6824	10.4441	10.80656	10.23698	10.47693	
	76	5.4556	5.2709	5.3652	11.1276	9.9238	10.5442	11.13224	10.60404	10.85258	
	Min	5.3674	5.1527	5.2273	10.7848	9.6065	10.2226	10.80656	10.23698	10.47693	
	Max	5.5168	5.3208	5.3797	11.2146	10.1860	10.5730	11.27389	10.73596	10.85258	
	Mean	5.4174	5.2118	5.2927	11.0020	9.8355	10.4426	11.02570	10.46639	10.67426	
	Std. Dev	0.0455	0.0484	0.0492	0.1585	0.1559	0.1107	0.13791	0.12784	0.12040	
	Mean - 3 Sigma	5.2810	5.0667	5.1451	10.5264	9.3678	10.1106	10.61196	10.08286	10.31307	
Mean + 3 Sigma	5.5538	5.3570	5.4403	11.4776	10.3032	10.7746	11.43943	10.84991	11.03544		
Limit: <13mA				Limit: <13mA				Limit: <13mA			

Wafer #	SN	CHA Prop Delay Rise 3V, 3V			CHB Prop Delay Rise 3V, 3V			CHC Prop Delay Rise 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	52.8970	52.5840	52.5840	52.8970	52.5840	52.5840	52.58400	52.58400	52.27100	
	68	53.5230	53.5230	53.5230	53.5230	53.5230	53.5230	53.21000	53.52300	53.21000	
	11	53.8360	52.8970	53.5230	54.1490	53.2100	53.5230	55.08800	53.52300	54.14900	
	12	53.2100	52.5840	53.2100	53.2100	52.5840	52.8970	53.21000	53.21000	53.52300	
	13	53.5230	53.5230	53.5230	53.2100	53.2100	53.2100	53.21000	53.52300	53.83600	
	14	52.5840	51.9580	52.5840	52.5840	51.9580	52.5840	52.58400	52.89700	53.52300	
	15	53.2100	52.8970	53.2100	53.2100	52.5840	53.2100	53.21000	53.52300	54.14900	
	16	52.8970	52.2710	52.8970	52.8970	52.2710	54.7750	52.58400	52.89700	52.89700	
	71	53.8360	53.2100	53.8360	53.2100	53.2100	53.5230	53.21000	54.14900	54.14900	
	72	53.2100	52.5840	52.8970	52.8970	52.5840	52.5840	53.52300	54.77500	53.83600	
	73	52.8970	52.2710	52.8970	52.8970	52.2710	52.8970	52.58400	52.89700	53.21000	
	74	54.4620	54.1490	54.4620	54.4620	54.4620	53.8360	53.52300	54.14900	54.14900	
	75	53.8360	53.2100	53.8360	53.8360	53.2100	53.8360	52.58400	53.21000	54.46200	
	76	54.1490	53.5230	53.8360	53.8360	53.5230	53.8360	53.83600	54.46200	54.46200	
	Min	52.5840	51.9580	52.5840	52.5840	51.9580	52.5840	52.58400	52.89700	52.89700	
	Max	54.4620	54.1490	54.4620	54.4620	54.4620	54.7750	55.08800	54.77500	54.46200	
	Mean	53.4708	52.9231	53.3926	53.3665	52.8709	53.4448	53.26217	53.60125	53.86208	
	Std. Dev	0.5636	0.6325	0.5414	0.5740	0.5734	0.6951	0.71666	0.64181	0.48962	
	Mean - 3 Sigma	51.7800	51.0256	51.7682	51.6444	51.1507	51.3594	51.11220	51.67583	52.39323	
Mean + 3 Sigma	55.1617	54.8205	55.0169	55.0886	54.5911	55.5301	55.41213	55.52667	55.33094		
Limit: 29-x-66ns				Limit: 29-x-66ns				Limit: 29-x-66ns			

Wafer #	SN	CHD Prop Delay Rise 3V, 3V			CHA Prop Delay Fall 3V, 3V			CHB Prop Delay Fall 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	52.8970	52.8970	52.5840	52.2710	51.9580	51.9580	52.89700	52.58400	52.58400	
	68	52.8970	53.2100	52.8970	52.8970	53.2100	53.2100	53.21000	53.52300	53.52300	
	11	55.0880	53.8360	54.1490	53.2100	52.2710	52.8970	54.14900	52.89700	53.52300	
	12	52.8970	53.2100	53.5230	52.8970	52.2710	52.5840	52.89700	52.27100	52.58400	
	13	53.2100	53.5230	53.8360	53.2100	52.5840	52.8970	53.21000	52.58400	53.21000	
	14	52.5840	52.8970	53.2100	52.5840	51.6450	52.2710	52.27100	51.64500	52.27100	
	15	52.8970	53.2100	53.8360	53.2100	52.5840	52.8970	51.95800	51.95800	52.58400	
	16	53.2100	53.2100	53.5230	52.5840	51.3320	52.2710	52.58400	51.64500	52.58400	
	71	53.2100	54.1490	54.1490	53.5230	52.5840	53.2100	52.89700	52.27100	52.89700	
	72	52.8970	54.4620	53.2100	52.2710	52.5840	52.5840	52.89700	52.27100	52.58400	
	73	52.5840	52.8970	53.2100	52.5840	51.6450	51.9580	52.89700	51.95800	52.58400	
	74	52.8970	53.8360	54.1490	53.5230	52.8970	53.2100	53.83600	52.89700	53.52300	
	75	52.8970	53.5230	54.4620	52.8970	52.2710	52.8970	52.89700	52.27100	52.58400	
	76	53.2100	53.8360	53.5230	53.2100	52.5840	53.2100	53.83600	52.89700	53.52300	
	Min	52.5840	52.8970	53.2100	52.5840	51.3320	51.9580	52.27100	51.64500	52.27100	
	Max	55.0880	54.4620	54.4620	53.5230	52.8970	53.2100	54.14900	52.89700	53.52300	
	Mean	53.1318	53.5491	53.7317	53.0535	52.2449	52.7405	53.10567	52.29708	52.87092	
	Std. Dev	0.6555	0.4896	0.4290	0.3403	0.4711	0.4114	0.55565	0.45178	0.45178	
	Mean - 3 Sigma	51.1651	52.0802	52.4446	52.0327	50.8317	51.5064	51.43871	50.94175	51.51559	
Mean + 3 Sigma	55.0984	55.0179	55.0187	54.0743	53.6582	53.9746	54.77263	53.65241	54.22625		
Limit: 29-x-66ns				Limit: 29-x-66ns				Limit: 29-x-66ns			

Wafer #	SN	CHC Prop Delay Fall 3V, 3V			CHD Prop Delay Fall 3V, 3V			CHA PWD 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	52.5840	52.5840	52.5840	53.5230	53.5230	53.5230	0.62600	0.62600	0.62600	
	68	52.8970	52.8970	52.8970	54.1490	54.1490	54.1490	0.62600	0.31300	0.31300	
	11	53.2100	52.5840	53.5230	54.1490	53.2100	53.8360	0.62600	0.62600	0.62600	
	12	53.2100	52.2710	52.8970	53.5230	52.5840	53.2100	0.31300	0.31300	0.62600	
	13	53.5230	52.5840	53.5230	54.4620	53.2100	54.1490	0.31300	0.93900	0.62600	
	14	52.5840	52.2710	52.8970	53.5230	52.5840	53.5230	0.00000	0.31300	0.31300	
	15	53.5230	52.5840	53.5230	53.8360	52.8970	53.8360	0.00000	0.31300	0.31300	
	16	52.8970	51.9580	52.5840	54.1490	52.8970	53.5230	0.31300	0.93900	0.62600	
	71	52.8970	52.5840	53.2100	53.2100	52.5840	53.2100	0.31300	0.62600	0.62600	
	72	52.8970	52.5840	52.8970	53.8360	53.2100	53.5230	0.00000	0.31300	0.31300	
	73	52.5840	51.9580	52.5840	52.8970	52.2710	52.5840	0.31300	0.62600	0.93900	
	74	52.8970	52.8970	53.2100	54.1490	53.8360	54.1490	0.93900	1.25200	1.25200	
	75	52.5840	52.2710	52.8970	53.2100	52.5840	53.2100	0.93900	0.93900	0.93900	
	76	53.5230	53.2100	53.5230	53.8360	53.2100	53.8360	0.93900	0.93900	0.62600	
	Min	52.5840	51.9580	52.5840	52.8970	52.2710	52.5840	0.00000	0.31300	0.31300	
	Max	53.5230	53.2100	53.5230	54.4620	53.8360	54.1490	0.93900	1.25200	1.25200	
	Mean	53.0274	52.4797	53.1057	53.7317	52.9231	53.5491	0.41733	0.67817	0.65208	
	Std. Dev	0.3645	0.3614	0.3614	0.4687	0.4316	0.4518	0.36142	0.32235	0.28181	
	Mean - 3 Sigma	51.9340	51.3954	52.0214	52.3255	51.6282	52.1938	-0.66693	-0.28887	-0.19333	
Mean + 3 Sigma	54.1209	53.5639	54.1899	55.1378	54.2179	54.9044	1.50160	1.64520	1.49750		
Limit: 29-x-66ns				Limit: 29-x-66ns				Limit: <5ns			

Wafer #	CHB PWD 3V, 3V			ns	CHC PWD 3V, 3V			ns	CHD PWD 3V, 3V			ns
	SN	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal		
CTRL	9	0.0000	0.0000	0.0000	0.0000	0.0000	0.3130	0.62600	0.62600	0.93900		
	68	0.3130	0.0000	0.0000	0.3130	0.6260	0.3130	1.25200	0.93900	1.25200		
	11	0.0000	0.3130	0.0000	1.8780	0.9390	0.6260	0.93900	0.62600	0.31300		
	12	0.3130	0.3130	0.3130	0.0000	0.9390	0.6260	0.62600	0.62600	0.31300		
	13	0.0000	0.6260	0.0000	0.3130	0.9390	0.3130	1.25200	0.31300	0.31300		
	14	0.3130	0.3130	0.3130	0.0000	0.6260	0.6260	0.93900	0.31300	0.31300		
	15	0.3130	0.6260	0.6260	0.3130	0.9390	0.6260	0.93900	0.31300	0.00000		
	16	0.3130	0.6260	2.1910	0.3130	0.9390	0.3130	0.93900	0.31300	0.00000		
	71	0.3130	0.9390	0.6260	0.3130	1.5650	0.9390	0.00000	1.56500	0.93900		
	72	0.0000	0.3130	0.0000	0.6260	2.1910	0.9390	0.93900	1.25200	0.31300		
	73	0.0000	0.3130	0.3130	0.0000	0.9390	0.6260	0.31300	0.62600	0.62600		
	74	0.6260	0.9390	0.9390	0.6260	1.2520	0.9390	1.25200	0.00000	0.00000		
	75	0.9390	0.9390	1.2520	0.0000	0.9390	1.5650	0.31300	0.93900	1.25200		
	76	0.0000	0.6260	0.3130	0.3130	1.2520	0.9390	0.62600	0.62600	0.31300		
	Min	0.0000	0.3130	0.0000	0.0000	0.6260	0.3130	0.00000	0.00000	0.00000		
	Max	0.9390	0.9390	2.1910	1.8780	2.1910	1.5650	1.25200	1.56500	1.25200		
	Mean	0.2608	0.5738	0.5738	0.3913	1.1216	0.7564	0.75642	0.62600	0.39125		
	Std. Dev	0.2934	0.2613	0.6377	0.5191	0.4105	0.3392	0.38816	0.44265	0.38043		
	Mean - 3 Sigma	-0.6194	-0.2101	-1.3394	-1.1659	-0.1098	-0.2611	-0.40805	-0.70195	-0.75004		
	Mean + 3 Sigma	1.1411	1.3578	2.4871	1.9484	2.3530	1.7739	1.92088	1.95395	1.53254		
		Limit: <5ns			Limit: <5ns			Limit: <5ns				

Wafer #	AB_Rise Match Codirection 3V, 3V			ns	AB_Fall Match Codirection 3V, 3V			ns	CD_Rise Match Codirection 3V, 3V			ns
	SN	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal		
CTRL	9	0.0000	0.0000	0.0000	0.6260	0.6260	0.6260	0.31300	0.31300	0.31300		
	68	0.0000	0.0000	0.0000	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300		
	11	0.3130	0.3130	0.0000	0.9390	0.6260	0.6260	0.00000	0.31300	0.00000		
	12	0.0000	0.0000	0.3130	0.0000	0.0000	0.0000	0.31300	0.00000	0.00000		
	13	0.3130	0.3130	0.3130	0.0000	0.0000	0.3130	0.00000	0.00000	0.00000		
	14	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	0.00000	0.00000	0.31300		
	15	0.0000	0.3130	0.0000	0.3130	0.6260	0.3130	0.31300	0.31300	0.31300		
	16	0.0000	0.0000	1.8780	0.0000	0.3130	0.3130	0.62600	0.31300	0.62600		
	71	0.6260	0.0000	0.3130	0.6260	0.3130	0.3130	0.00000	0.00000	0.00000		
	72	0.3130	0.0000	0.3130	0.3130	0.0000	0.0000	0.62600	0.31300	0.62600		
	73	0.0000	0.3130	0.0000	0.3130	0.6260	0.3130	0.31300	0.31300	0.31300		
	74	0.0000	0.0000	1.8780	0.0000	0.3130	0.3130	0.62600	0.31300	0.62600		
	75	0.6260	0.0000	0.3130	0.6260	0.3130	0.3130	0.00000	0.00000	0.00000		
	76	0.3130	0.0000	0.3130	0.3130	0.0000	0.0000	0.62600	0.31300	0.62600		
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000		
	Max	0.6260	0.3130	1.8780	0.9390	0.6260	0.6260	0.62600	0.31300	0.62600		
	Mean	0.2087	0.1043	0.4695	0.3130	0.2608	0.2348	0.28692	0.18258	0.28692		
	Std. Dev	0.2437	0.1541	0.6740	0.2984	0.2613	0.1946	0.28181	0.16117	0.28181		
	Mean - 3 Sigma	-0.5223	-0.3580	-1.5524	-0.5823	-0.5231	-0.3489	-0.55850	-0.30093	-0.55850		
	Mean + 3 Sigma	0.9397	0.5667	2.4914	1.2083	1.0448	0.8184	1.13233	0.66610	1.13233		
		Limit: <5ns			Limit: <5ns			Limit: <5ns				

Wafer #	CD_Fall Match Codirection 3V, 3V			ns	AB_C Rise Match Opposing 3V, 3V			ns	AB_C Fall Match Opposing 3V, 3V			ns
	SN	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal		
CTRL	9	0.9390	0.9390	0.9390	0.3130	0.0000	0.3130	0.31300	0.62600	0.62600		
	68	1.2520	1.2520	1.2520	0.3130	0.0000	0.3130	0.31300	0.62600	0.62600		
	11	0.9390	0.6260	0.3130	1.2520	0.6260	0.6260	0.93900	0.31300	0.62600		
	12	0.3130	0.3130	0.3130	0.0000	0.6260	0.6260	0.31300	0.00000	0.31300		
	13	0.9390	0.6260	0.6260	0.3130	0.3130	0.6260	0.31300	0.00000	0.62600		
	14	0.9390	0.3130	0.6260	0.0000	0.9390	0.9390	0.31300	0.62600	0.62600		
	15	0.3130	0.3130	0.3130	0.0000	0.9390	0.9390	0.62600	0.62600	0.93900		
	16	1.2520	0.9390	0.9390	0.3130	0.6260	1.8780	0.31300	0.62600	0.31300		
	71	0.3130	0.0000	0.0000	0.6260	0.9390	0.6260	0.31300	0.31300	0.31300		
	72	0.9390	0.6260	0.6260	0.6260	2.1910	1.2520	0.31300	0.31300	0.31300		
	73	0.3130	0.3130	0.0000	0.3130	0.6260	0.3130	0.31300	0.31300	0.62600		
	74	1.2520	0.9390	0.9390	0.9390	0.3130	0.9390	0.93900	0.00000	0.31300		
	75	0.6260	0.3130	0.3130	1.2520	0.0000	0.6260	0.31300	0.00000	0.31300		
	76	0.3130	0.0000	0.3130	0.3130	0.9390	0.6260	0.31300	0.62600	0.31300		
	Min	0.3130	0.0000	0.0000	0.0000	0.0000	0.3130	0.31300	0.00000	0.31300		
	Max	1.2520	0.9390	0.9390	1.2520	2.1910	1.8780	0.93900	0.62600	0.93900		
	Mean	0.7043	0.4434	0.4434	0.4956	0.7564	0.7825	0.46950	0.31300	0.46950		
	Std. Dev	0.3804	0.3118	0.3118	0.4518	0.5414	0.4325	0.24969	0.26693	0.21102		
	Mean - 3 Sigma	-0.4370	-0.4920	-0.4920	-0.8597	-0.8679	-0.5149	-0.27956	-0.48778	-0.16357		
	Mean + 3 Sigma	1.8455	1.3789	1.3789	1.8509	2.3808	2.0799	1.21856	1.11378	1.10257		
		Limit: <5ns			Limit: <7ns			Limit: <7ns				

Wafer #	SN	AB_D Rise Match Opposing 3V, 3V			ns			AB_D Fall Match Opposing 3V, 3V			ns			ChA Input Leakage @ 0V 3V, 3V			uA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal			
CTRL	9	0.0000	0.3130	0.0000	1.2520	1.5650	1.5650	-0.00054	-0.00077	0.00036									
	68	0.6260	0.3130	0.6260	1.2520	0.9390	0.9390	0.00022	-0.00062	-0.00094									
	11	1.2520	0.9390	0.6260	0.9390	0.9390	0.9390	0.00022	-0.00016	0.00097									
	12	0.3130	0.6260	0.6260	0.6260	0.3130	0.6260	0.00030	0.00092	-0.00033									
	13	0.3130	0.3130	0.6260	1.2520	0.6260	1.2520	-0.00139	-0.00016	0.00074									
	14	0.0000	0.9390	0.6260	1.2520	0.9390	1.2520	-0.00162	-0.00008	-0.00056									
	15	0.3130	0.6260	0.6260	0.9390	0.9390	1.2520	-0.00116	0.00030	0.00105									
	16	0.3130	0.9390	1.2520	1.5650	1.5650	1.2520	0.00053	0.00007	0.00074									
	71	0.6260	0.9390	0.6260	0.3130	0.3130	0.3130	-0.00216	-0.00016	-0.00048									
	72	0.3130	1.8780	0.6260	0.9390	0.9390	0.9390	-0.00070	0.00030	0.00005									
	73	0.3130	0.6260	0.3130	0.3130	0.6260	0.6260	0.00130	-0.00092	-0.00033									
	74	1.5650	0.3130	0.3130	0.6260	0.9390	0.9390	-0.00039	0.00030	-0.00056									
	75	0.9390	0.3130	0.6260	0.3130	0.3130	0.6260	-0.00001	0.00038	-0.00071									
	76	0.9390	0.3130	0.3130	0.6260	0.6260	0.6260	-0.00016	-0.00115	-0.00048									
	Min	0.0000	0.3130	0.3130	0.3130	0.3130	0.3130	-0.00216	-0.00115	-0.00071									
	Max	1.5650	1.8780	1.2520	1.5650	1.2520	0.00130	0.00092	0.00092	0.00105									
	Mean	0.5999	0.7303	0.5999	0.8086	0.7564	0.8668	-0.00044	-0.00003	0.00001									
	Std. Dev	0.4711	0.4493	0.2482	0.4105	0.3645	0.3223	0.00100	0.00056	0.00067									
	Mean - 3 Sigma	-0.8133	-0.6176	-0.1447	-0.4228	-0.3370	-0.0802	-0.00344	-0.00172	-0.00200									
Mean + 3 Sigma	2.0132	2.0783	1.3445	2.0400	1.8499	1.8539	0.00257	0.00167	0.00202										
Limit: <7ns				Limit: <7ns				Limit: +/-10uA											

Wafer #	SN	ChB Input Leakage @ 0V 3V, 3V			uA			ChC Input Leakage @ 0V 3V, 3V			uA			ChD Input Leakage @ 0V 3V, 3V			uA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal						
CTRL	9	-0.0010	-0.0003	-0.0009	0.0009	0.0004	0.0001	-0.00064	0.00001	-0.00080									
	68	-0.0013	-0.0003	0.0017	-0.0005	0.0003	-0.0013	-0.00017	0.00093	0.00073									
	11	0.0002	0.0002	-0.0011	-0.0002	0.0001	-0.0006	-0.00117	0.00024	-0.00019									
	12	-0.0010	-0.0004	-0.0029	-0.0015	0.0004	-0.0005	0.00082	0.00086	-0.00026									
	13	-0.0006	0.0000	-0.0020	0.0001	0.0011	-0.0011	0.00075	-0.00161	-0.00087									
	14	-0.0004	-0.0009	-0.0016	0.0011	0.0010	0.0003	-0.00156	0.00066	0.00066									
	15	-0.0006	0.0013	0.0000	-0.0011	-0.0004	0.0012	-0.00071	0.00070	0.00020									
	16	-0.0004	-0.0011	0.0002	-0.0006	0.0005	-0.0004	-0.00110	0.00032	-0.00064									
	71	0.0006	-0.0009	-0.0013	-0.0006	-0.0001	0.0005	-0.00090	-0.00007	0.00012									
	72	-0.0018	0.0001	0.0003	0.0004	0.0012	0.0007	0.00082	-0.00099	-0.00240									
	73	-0.0004	-0.0012	-0.0003	0.0003	-0.0013	-0.0014	-0.00064	0.00024	0.00058									
	74	-0.0013	0.0009	-0.0005	-0.0008	0.0001	-0.0003	0.00021	-0.00014	-0.00080									
	75	0.0003	0.0004	-0.0009	-0.0006	-0.0009	0.0001	-0.00017	0.00093	-0.00149									
	76	-0.0008	-0.0001	0.0001	-0.0011	-0.0014	-0.0012	-0.00087	0.00093	-0.00049									
	Min	-0.0018	-0.0012	-0.0029	-0.0015	-0.0014	-0.0014	-0.00156	-0.00161	-0.00240									
	Max	0.0006	0.0013	0.0003	0.0011	0.0012	0.0012	0.00090	0.00093	0.00066									
	Mean	-0.0005	-0.0001	-0.0008	-0.0004	0.0000	-0.0002	-0.00023	0.00008	-0.00047									
	Std. Dev	0.0007	0.0008	0.0010	0.0007	0.0009	0.0008	0.00090	0.00079	0.00087									
	Mean - 3 Sigma	-0.0026	-0.0025	-0.0038	-0.0026	-0.0026	-0.0026	-0.00292	-0.00230	-0.00308									
Mean + 3 Sigma	0.0015	0.0022	0.0021	0.0018	0.0027	0.0022	0.00246	0.00246	0.00215										
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA											

Wafer #	SN	ChA Input Leakage @ Vdd1 3V, 3V			uA			ChB Input Leakage @ Vdd1 3V, 3V			uA			ChC Input Leakage @ Vdd2 3V, 3V			uA		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal						
CTRL	9	0.0028	0.0015	0.0009	0.0020	0.0024	0.0015	0.00006	-0.00048	-0.00010									
	68	0.0029	0.0009	-0.0003	0.0005	0.0014	0.0027	0.00106	0.00075	-0.00071									
	11	0.0015	0.0034	0.0011	0.0005	0.0014	0.0027	0.00129	0.00052	-0.00033									
	12	0.0012	0.0027	0.0014	0.0004	0.0006	0.0015	-0.00048	0.00129	0.00028									
	13	0.0023	0.0015	0.0024	0.0001	0.0019	0.0016	-0.00002	0.00129	0.00074									
	14	0.0028	0.0025	0.0026	0.0018	0.0019	0.0023	-0.00109	0.00006	0.00097									
	15	0.0012	0.0028	0.0006	0.0024	0.0008	0.0015	0.00098	0.00029	-0.00132									
	16	0.0015	0.0001	0.0024	0.0019	0.0018	0.0011	0.00006	0.00091	-0.00079									
	71	0.0015	0.0007	0.0015	0.0020	0.0018	0.0013	-0.00002	0.00191	0.00036									
	72	0.0014	0.0008	0.0022	0.0008	0.0013	0.0021	0.00014	0.00021	0.00028									
	73	0.0031	0.0003	0.0034	0.0003	0.0005	-0.0003	0.00029	0.00060	0.00005									
	74	0.0030	0.0015	0.0019	-0.0004	0.0015	0.0017	0.00044	0.00044	0.00112									
	75	0.0018	0.0015	0.0007	-0.0001	0.0004	0.0018	0.00190	-0.00040	0.00227									
	76	0.0017	0.0023	0.0024	-0.0004	0.0028	0.0008	0.00052	-0.00017	0.00005									
	Min	0.0012	0.0001	0.0006	-0.0004	-0.0002	-0.0003	-0.00109	-0.00040	-0.00132									
	Max	0.0031	0.0034	0.0034	0.0024	0.0028	0.0027	0.00190	0.00191	0.00227									
	Mean	0.0019	0.0017	0.0019	0.0008	0.0012	0.0015	0.00044	0.00058	0.00031									
	Std. Dev	0.0007	0.0011	0.0008	0.0010	0.0009	0.0008	0.00089	0.00067	0.00093									
	Mean - 3 Sigma	-0.0002	-0.0015	-0.0006	-0.0022	-0.0013	-0.0008	-0.00223	-0.00143	-0.00249									
Mean + 3 Sigma	0.0040	0.0048	0.0044	0.0038	0.0038	0.0038	0.00312	0.00259	0.00310										
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA											

Wafer #	SN	Chd Input Leakage @ Vdd2 3V, 3V			Voh_A @ -20uA 3V, 3V			Voh_B @ -20uA 3V, 3V			V
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	-0.0002	0.0009	0.0004	2.9976	2.9957	2.9925	2.99720	2.99580	2.99486	
	68	0.0007	-0.0008	-0.0001	2.9979	2.9970	2.9924	2.99776	2.99650	2.99441	
	11	-0.0005	0.0011	0.0003	2.9967	2.9962	2.9925	2.99640	2.99597	2.99505	
	12	0.0008	0.0017	0.0000	2.9981	2.9966	2.9926	2.99793	2.99658	2.99479	
	13	0.0004	0.0011	-0.0001	2.9980	2.9953	2.9927	2.99793	2.99545	2.99500	
	14	0.0011	0.0013	0.0017	2.9979	2.9965	2.9924	2.99782	2.99712	2.99482	
	15	0.0006	0.0002	0.0017	2.9982	2.9968	2.9925	2.99782	2.99638	2.99489	
	16	0.0000	0.0000	-0.0002	2.9978	2.9965	2.9914	2.99801	2.99694	2.99362	
	71	-0.0003	0.0009	-0.0003	2.9979	2.9970	2.9920	2.99778	2.99652	2.99438	
	72	0.0002	0.0009	0.0006	2.9978	2.9965	2.9923	2.99778	2.99668	2.99489	
	73	0.0014	0.0012	0.0014	2.9978	2.9964	2.9919	2.99776	2.99657	2.99435	
	74	0.0001	0.0001	0.0013	2.9980	2.9967	2.9923	2.99782	2.99636	2.99480	
	75	0.0007	0.0018	0.0002	2.9969	2.9965	2.9925	2.99657	2.99625	2.99504	
	76	0.0001	0.0007	0.0000	2.9977	2.9967	2.9925	2.99733	2.99690	2.99483	
	Min	-0.0005	0.0000	-0.0003	2.9967	2.9953	2.9914	2.99640	2.99545	2.99362	
	Max	0.0014	0.0018	0.0017	2.9982	2.9970	2.9927	2.99801	2.99712	2.99505	
	Mean	0.0004	0.0009	0.0006	2.9977	2.9965	2.9923	2.99758	2.99648	2.99471	
	Std. Dev	0.0006	0.0006	0.0008	0.0005	0.0004	0.0004	0.00054	0.00045	0.00041	
Mean - 3 Sigma	-0.0013	-0.0008	-0.0017	2.9963	2.9952	2.9912	2.99596	2.99512	2.99348		
Mean + 3 Sigma	0.0021	0.0027	0.0029	2.9991	2.9977	2.9933	2.99919	2.99784	2.99593		
Limit: +/-10uA				Limit: >2.9V			Limit: >2.9V				

Wafer #	SN	Voh_C @ -20uA 3V, 3V			V	Voh_D @ -20uA 3V, 3V			Voh_A @ -4mA 3V, 3V			V
		PRE	100k	Anneal		PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.9961	2.9962	2.9951	2.9961	2.9957	2.9950	2.70836	2.70253	2.70483		
	68	2.9972	2.9946	2.9949	2.9967	2.9932	2.9951	2.70742	2.70484	2.69977		
	11	2.9920	2.9952	2.9952	2.9920	2.9935	2.9951	2.69160	2.69154	2.69077		
	12	2.9971	2.9948	2.9951	2.9969	2.9933	2.9948	2.69638	2.69485	2.68749		
	13	2.9970	2.9947	2.9950	2.9968	2.9939	2.9951	2.71158	2.68617	2.69071		
	14	2.9970	2.9951	2.9951	2.9968	2.9941	2.9950	2.71161	2.69669	2.68971		
	15	2.9970	2.9945	2.9950	2.9969	2.9937	2.9948	2.71058	2.69756	2.69005		
	16	2.9970	2.9935	2.9951	2.9968	2.9924	2.9949	2.71161	2.69709	2.68137		
	71	2.9972	2.9937	2.9949	2.9967	2.9925	2.9949	2.70851	2.68929	2.67784		
	72	2.9969	2.9919	2.9952	2.9966	2.9904	2.9951	2.70948	2.69428	2.68818		
	73	2.9971	2.9946	2.9951	2.9968	2.9940	2.9949	2.71039	2.69678	2.68359		
	74	2.9970	2.9949	2.9952	2.9969	2.9934	2.9948	2.71089	2.69163	2.68305		
	75	2.9970	2.9942	2.9931	2.9970	2.9936	2.9933	2.70398	2.69151	2.68580		
	76	2.9971	2.9945	2.9952	2.9969	2.9936	2.9948	2.70611	2.69060	2.68405		
	Min	2.9920	2.9919	2.9931	2.9920	2.9904	2.9933	2.69160	2.68617	2.67784		
	Max	2.9972	2.9952	2.9952	2.9970	2.9941	2.9951	2.71161	2.69756	2.69077		
	Mean	2.9966	2.9943	2.9949	2.9964	2.9932	2.9948	2.70689	2.69317	2.68605		
	Std. Dev	0.0014	0.0009	0.0006	0.0014	0.0010	0.0005	0.00654	0.00360	0.00414		
Mean - 3 Sigma	2.9923	2.9915	2.9932	2.9922	2.9901	2.9934	2.68726	2.68237	2.67363			
Mean + 3 Sigma	3.0009	2.9970	2.9967	3.0006	2.9963	2.9962	2.72653	2.70397	2.69847			
Limit: >2.9V				Limit: >2.9V			Limit: >2.6V					

Wafer #	SN	Voh_B @ -4mA 3V, 3V			V	Voh_C @ -4mA 3V, 3V			Voh_D @ -4mA 3V, 3V			V
		PRE	100k	Anneal		PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.6790	2.6970	2.6970	2.7082	2.7116	2.7103	2.70441	2.69105	2.70594		
	68	2.6996	2.6717	2.6168	2.7092	2.7041	2.6983	2.70601	2.69264	2.70304		
	11	2.6735	2.6853	2.6805	2.6690	2.6930	2.6791	2.67734	2.68088	2.69345		
	12	2.6830	2.6885	2.6858	2.7123	2.6956	2.6984	2.70554	2.68506	2.69733		
	13	2.7066	2.6755	2.6752	2.7108	2.6922	2.6979	2.70782	2.68768	2.69517		
	14	2.6877	2.6730	2.6753	2.7074	2.6947	2.6983	2.70917	2.68718	2.69561		
	15	2.7036	2.6774	2.6739	2.7076	2.6948	2.6988	2.70773	2.68971	2.69551		
	16	2.7048	2.6830	2.6602	2.7092	2.6886	2.6984	2.70207	2.68294	2.69573		
	71	2.7020	2.6641	2.6768	2.7083	2.6822	2.6934	2.70604	2.67922	2.69133		
	72	2.6944	2.6661	2.6686	2.7076	2.6746	2.6972	2.70529	2.67083	2.69545		
	73	2.6883	2.6743	2.6662	2.7115	2.6945	2.6978	2.70938	2.69096	2.69542		
	74	2.6914	2.6859	2.6728	2.7067	2.6884	2.6933	2.70410	2.68347	2.68718		
	75	2.6847	2.6685	2.6748	2.7078	2.6888	2.6824	2.70529	2.68088	2.67510		
	76	2.6934	2.6739	2.6673	2.7087	2.6917	2.6955	2.70510	2.68562	2.68731		
	Min	2.6735	2.6641	2.6602	2.6690	2.6746	2.6791	2.67734	2.67083	2.67510		
	Max	2.7066	2.6885	2.6858	2.7123	2.6956	2.6988	2.70938	2.69096	2.69733		
	Mean	2.6928	2.6763	2.6731	2.7056	2.6899	2.6942	2.70374	2.68370	2.69205		
	Std. Dev	0.0101	0.0080	0.0068	0.0117	0.0062	0.0066	0.00858	0.00543	0.00629		
Mean - 3 Sigma	2.6625	2.6522	2.6527	2.6706	2.6714	2.6744	2.67801	2.66741	2.67319			
Mean + 3 Sigma	2.7230	2.7003	2.6935	2.7405	2.7084	2.7140	2.72947	2.70000	2.71091			
Limit: >2.6V				Limit: >2.6V			Limit: >2.6V					

Wafer #	SN	Vol_CHA @ 20uA 3V, 3V			Vol_CHB @ 20uA 3V, 3V			Vol_CHC @ 20uA 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	4.5708	1.0210	0.1959	4.7695	1.0664	0.7912	1.83200	1.63432	1.47000	
	68	1.1755	1.1584	0.6081	1.3447	1.4536	1.1800	1.61958	2.03383	1.36000	
	11	2.0634	1.3207	0.6955	2.2509	1.5161	1.2900	1.65707	1.68426	1.48000	
	12	1.0942	1.4517	0.5331	1.1385	1.4474	1.0300	2.22559	1.82784	1.36000	
	13	0.8753	1.3581	0.6393	1.0260	1.3100	1.2300	2.10064	1.81535	1.34000	
	14	0.8628	1.1271	1.3800	1.0260	1.1164	1.9500	2.01942	1.70299	1.31000	
	15	0.9191	0.9961	0.2084	0.9510	1.1039	0.9597	1.92571	1.77790	1.35000	
	16	1.1192	0.9024	0.3021	1.2072	0.9790	0.9347	1.65707	1.81535	1.11000	
	71	1.1255	1.1209	0.2958	1.1447	1.2288	0.9160	1.56336	1.70923	1.23000	
	72	0.8816	1.0585	0.3270	1.0072	1.1913	0.7849	1.55711	1.54069	1.18000	
	73	0.9316	1.0460	0.3083	1.0760	1.2100	0.9909	1.53212	1.84032	1.28000	
	74	1.5882	1.2270	0.3395	1.6697	1.1476	0.8786	1.80076	1.92147	1.16000	
	75	1.0129	0.9524	0.1834	1.2072	1.1476	0.9472	1.50713	2.68303	1.17000	
	76	1.1692	1.6577	0.4082	1.2385	1.4848	0.8411	1.90072	1.85280	1.31000	
	Min	0.8628	0.9024	0.1834	0.9510	0.9790	0.7849	1.50713	1.54069	1.11000	
	Max	2.0634	1.6577	1.3800	2.2509	1.5161	1.9500	2.22559	2.68303	1.48000	
	Mean	1.1369	1.1849	0.4684	1.2452	1.2402	1.0628	1.78723	1.84760	1.27333	
	Std. Dev	0.3541	0.2251	0.3286	0.3673	0.1669	0.3155	0.24440	0.28151	0.10620	
	Mean - 3 Sigma	0.0747	0.5097	-0.5176	0.1434	0.7395	0.1161	1.05403	1.00307	0.95473	
Mean + 3 Sigma	2.1991	1.8601	1.4543	2.3471	1.7410	2.0094	2.52042	2.69214	1.59194		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHD @ 20uA 3V, 3V			Vol_CHA @ 400uA 3V, 3V			Vol_CHB @ 400uA 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.7435	1.4440	1.2700	24.8428	20.4159	19.8200	27.47418	20.99524	21.40000	
	68	1.5060	1.6874	1.1100	20.7159	20.9278	20.0900	21.73707	24.36147	27.30000	
	11	1.5248	1.3504	1.3700	22.7231	20.5907	19.8400	24.63687	21.33873	22.00000	
	12	2.1623	1.5314	1.1300	21.6976	20.6718	19.9400	23.26197	21.52609	20.99000	
	13	1.8873	1.5127	1.1300	20.3782	20.5907	19.9600	20.96212	22.13188	22.54000	
	14	1.9560	1.4316	1.0700	20.3595	20.2599	20.8000	22.91824	22.70645	23.23000	
	15	1.9373	1.5252	1.2800	20.3970	20.0289	19.4700	21.26210	22.28177	22.31000	
	16	1.5623	1.4690	1.0800	20.4720	19.8728	19.5700	21.34960	21.42616	22.48000	
	71	1.4435	1.3255	1.0800	20.5220	20.5657	19.6000	21.43709	23.28103	20.88000	
	72	1.4873	1.3754	1.1200	20.4032	20.1225	19.3500	22.07455	22.81263	22.25000	
	73	1.4310	1.5377	0.8787	20.2344	20.0102	19.4700	22.71200	22.42541	22.08000	
	74	1.6810	1.5689	1.1000	21.0410	20.1225	19.5200	23.03073	20.90156	21.50000	
	75	1.3623	2.4114	1.0300	20.4157	19.9602	19.3800	22.52452	22.55032	21.33000	
	76	1.7373	1.7062	1.1200	20.5971	20.6718	19.5100	22.12454	22.63776	22.16000	
	Min	1.3623	1.3255	0.8787	20.2344	19.8728	19.3500	20.96212	20.90156	20.88000	
	Max	2.1623	2.4114	1.3700	22.7231	20.6718	20.8000	24.63687	23.28103	23.23000	
	Mean	1.6810	1.5621	1.1157	20.7701	20.6718	19.7008	22.35786	22.16832	21.97917	
	Std. Dev	0.2550	0.2874	0.1208	0.7346	0.3067	0.4024	1.04791	0.71640	0.68713	
	Mean - 3 Sigma	0.9160	0.6998	0.7533	18.5662	19.3689	18.4937	19.21414	20.01910	19.91776	
Mean + 3 Sigma	2.4460	2.4244	1.4781	22.9740	21.2091	20.9079	25.50158	24.31753	24.04057		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHC @ 400uA 3V, 3V			Vol_CHD @ 400uA 3V, 3V			Vol_CHA @ 4mA 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	21.9862	21.7970	21.5900	22.3887	23.5498	22.0300	220.00915	206.66800	206.69000	
	68	21.9050	22.3838	22.4200	22.2699	23.2440	21.7800	209.23536	210.70054	209.58000	
	11	22.9546	21.5847	23.1700	22.0949	22.3141	21.9200	221.97257	204.46447	204.98000	
	12	22.5610	21.4599	21.3600	23.1825	22.2767	21.4300	221.30351	204.94513	205.33000	
	13	22.3673	21.6534	21.1400	22.7512	21.8086	21.4100	206.96555	205.14488	205.02000	
	14	22.7234	21.3101	21.0700	22.6512	21.7649	21.4600	206.76546	203.74036	207.13000	
	15	22.4111	21.6534	21.1400	22.5824	21.7836	21.7200	207.14062	203.19104	204.36000	
	16	22.0675	21.5910	21.0600	22.7449	21.7711	21.5500	207.62835	202.04869	204.20000	
	71	21.9050	21.8719	21.4700	22.1949	21.8398	21.8000	208.20987	205.83154	205.30000	
	72	21.8675	21.6909	21.3500	22.2324	21.6338	21.4100	208.53503	204.40204	204.15000	
	73	21.6176	21.3975	21.2100	22.0011	21.6588	21.4100	206.44656	202.79777	203.56000	
	74	22.2299	22.0155	21.3300	22.6387	22.2829	22.1300	209.01026	202.71038	204.02000	
	75	21.9300	22.9705	21.4900	22.1637	23.3376	22.2600	206.83423	202.72286	203.13000	
	76	22.3736	21.9281	21.3200	22.6012	22.3828	22.0600	208.03480	205.64426	203.80000	
	Min	21.6176	21.3101	21.0600	22.0011	21.6338	21.4100	206.44656	202.04869	203.13000	
	Max	22.9546	22.9705	23.1700	23.1825	23.3376	22.2600	221.97257	205.83154	207.13000	
	Mean	22.2507	21.7606	21.4258	22.4866	22.0712	21.7133	209.90390	203.97029	204.58167	
	Std. Dev	0.3901	0.4351	0.5680	0.3486	0.4859	0.3148	5.53758	1.27557	1.06086	
	Mean - 3 Sigma	21.0805	20.4554	19.7218	21.4408	20.6134	20.7689	193.29115	200.14357	201.39910	
Mean + 3 Sigma	23.4209	23.0658	23.1299	23.5323	23.5290	22.6577	226.51665	207.79700	207.76423		
Limit: <100mV				Limit: <100mV				Limit: <400mV			

Wafer #	SN	Vol_CHB @ 4mA 3V, 3V			Vol_CHC @ 4mA 3V, 3V			Vol_CHD @ 4mA 3V, 3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	247.7028	213.4701	218.3600	215.3820	214.4159	215.1800	219.96446	234.79690	220.66000	
	68	218.8798	245.9208	271.5500	217.8310	219.9154	226.8600	221.65832	230.48432	221.96000	
	11	241.7095	213.0204	222.3900	227.5895	211.7005	232.7100	220.80201	224.21208	219.75000	
	12	236.9411	214.4069	212.8600	217.8935	210.9826	213.6400	225.22731	221.55963	216.93000	
	13	213.6114	220.0714	227.4500	217.4186	212.5370	213.9400	222.29587	217.22209	217.60000	
	14	232.6351	229.5955	228.0000	220.0551	209.8341	211.9300	220.45824	218.45159	216.44000	
	15	216.4362	225.8608	228.1200	219.8551	212.0189	213.2900	221.31455	218.13953	218.21000	
	16	215.6925	217.7669	229.7500	217.6873	210.7954	213.1100	226.58364	216.02382	218.00000	
	71	217.0987	234.3545	213.0300	217.5623	215.1588	216.4600	220.98953	218.63258	220.60000	
	72	225.3481	232.5933	228.3800	218.1246	213.2299	214.4400	221.45206	216.95997	217.97000	
	73	231.0790	227.9655	226.8500	215.3258	210.6580	213.2200	219.90195	215.21248	217.35000	
	74	230.6665	211.6402	219.9200	220.1550	215.4709	216.8100	224.06472	221.04162	224.91000	
	75	227.6292	227.7032	218.4000	218.1871	217.1625	217.4500	221.91459	225.79105	226.02000	
	76	223.4107	225.1863	226.2500	219.3304	214.7967	216.0000	223.75846	221.15396	224.16000	
	Min	213.6114	211.6402	212.8600	215.3258	209.8341	211.9300	219.90195	215.21248	216.44000	
	Max	241.7095	234.3545	229.7500	227.5895	217.1625	232.7100	226.58364	225.79105	226.02000	
	Mean	226.0215	223.3471	223.4500	219.0987	212.8621	216.0833	222.39691	219.53337	219.82833	
	Std. Dev	9.0367	7.7530	6.0432	3.0059	2.3051	5.5132	2.06684	3.26039	3.35653	
	Mean - 3 Sigma	198.9113	200.0882	205.3205	210.0809	205.9468	199.5438	216.19639	209.75219	209.75875	
Mean + 3 Sigma	253.1317	246.6059	241.5795	228.1165	219.7774	232.6229	228.59743	229.31454	229.89791		
Limit: <400mV				Limit: <400mV				Limit: <400mV			

Wafer #	SN	Idd1 Quiescent 3.6V, 3.6V			Idd2 Quiescent 3.6V, 3.6V			Idd1 Dynamic 2MBPS 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.9259	1.9039	1.9417	1.8941	1.9344	1.9112	2.59596	2.56451	2.56589	
	68	1.9027	1.7879	1.8998	1.9348	1.9613	1.9487	2.60414	2.47938	2.56508	
	11	1.8864	1.8347	1.9308	1.9092	1.9470	1.9369	2.60300	2.46522	2.57688	
	12	1.8958	1.8347	1.9141	1.9006	1.9405	1.9283	2.57704	2.44268	2.54620	
	13	1.8864	1.8282	1.8840	1.8954	1.9421	1.9279	2.57879	2.43524	2.55234	
	14	1.9267	1.8425	1.9291	1.8925	1.9320	1.8974	2.59474	2.45680	2.56504	
	15	1.9267	1.8347	1.9320	1.9181	1.9539	1.9478	2.59686	2.45672	2.57208	
	16	1.9381	1.8477	1.9580	1.9165	1.9478	1.9373	2.60264	2.44854	2.56353	
	71	1.8970	1.8376	1.9141	1.9458	1.9820	1.9641	2.59466	2.44085	2.55906	
	72	1.9051	1.8453	1.9438	1.9242	1.9377	1.9466	2.61069	2.46262	2.57358	
	73	1.9185	1.8547	1.9263	1.9690	1.9527	1.9576	2.60223	2.45998	2.57012	
	74	1.8925	1.8160	1.9259	1.9234	1.9914	1.9450	2.61232	2.44748	2.56907	
	75	1.9409	1.8477	1.9214	1.9442	1.9812	1.9763	2.61049	2.45473	2.56369	
	76	1.8799	1.8217	1.9010	1.9181	1.9519	1.9458	2.60040	2.45452	2.56040	
	Min	1.8799	1.8160	1.8840	1.8925	1.9320	1.8974	2.57704	2.43524	2.54620	
	Max	1.9409	1.8547	1.9580	1.9690	1.9914	1.9763	2.61232	2.46522	2.57688	
	Mean	1.9078	1.8371	1.9234	1.9214	1.9550	1.9426	2.59866	2.45212	2.56433	
	Std. Dev	0.0214	0.0113	0.0191	0.0224	0.0192	0.0200	0.01136	0.00922	0.00893	
	Mean - 3 Sigma	1.8436	1.8032	1.8660	1.8542	1.8973	1.8825	2.56459	2.42446	2.53756	
Mean + 3 Sigma	1.9720	1.8711	1.9807	1.9887	2.0128	2.0027	2.63272	2.47977	2.59111		
Limit: <2.4mA				Limit: <2.3mA				Limit: <13mA			

Wafer #	SN	Idd2 Dynamic 2MBPS 3.6V, 3.6V			Idd1 Dynamic 10MBPS 3.6V, 3.6V			Idd2 Dynamic 10MBPS 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	2.5838	2.5697	2.5731	6.0964	6.0840	6.0265	5.91808	5.86954	5.87564	
	68	2.6247	2.5965	2.5897	6.2644	6.0743	6.1386	6.04634	5.94604	5.95308	
	11	2.5848	2.5776	2.5721	6.2346	5.8467	6.0798	5.94596	5.79064	5.85363	
	12	2.5764	2.5593	2.5556	6.0964	5.7095	5.9418	6.00138	5.79561	5.86323	
	13	2.5975	2.5783	2.5715	6.2349	5.7792	6.0084	6.04215	5.82051	5.87638	
	14	2.5820	2.5718	2.5564	6.1033	5.7512	5.9603	5.96740	5.80354	5.83805	
	15	2.6074	2.5871	2.5827	6.1915	5.8245	6.0380	6.08027	5.86714	5.93074	
	16	2.5956	2.5736	2.5715	6.1522	5.7268	5.9712	6.01720	5.78381	5.85127	
	71	2.6187	2.6008	2.5947	6.2298	5.7723	6.0039	6.03165	5.82441	5.86051	
	72	2.5998	2.5740	2.5612	6.2286	5.7973	6.0202	6.01598	5.81412	5.82120	
	73	2.6438	2.6218	2.6081	6.2021	5.7550	5.9794	6.10367	5.91357	5.93119	
	74	2.6107	2.6013	2.5915	6.2687	5.7561	6.0561	5.99763	5.82417	5.85335	
	75	2.6216	2.6008	2.5972	6.2599	5.7483	6.0433	5.97216	5.76838	5.80985	
	76	2.6194	2.6105	2.6094	6.2781	5.8529	6.0686	6.03873	5.87463	5.93831	
	Min	2.5764	2.5593	2.5556	6.0964	5.7095	5.9418	5.94596	5.76838	5.80985	
	Max	2.6438	2.6218	2.6094	6.2781	5.8529	6.0798	6.10367	5.91357	5.93831	
	Mean	2.6048	2.5881	2.5810	6.2067	5.7766	6.0142	6.01785	5.82338	5.86898	
	Std. Dev	0.0194	0.0187	0.0191	0.0606	0.0455	0.0445	0.04569	0.04221	0.04283	
	Mean - 3 Sigma	2.5466	2.5320	2.5238	6.0248	5.6400	5.8807	5.88078	5.69675	5.74048	
Mean + 3 Sigma	2.6630	2.6441	2.6382	6.3886	5.9132	6.1478	6.15492	5.95001	5.99747		
Limit: <13mA				Limit: <13mA				Limit: <13mA			

Wafer #	SN	Idd1 Dynamic 25MBPS 3.6V, 3.6V			mA			Idd2 Dynamic 25MBPS 3.6V, 3.6V			mA			CHA Prop Delay Rise 3.6V, 3.6V			ns				
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal					
CTRL	9	12.3714	12.3308	12.2332	11.9122	11.8418	11.9252	45.07200	45.07200	45.38500	68	12.8059	12.4858	12.5413	12.3211	12.0574	12.1133	46.32400	46.32400	46.32400	
	11	12.7009	11.8616	12.3551	12.0586	11.6520	11.8833	46.01100	45.07200	45.69800	12	12.4173	11.5570	12.0187	12.2370	11.6630	11.8858	46.01100	45.07200	45.69800	
	12	12.4173	11.5570	12.0187	12.2370	11.6630	11.8858	46.01100	45.07200	45.69800	13	12.7177	11.7289	12.2207	12.3625	11.7100	11.9526	46.32400	45.38500	46.01100	
	14	12.4187	11.6373	12.0695	12.1317	11.6597	11.8006	45.38500	44.75900	45.07200	15	12.6620	11.8280	12.2880	12.4242	11.8249	12.0275	46.01100	45.38500	45.69800	
	16	12.5428	11.5629	12.0799	12.2931	11.6303	11.8270	45.69800	44.75900	45.38500	71	12.7914	11.6966	12.1864	12.2991	11.6992	11.8745	46.32400	45.69800	46.32400	
	72	12.7435	11.7374	12.1937	12.2963	11.7121	11.8039	46.01100	45.07200	45.69800	72	12.7435	11.7374	12.1937	12.2963	11.7121	11.8039	46.01100	45.07200	45.69800	
	73	12.6261	11.6255	12.0825	12.4591	11.9160	11.9969	45.69800	45.07200	45.38500	73	12.6261	11.6255	12.0825	12.4591	11.9160	11.9969	45.69800	45.07200	45.38500	
	74	12.8965	11.7299	12.3186	12.1681	11.6836	11.8176	46.95000	46.01100	46.63700	74	12.8965	11.7299	12.3186	12.1681	11.6836	11.8176	46.95000	46.01100	46.63700	
	75	12.8248	11.6575	12.2409	12.0718	11.5220	11.7143	46.01100	45.38500	46.01100	75	12.8248	11.6575	12.2409	12.0718	11.5220	11.7143	46.01100	45.38500	46.01100	
	76	12.8694	11.8737	12.3283	12.3179	11.8206	12.0537	46.63700	46.01100	46.32400	76	12.8694	11.8737	12.3283	12.3179	11.8206	12.0537	46.63700	46.01100	46.32400	
	Min	12.4173	11.5570	12.0187	12.0586	11.5220	11.7143	45.38500	44.75900	45.07200	Max	12.8965	11.8737	12.3551	12.4591	11.9160	12.0537	46.95000	46.01100	46.37000	
	Mean	12.6843	11.7080	12.1985	12.2599	11.7078	11.8865	46.08925	45.30675	45.82842	Mean	12.6843	11.7080	12.1985	12.2599	11.7078	11.8865	46.08925	45.30675	45.82842	
	Std. Dev	0.1599	0.1068	0.1139	0.1300	0.1038	0.1030	0.42468	0.42468	0.45178	Std. Dev	0.1599	0.1068	0.1139	0.1300	0.1038	0.1030	0.42468	0.42468	0.45178	
	Mean - 3 Sigma	12.2047	11.3875	11.8567	11.8700	11.3965	11.5774	44.81521	44.03271	44.47309	Mean - 3 Sigma	12.2047	11.3875	11.8567	11.8700	11.3965	11.5774	44.81521	44.03271	44.47309	
	Mean + 3 Sigma	13.1639	12.0285	12.5403	12.6499	12.0191	12.1955	47.36329	46.58079	47.18375	Mean + 3 Sigma	13.1639	12.0285	12.5403	12.6499	12.0191	12.1955	47.36329	46.58079	47.18375	
		Limit: <13mA			Limit: <13mA			Limit: 29<x<66nS													

Wafer #	SN	CHB Prop Delay Rise 3.6V, 3.6V			ns			CHC Prop Delay Rise 3.6V, 3.6V			ns			CHD Prop Delay Rise 3.6V, 3.6V			ns				
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal					
CTRL	9	45.3850	45.3850	45.3850	45.0720	45.0720	45.0720	45.38500	45.38500	45.38500	68	46.0110	46.0110	46.3240	45.6980	45.6980	45.6980	45.38500	45.69800	45.69800	
	11	46.3240	45.3850	46.0110	45.6980	45.0720	45.6980	46.01100	45.38500	45.69800	12	46.3240	45.3850	46.0110	45.6980	45.0720	45.6980	46.01100	45.38500	45.69800	
	12	45.6980	45.0720	45.3850	45.6980	45.0720	45.3850	45.69800	45.07200	45.38500	13	46.0110	45.0720	45.6980	45.6980	45.0720	45.6980	46.01100	45.07200	45.38500	
	14	45.0720	44.4460	45.0720	45.3850	44.7590	45.3850	45.38500	44.75900	45.07200	15	45.6980	44.7590	45.3850	45.6980	45.0720	45.6980	45.38500	45.07200	45.38500	
	16	45.6980	44.7590	45.3850	45.0720	44.4460	45.0720	45.69800	45.07200	45.38500	71	45.6980	44.7590	45.3850	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	
	72	45.3850	44.7590	45.0720	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	72	45.3850	44.7590	45.0720	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	
	73	45.3850	44.7590	45.0720	44.4460	45.0720	45.0720	45.07200	44.44600	45.07200	73	45.3850	44.7590	45.0720	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	
	74	46.3240	45.6980	46.3240	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	74	46.3240	45.6980	46.3240	45.6980	45.0720	45.3850	45.38500	45.07200	45.38500	
	75	45.6980	45.3850	45.6980	45.0720	44.4460	45.0720	45.07200	44.75900	45.07200	75	45.6980	45.3850	45.6980	45.0720	44.4460	45.0720	45.07200	44.75900	45.07200	
	76	46.3240	45.3850	46.0110	46.0110	45.3850	46.0110	45.69800	45.07200	45.38500	76	46.3240	45.3850	46.0110	46.0110	45.3850	46.0110	45.69800	45.07200	45.38500	
	Min	45.0720	44.4460	45.0720	45.0720	44.4460	45.0720	45.07200	44.44600	45.07200	Max	46.3240	45.6980	46.3240	46.0110	45.3850	46.0110	45.38500	45.07200	45.69800	
	Mean	45.7763	45.0720	45.5676	45.5154	44.9155	45.4372	45.51542	44.99375	45.35892	Mean	45.7763	45.0720	45.5676	45.5154	44.9155	45.4372	45.51542	44.99375	45.35892	
	Std. Dev	0.4032	0.3775	0.4105	0.3118	0.3130	0.2934	0.31181	0.23593	0.20926	Std. Dev	0.4032	0.3775	0.4105	0.3118	0.3130	0.2934	0.31181	0.23593	0.20926	
	Mean - 3 Sigma	44.5668	43.9395	44.3362	44.5800	43.9765	44.5569	44.57998	44.28595	44.73114	Mean - 3 Sigma	44.5668	43.9395	44.3362	44.5800	43.9765	44.5569	44.57998	44.28595	44.73114	
	Mean + 3 Sigma	46.9857	46.2045	46.7990	46.4509	45.8545	46.3174	46.45085	45.70155	45.98669	Mean + 3 Sigma	46.9857	46.2045	46.7990	46.4509	45.8545	46.3174	46.45085	45.70155	45.98669	
		Limit: 29<x<66nS			Limit: 29<x<66nS			Limit: 29<x<66nS													

Wafer #	SN	CHA Prop Delay Fall 3.6V, 3.6V			ns			CHB Prop Delay Fall 3.6V, 3.6V			ns			CHC Prop Delay Fall 3.6V, 3.6V			ns			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal				
CTRL	9	45.0720	45.0720	45.0720	45.6980	45.6980	45.6980	45.69800	45.69800	45.69800	68	46.3240	46.6370	46.6370	46.3240	46.6370	46.6370	46.01100	46.01100	46.01100
	11	46.6370	45.3850	46.0110	47.2630	46.0110	46.6370	46.32400	45.69800	46.32400	12	46.3240	45.3850	46.0110	46.3240	45.6980	46.3240	45.69800	46.01100	46.01100
	12	46.3240	45.3850	46.0110	45.6980	46.3240	45.6980	46.32400	45.69800	46.01100	13	46.6370	45.3850	46.0110	46.6370	45.3850	46.0110	46.63700	45.69800	46.63700
	14	45.6980	45.0720	45.6980	44.7590	45.6980	45.0720	46.01100	45.38500	46.01100	15	45.6980	45.0720	45.6980	44.7590	45.6980	45.0720	46.01100	45.38500	46.01100
	16	46.3240	45.6980	46.0110	46.0110	45.0720	45.6980	46.63700	45.69800	46.32400	71	46.0110	44.7590	45.3850	46.0110	45.0720	45.3850	46.01100	45.07200	45.69800
	72	46.9500	46.0110	46.3240	46.3240	45.3850	46.0110	46.01100	45.69800	46.01100	72	46.9500	46.0110	46.3240	46.3240	45.3850	46.0110	46.01100	45.69800	46.01100
	73	46.6370	45.3850	45.6980	46.0110	45.0720	45.3850	46.01100	45.38500	46.01100	73	46.6370	45.3850	45.6980	46.0110	45.0720	45.3850	46.01100	45.38500	46.01100
	74	46.9500	46.0110	46.3240	46.9500	46.0110	46.3240	46.01100	45.07200	45.69800	74	46.9500	46.0110	46.3240	46.9500	46.0110	46.3240	46.01100	45.07200	45.69800
	75	46.3240	45.3850	46.0110	46.0110	45.3850	45.6980	45.69800	45.07200	45.69800	75	46.3240	45.3850	46.0110	46.0110	45.3850	45.6980	45.69800	45.07200	45.69800
	76	46.6370	45.6980	46.3240	46.9500	46.0110	46.3240	46.63700	46.01100	46.63700	76	46.6370	45.6980	46.3240	46.9500	46.0110	46.3240	46.63700	46.01100	46.63700
	Min	45.6980	44.7590	45.0720	45.6980	44.7590	45.0720	45.69800	45.07200	45.69800	Min	45.6980	44.7590	45.0720	45.6980	44.7590	45.0720	45.69800	45.07200	45.69800
	Max	46.9500	46.0110	46.3240	47.2630	46.0110	46.6370	46.63700	46.01100	46.63700	Max	46.9500	46.0110	46.3240	47.2630					

Wafer #	SN	CHD Prop Delay Fall 3.6V, 3.6V			CHA PWD 3.6V, 3.6V			CHB PWD 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	46.9500	47.2630	46.9500	0.0000	0.0000	0.3130	0.31300	0.31300	0.31300
	68	47.5760	47.5760	47.5760	0.0000	0.3130	0.3130	0.31300	0.62600	0.31300
	11	47.5760	46.3240	46.9500	0.6260	0.3130	0.3130	0.93900	0.62600	0.62600
	12	47.2630	46.0110	46.6370	0.3130	0.3130	0.0000	0.62600	0.00000	0.31300
	13	47.5760	46.6370	47.2630	0.3130	0.0000	0.0000	0.62600	0.31300	0.31300
	14	46.9500	46.3240	46.6370	0.3130	0.3130	0.0000	0.62600	0.31300	0.00000
	15	47.2630	46.0110	46.9500	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300
	16	47.5760	46.3240	46.9500	0.3130	0.0000	0.0000	0.31300	0.31300	0.00000
	71	46.6370	45.6980	46.3240	0.6260	0.3130	0.0000	0.62600	0.00000	0.31300
	72	47.5760	46.3240	46.9500	0.6260	0.3130	0.0000	0.62600	0.31300	0.31300
	73	46.6370	45.6980	46.0110	0.3130	0.3130	0.3130	0.62600	0.31300	0.62600
	74	47.2630	46.6370	46.9500	0.0000	0.0000	0.3130	0.62600	0.31300	0.00000
	75	46.6370	45.6980	46.3240	0.3130	0.0000	0.0000	0.31300	0.00000	0.00000
	76	47.2630	46.3240	46.9500	0.0000	0.3130	0.0000	0.62600	0.62600	0.31300
	Min	46.6370	45.6980	46.0110	0.0000	0.0000	0.0000	0.31300	0.00000	0.00000
	Max	47.5760	46.6370	47.2630	0.6260	0.3130	0.3130	0.93900	0.62600	0.62600
	Mean	47.1848	46.1675	46.7413	0.3391	0.2087	0.1043	0.57383	0.28692	0.26083
	Std. Dev	0.3804	0.3403	0.3614	0.2093	0.1541	0.1541	0.18071	0.20926	0.22465
	Mean - 3 Sigma	46.0435	45.1467	45.6571	-0.2887	-0.2537	-0.3580	0.03170	-0.34086	-0.41313
Mean + 3 Sigma	48.3260	47.1883	47.8256	0.9669	0.6710	0.5667	1.11597	0.91469	0.93479	
Limit: 29-x-66nS				Limit: <5nS			Limit: <5nS			

Wafer #	SN	CHC PWD 3.6V, 3.6V			CHD PWD 3.6V, 3.6V			AB_Rise Match Codirection 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	0.6260	0.6260	0.6260	1.5650	1.8780	1.5650	0.31300	0.31300	0.00000
	68	0.3130	0.3130	0.3130	2.1910	1.8780	1.8780	0.31300	0.31300	0.00000
	11	0.6260	0.6260	0.6260	1.5650	0.9390	1.2520	0.31300	0.31300	0.31300
	12	0.6260	0.6260	0.6260	1.5650	0.9390	1.2520	0.31300	0.00000	0.31300
	13	0.9390	0.6260	0.9390	1.5650	1.5650	1.5650	0.31300	0.31300	0.31300
	14	0.6260	0.6260	0.6260	1.5650	1.5650	1.5650	0.31300	0.31300	0.00000
	15	0.9390	0.6260	0.6260	1.8780	0.9390	1.5650	0.31300	0.62600	0.31300
	16	0.9390	0.6260	0.6260	1.8780	1.2520	1.5650	0.00000	0.00000	0.00000
	71	0.6260	0.6260	0.6260	1.2520	0.6260	0.9390	0.62600	0.31300	0.62600
	72	0.3130	0.3130	0.6260	2.1910	1.2520	1.5650	0.62600	0.31300	0.62600
	73	0.9390	0.6260	0.6260	1.5650	1.2520	0.9390	0.31300	0.31300	0.31300
	74	0.3130	0.0000	0.3130	1.8780	1.5650	1.5650	0.62600	0.31300	0.31300
	75	0.6260	0.6260	0.6260	1.5650	0.9390	1.2520	0.31300	0.00000	0.31300
	76	0.6260	0.6260	0.6260	1.5650	1.2520	1.5650	0.31300	0.62600	0.31300
	Min	0.3130	0.0000	0.3130	1.2520	0.6260	0.9390	0.00000	0.00000	0.00000
	Max	0.9390	0.6260	0.9390	2.1910	1.5650	1.5650	0.62600	0.62600	0.62600
	Mean	0.6782	0.5478	0.6260	1.6693	1.1738	1.3824	0.36517	0.28692	0.31300
	Std. Dev	0.2247	0.1946	0.1335	0.2437	0.3021	0.2482	0.18071	0.20926	0.18875
	Mean - 3 Sigma	0.0042	-0.0359	0.2256	0.9383	0.2673	0.6378	-0.17697	-0.34086	-0.25324
Mean + 3 Sigma	1.3521	1.1314	1.0264	2.4003	2.0802	2.1270	0.90730	0.91469	0.87924	
Limit: <5nS				Limit: <5nS			Limit: <5nS			

Wafer #	SN	AB_Fall Match Codirection 3.6V, 3.6V			CD_Rise Match Codirection 3.6V, 3.6V			CD_Fall Match Codirection 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	0.6260	0.6260	0.6260	0.3130	0.3130	0.3130	1.25200	1.56500	1.25200
	68	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	1.56500	1.56500	1.56500
	11	0.6260	0.6260	0.6260	0.3130	0.3130	0.0000	1.25200	0.62600	0.62600
	12	0.0000	0.3130	0.0000	0.0000	0.0000	0.0000	0.93900	0.31300	0.62600
	13	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	0.93900	0.93900	0.62600
	14	0.0000	0.3130	0.0000	0.0000	0.0000	0.3130	0.93900	0.93900	0.62600
	15	0.3130	0.6260	0.3130	0.3130	0.0000	0.3130	0.62600	0.31300	0.62600
	16	0.0000	0.3130	0.0000	0.6260	0.6260	0.3130	1.56500	1.25200	1.25200
	71	0.6260	0.6260	0.3130	0.0000	0.0000	0.0000	0.62600	0.00000	0.31300
	72	0.6260	0.3130	0.3130	0.3130	0.0000	0.0000	1.56500	0.93900	0.93900
	73	0.0000	0.3130	0.6260	0.0000	0.0000	0.0000	0.62600	0.62600	0.31300
	74	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	1.25200	1.56500	1.25200
	75	0.3130	0.0000	0.3130	0.0000	0.3130	0.0000	0.93900	0.62600	0.62600
	76	0.3130	0.3130	0.0000	0.3130	0.3130	0.6260	0.62600	0.31300	0.31300
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.62600	0.00000	0.31300
	Max	0.6260	0.6260	0.6260	0.6260	0.6260	0.6260	1.56500	1.56500	1.25200
	Mean	0.2348	0.3130	0.2087	0.2087	0.1304	0.1304	0.99117	0.70425	0.67817
	Std. Dev	0.2711	0.2312	0.2437	0.2039	0.2093	0.2093	0.34888	0.44516	0.32235
	Mean - 3 Sigma	-0.5784	-0.3805	-0.5223	-0.4029	-0.4974	-0.4974	-0.05548	-0.63122	-0.28887
Mean + 3 Sigma	1.0479	1.0065	0.9397	0.8203	0.7582	0.7582	2.03781	2.03972	1.64520	
Limit: <5nS				Limit: <5nS			Limit: <5nS			

Wafer #	SN	AB_C Rise Match Opposing 3.6V, 3.6V			AB_C Fall Match Opposing 3.6V, 3.6V			AB_D Rise Match Opposing 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	0.3130	0.3130	0.3130	0.6260	0.6260	0.6260	0.31300	0.31300	0.00000
	68	0.6260	0.6260	0.6260	0.3130	0.6260	0.6260	0.93900	0.62600	0.62600
	11	0.6260	0.3130	0.3130	0.9390	0.3130	0.3130	0.31300	0.31300	0.31300
	12	0.3130	0.0000	0.3130	0.0000	0.6260	0.3130	0.31300	0.00000	0.31300
	13	0.6260	0.3130	0.3130	0.0000	0.3130	0.6260	0.31300	0.31300	0.31300
	14	0.3130	0.3130	0.3130	0.3130	0.6260	0.9390	0.31300	0.31300	0.00000
	15	0.3130	0.3130	0.3130	0.6260	0.6260	0.6260	0.31300	0.31300	0.31300
	16	0.6260	0.3130	0.3130	0.0000	0.3130	0.3130	0.00000	0.31300	0.00000
	71	0.9390	0.6260	0.9390	0.9390	0.3130	0.3130	0.93900	0.62600	0.93900
	72	0.3130	0.3130	0.3130	0.6260	0.3130	0.6260	0.62600	0.31300	0.31300
	73	0.6260	0.6260	0.3130	0.0000	0.3130	0.6260	0.62600	0.62600	0.31300
	74	1.2520	0.9390	1.2520	0.9390	0.9390	0.6260	1.56500	0.93900	1.25200
	75	0.9390	0.9390	0.9390	0.6260	0.3130	0.3130	0.93900	0.62600	0.93900
	76	0.6260	0.6260	0.3130	0.3130	0.3130	0.3130	0.93900	0.93900	0.93900
	Min	0.3130	0.0000	0.3130	0.0000	0.3130	0.3130	0.00000	0.00000	0.00000
	Max	1.2520	0.9390	1.2520	0.9390	0.9390	0.9390	1.56500	0.93900	1.25200
	Mean	0.6260	0.4695	0.4956	0.4434	0.4434	0.4956	0.62600	0.46950	0.49558
	Std. Dev	0.2984	0.2831	0.3392	0.3882	0.2093	0.2093	0.42205	0.28312	0.41046
	Mean - 3 Sigma	-0.2693	-0.3799	-0.5219	-0.7210	-0.1844	-0.1322	-0.64015	-0.37986	-0.73580
	Mean + 3 Sigma	1.5213	1.3189	1.5131	1.6079	1.0712	1.1234	1.89215	1.31886	1.72696
		Limit: <7nS			Limit: <7nS			Limit: <7nS		

Wafer #	SN	AB_D Fall Match Opposing 3.6V, 3.6V			ChA Input Leakage @ 0V 3.6V, 3.6V			ChB Input Leakage @ 0V 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	1.8780	2.1910	1.8780	0.0007	-0.0004	0.0006	-0.00044	-0.00001	-0.00002
	68	1.2520	0.9390	0.9390	0.0004	-0.0002	0.0001	-0.00052	0.00022	-0.00040
	11	0.9390	0.9390	0.9390	-0.0013	-0.0009	0.0011	-0.00017	-0.00101	-0.00017
	12	0.9390	0.9390	0.9390	-0.0003	0.0010	-0.0005	0.00148	-0.00032	-0.00025
	13	0.9390	1.2520	1.2520	0.0002	0.0001	-0.0003	-0.00036	-0.00116	0.00052
	14	1.2520	1.5650	1.5650	-0.0001	-0.0009	-0.0009	-0.00055	-0.00010	-0.00010
	15	1.2520	0.9390	1.2520	-0.0012	0.0001	0.0012	0.00010	-0.00055	-0.00171
	16	1.5650	1.5650	1.5650	-0.0007	0.0006	-0.0006	-0.00052	-0.00086	0.00098
	71	0.3130	0.3130	0.3130	-0.0002	-0.0007	0.0007	-0.00129	0.00091	0.00052
	72	1.5650	1.2520	1.5650	0.0004	-0.0008	-0.0015	-0.00060	-0.00162	-0.00002
	73	0.6260	0.9390	0.9390	-0.0024	-0.0007	-0.0007	-0.00129	-0.00009	0.00052
	74	0.3130	0.6260	0.6260	-0.0015	-0.0013	0.0001	0.00010	-0.00009	0.00075
	75	0.6260	0.3130	0.6260	0.0000	-0.0007	0.0002	-0.00067	0.00052	-0.00040
	76	0.6260	0.6260	0.6260	-0.0015	-0.0006	0.0000	-0.00090	-0.00201	-0.00071
	Min	0.3130	0.3130	0.3130	-0.0024	-0.0013	-0.0015	-0.00129	-0.00201	-0.00171
	Max	1.5650	1.5650	1.5650	0.0004	0.0010	0.0012	0.00148	0.00091	0.00098
	Mean	0.9129	0.9390	1.0173	-0.0007	-0.0004	-0.0001	-0.00031	-0.00057	-0.00001
	Std. Dev	0.4316	0.4220	0.4247	0.0009	0.0007	0.0008	0.00076	0.00084	0.00074
	Mean - 3 Sigma	-0.3819	-0.3271	-0.2568	-0.0033	-0.0024	-0.0025	-0.00260	-0.00308	-0.00223
	Mean + 3 Sigma	2.2078	2.2051	2.2913	0.0019	0.0017	0.0023	0.00198	0.00194	0.00221
		Limit: <7nS			Limit: +/-10uA			Limit: +/-10uA		

Wafer #	SN	ChC Input Leakage @ 0V 3.6V, 3.6V			ChD Input Leakage @ 0V 3.6V, 3.6V			ChA Input Leakage @ Vdd1 3.6V, 3.6V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	-0.0011	-0.0007	0.0007	0.0010	-0.0011	-0.0018	0.00261	0.00329	0.00242
	68	-0.0003	0.0004	0.0001	0.0005	0.0010	-0.0001	0.00199	0.00230	0.00257
	11	0.0001	0.0000	-0.0010	0.0004	-0.0012	0.0006	0.00330	0.00184	0.00326
	12	0.0003	0.0005	0.0007	0.0006	0.0017	0.0005	0.00245	0.00299	0.00143
	13	-0.0007	-0.0001	0.0003	-0.0018	-0.0001	-0.0006	0.00153	0.00237	0.00105
	14	-0.0002	-0.0007	-0.0009	-0.0006	-0.0011	-0.0007	0.00437	0.00214	0.00189
	15	-0.0009	-0.0006	0.0002	0.0001	0.0011	-0.0013	0.00291	0.00214	0.00250
	16	-0.0008	-0.0002	-0.0007	0.0002	-0.0001	-0.0002	0.00261	0.00306	0.00151
	71	-0.0007	0.0002	-0.0017	0.0001	-0.0003	-0.0001	0.00268	0.00237	0.00173
	72	-0.0007	-0.0009	0.0014	-0.0014	0.0000	0.0005	0.00238	0.00237	0.00212
	73	0.0008	-0.0009	-0.0013	-0.0018	-0.0005	-0.0008	0.00245	0.00207	0.00265
	74	0.0008	0.0002	0.0007	-0.0004	-0.0009	0.0004	0.00199	0.00176	0.00097
	75	-0.0007	-0.0005	-0.0006	-0.0004	-0.0001	-0.0003	0.00222	0.00276	0.00273
	76	0.0011	0.0009	0.0001	-0.0009	0.0000	-0.0005	0.00268	0.00276	0.00181
	Min	-0.0009	-0.0009	-0.0017	-0.0018	-0.0012	-0.0013	0.00153	0.00176	0.00097
	Max	0.0011	0.0009	0.0014	0.0006	0.0017	0.0006	0.00437	0.00306	0.00326
	Mean	-0.0001	-0.0002	-0.0002	-0.0005	-0.0001	-0.0002	0.00263	0.00239	0.00197
	Std. Dev	0.0007	0.0006	0.0009	0.0008	0.0008	0.0006	0.00071	0.00043	0.00070
	Mean - 3 Sigma	-0.0023	-0.0018	-0.0030	-0.0030	-0.0026	-0.0021	0.00051	0.00110	-0.00014
	Mean + 3 Sigma	0.0020	0.0015	0.0025	0.0020	0.0024	0.0016	0.00475	0.00367	0.00408
		Limit: +/-10uA			Limit: +/-10uA			Limit: +/-10uA		

Wafer #	SN	ChB Input Leakage @ Vdd1 3.6V, 3.6V			ChC Input Leakage @ Vdd2 3.6V, 3.6V			ChD Input Leakage @ Vdd2 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0009	0.0035	0.0006	0.0001	0.0009	0.0011	0.00090	0.00039	-0.00034	
	68	0.0027	0.0024	0.0011	-0.0002	0.0004	-0.0007	-0.00017	0.00070	0.00012	
	11	0.0023	0.0014	0.0016	0.0006	0.0011	0.0009	0.00013	0.00032	0.00096	
	12	0.0013	0.0025	0.0012	0.0008	0.0002	0.0014	0.00052	0.00093	0.00035	
	13	0.0022	0.0017	0.0011	0.0003	-0.0005	0.0016	0.00021	0.00101	0.00012	
	14	0.0019	0.0038	0.0015	0.0004	0.0009	0.0008	-0.00017	-0.00068	0.00196	
	15	0.0026	0.0030	0.0023	0.0013	-0.0009	0.0001	0.00082	0.00093	0.00050	
	16	0.0014	0.0013	0.0030	0.0001	-0.0015	-0.0002	0.00059	-0.00030	-0.00026	
	71	0.0025	0.0001	0.0017	0.0007	0.0008	0.0004	0.00113	0.00155	0.00043	
	72	0.0009	0.0004	0.0015	0.0020	0.0017	-0.0006	0.00113	0.00093	0.00066	
	73	0.0016	0.0025	0.0011	-0.0009	0.0000	0.0011	-0.00017	-0.00007	0.00104	
	74	0.0017	0.0026	0.0021	0.0005	0.0007	0.0008	0.00052	0.00101	0.00157	
	75	0.0008	0.0001	0.0026	0.0005	0.0005	0.0000	0.00029	0.00086	0.00035	
	76	0.0011	0.0002	0.0007	-0.0008	-0.0003	0.0007	0.00021	0.00286	0.00020	
	Min	0.0008	0.0001	0.0007	-0.0008	-0.0015	-0.0006	-0.00017	-0.00068	-0.00026	
	Max	0.0026	0.0038	0.0030	0.0020	0.0017	0.0016	0.00113	0.00286	0.00196	
	Mean	0.0017	0.0016	0.0017	0.0006	0.0002	0.0006	0.00043	0.00078	0.00066	
	Std. Dev	0.0006	0.0013	0.0007	0.0007	0.0009	0.0006	0.00044	0.00092	0.00063	
	Mean - 3 Sigma	-0.0001	-0.0022	-0.0003	-0.0014	-0.0025	-0.0014	-0.00088	-0.00198	-0.00124	
Mean + 3 Sigma	0.0035	0.0054	0.0037	0.0026	0.0029	0.0025	0.00175	0.00354	0.00255		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	Voh_A @ -20uA 3.6V, 3.6V			Voh_B @ -20uA 3.6V, 3.6V			Voh_C @ -20uA 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.5973	3.5958	3.5925	3.5971	3.5958	3.5950	3.59657	3.59611	3.59503	
	68	3.5975	3.5971	3.5925	3.5973	3.5971	3.5949	3.59753	3.59445	3.59484	
	11	3.5964	3.5955	3.5929	3.5960	3.5962	3.5949	3.59203	3.59415	3.59490	
	12	3.5978	3.5965	3.5927	3.5976	3.5971	3.5951	3.59741	3.59404	3.59500	
	13	3.5977	3.5966	3.5928	3.5977	3.5950	3.5952	3.59731	3.59434	3.59488	
	14	3.5977	3.5966	3.5925	3.5975	3.5966	3.5951	3.59722	3.59428	3.59476	
	15	3.5979	3.5966	3.5927	3.5976	3.5966	3.5952	3.59723	3.59417	3.59479	
	16	3.5978	3.5970	3.5911	3.5976	3.5964	3.5934	3.59724	3.59287	3.59497	
	71	3.5977	3.5966	3.5920	3.5976	3.5970	3.5943	3.59730	3.59292	3.59493	
	72	3.5977	3.5971	3.5927	3.5974	3.5969	3.5952	3.59727	3.59076	3.59491	
	73	3.5978	3.5966	3.5917	3.5975	3.5968	3.5941	3.59738	3.59405	3.59492	
	74	3.5977	3.5966	3.5925	3.5975	3.5966	3.5947	3.59730	3.59377	3.59486	
	75	3.5964	3.5965	3.5925	3.5962	3.5965	3.5951	3.59735	3.59437	3.59297	
	76	3.5974	3.5967	3.5928	3.5970	3.5968	3.5950	3.59756	3.59462	3.59485	
	Min	3.5964	3.5955	3.5911	3.5960	3.5950	3.5934	3.59203	3.59076	3.59297	
	Max	3.5979	3.5971	3.5929	3.5977	3.5971	3.5952	3.59756	3.59462	3.59500	
	Mean	3.5975	3.5965	3.5924	3.5973	3.5965	3.5948	3.59688	3.59370	3.59473	
	Std. Dev	0.0005	0.0005	0.0006	0.0006	0.0005	0.0006	0.00153	0.00107	0.00056	
	Mean - 3 Sigma	3.5959	3.5951	3.5908	3.5955	3.5949	3.5931	3.59229	3.59048	3.59305	
Mean + 3 Sigma	3.5991	3.5979	3.5941	3.5990	3.5982	3.5965	3.60148	3.59691	3.59640		
Limit: >3.5V				Limit: >3.5V				Limit: >3.5V			

Wafer #	SN	Voh_D @ -20uA 3.6V, 3.6V			Voh_A @ -4mA 3.6V, 3.6V			Voh_B @ -4mA 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.5966	3.5950	3.5948	3.3449	3.3392	3.3418	3.31898	3.33419	3.33604	
	68	3.5972	3.5930	3.5946	3.3448	3.3422	3.3373	3.33709	3.30995	3.28904	
	11	3.5921	3.5932	3.5947	3.3280	3.3324	3.3335	3.30857	3.32675	3.32230	
	12	3.5972	3.5928	3.5947	3.3340	3.3371	3.3308	3.31886	3.33003	3.32898	
	13	3.5972	3.5930	3.5946	3.3482	3.3281	3.3327	3.34325	3.31698	3.31739	
	14	3.5972	3.5933	3.5944	3.3480	3.3378	3.3320	3.32427	3.31501	3.31683	
	15	3.5974	3.5933	3.5946	3.3474	3.3389	3.3326	3.34097	3.31863	3.31586	
	16	3.5973	3.5924	3.5946	3.3481	3.3386	3.3232	3.34159	3.32441	3.30266	
	71	3.5971	3.5918	3.5945	3.3459	3.3336	3.3232	3.33875	3.30783	3.32127	
	72	3.5971	3.5896	3.5945	3.3461	3.3360	3.3315	3.33130	3.29253	3.31243	
	73	3.5971	3.5935	3.5948	3.3474	3.3385	3.3271	3.32489	3.31570	3.30937	
	74	3.5971	3.5932	3.5945	3.3478	3.3353	3.3278	3.32583	3.32963	3.31752	
	75	3.5971	3.5932	3.5927	3.3403	3.3348	3.3297	3.32139	3.31173	3.32030	
	76	3.5973	3.5939	3.5947	3.3441	3.3331	3.3291	3.33105	3.31679	3.31205	
	Min	3.5921	3.5896	3.5927	3.3280	3.3281	3.3232	3.30857	3.29253	3.30266	
	Max	3.5974	3.5939	3.5948	3.3482	3.3389	3.3335	3.34325	3.33003	3.32898	
	Mean	3.5968	3.5928	3.5944	3.3438	3.3353	3.3294	3.32923	3.31717	3.31641	
	Std. Dev	0.0015	0.0011	0.0006	0.0065	0.0032	0.0035	0.01060	0.01044	0.00679	
	Mean - 3 Sigma	3.5923	3.5894	3.5927	3.3243	3.3257	3.3189	3.29742	3.28586	3.29603	
Mean + 3 Sigma	3.6012	3.5961	3.5962	3.3633	3.3449	3.3400	3.36103	3.34848	3.33679		
Limit: >3.5V				Limit: >3.2V				Limit: >3.2V			

Wafer #	SN	Voh_C @ -4mA 3.6V, 3.6V			Voh_D @ -4mA 3.6V, 3.6V			Vol_CHA @ 20uA 3.6V, 3.6V			mV
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.3433	3.3467	3.3450	3.3392	3.3256	3.3403	5.15234	0.92739	0.41448	
	68	3.3454	3.3394	3.3338	3.3418	3.3277	3.3386	1.19424	1.18956	0.53313	
	11	3.3054	3.3327	3.3171	3.3135	3.3196	3.3315	2.28850	1.52665	0.68926	
	12	3.3467	3.3342	3.3360	3.3405	3.3238	3.3335	1.00665	1.38932	0.40199	
	13	3.3460	3.3316	3.3351	3.3421	3.3272	3.3326	0.85658	1.45798	0.76421	
	14	3.3432	3.3344	3.3358	3.3436	3.3261	3.3331	0.86909	1.06472	1.45000	
	15	3.3426	3.3345	3.3368	3.3427	3.3279	3.3329	0.91911	0.95235	0.23337	
	16	3.3443	3.3278	3.3365	3.3367	3.3225	3.3328	1.21300	0.80878	0.54562	
	71	3.3441	3.3235	3.3330	3.3417	3.3200	3.3290	1.13171	1.19581	0.26459	
	72	3.3434	3.3151	3.3355	3.3413	3.3119	3.3324	0.92536	1.06472	0.23961	
	73	3.3472	3.3351	3.3362	3.3435	3.3304	3.3327	0.90660	0.93363	0.37700	
	74	3.3430	3.3301	3.3320	3.3399	3.3242	3.3252	1.67571	1.21453	0.21463	
	75	3.3437	3.3304	3.3221	3.3411	3.3214	3.3145	1.14422	1.10217	0.26459	
	76	3.3439	3.3320	3.3341	3.3397	3.3256	3.3257	1.18173	1.58907	0.45819	
	Min	3.3054	3.3151	3.3171	3.3135	3.3119	3.3145	0.85658	0.80878	0.21463	
	Max	3.3472	3.3351	3.3368	3.3436	3.3304	3.3335	2.28850	1.58907	1.45000	
	Mean	3.3411	3.3301	3.3325	3.3389	3.3234	3.3297	1.17652	1.19164	0.49192	
	Std. Dev	0.0114	0.0058	0.0063	0.0082	0.0049	0.0056	0.41757	0.25070	0.35253	
	Mean - 3 Sigma	3.3070	3.3128	3.3137	3.3143	3.3088	3.3129	-0.07619	0.43954	-0.56568	
Mean + 3 Sigma	3.3752	3.3474	3.3513	3.3635	3.3380	3.3464	2.42924	1.94375	1.54952		
Limit: >3.2V				Limit: >3.2V				Limit: <100mV			

Wafer #	SN	Vol_CHB @ 20uA 3.6V, 3.6V			Vol_CHC @ 20uA 3.6V, 3.6V			Vol_CHD @ 20uA 3.6V, 3.6V			mV
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	5.2320	0.9727	0.7600	2.0444	1.7467	1.6800	2.00604	1.38162	1.45000	
	68	1.2635	1.3224	1.4500	1.6758	2.0963	1.3200	1.61851	1.86218	1.32000	
	11	2.3322	1.5785	1.1600	1.7258	1.8154	1.4900	1.61226	1.53765	1.42000	
	12	0.9947	1.5535	1.1200	2.4630	1.9215	1.2800	2.38107	1.86218	1.19000	
	13	0.8885	1.5223	1.2800	2.2256	1.8715	1.2400	2.24981	1.52517	1.18000	
	14	1.0635	1.2850	2.0300	2.3755	1.7529	1.4400	2.19355	1.39410	1.18000	
	15	0.9635	1.1601	0.8224	2.1319	1.9402	1.4400	2.02479	1.46900	1.18000	
	16	1.2072	0.9040	1.0900	1.7633	1.7342	1.3000	1.69977	1.53141	1.01000	
	71	1.2510	1.3037	0.8224	1.6821	1.8341	1.1600	1.51226	1.52517	1.22000	
	72	0.9698	1.0414	0.8349	1.6008	1.7592	1.2800	1.52476	1.35666	1.22000	
	73	0.9760	1.1601	0.9847	1.6446	1.8403	1.2600	1.58726	1.50644	1.02000	
	74	1.8697	1.2787	0.8536	1.7633	1.9589	1.2100	1.68102	1.79353	1.28000	
	75	1.1760	1.2350	0.9784	1.6383	2.8765	1.1800	1.61851	2.57990	1.16000	
	76	1.3072	1.7596	0.9784	1.9757	1.9964	1.4100	1.79353	1.80601	1.20000	
	Min	0.8885	0.9040	0.8224	1.6008	1.7342	1.1600	1.51226	1.35666	1.01000	
	Max	2.3322	1.7596	2.0300	2.4630	2.8765	1.4900	2.38107	2.57990	1.42000	
	Mean	1.2499	1.3152	1.0796	1.9158	1.9418	1.3075	1.82322	1.65727	1.18833	
	Std. Dev	0.4298	0.2463	0.3336	0.3081	0.3064	0.1107	0.30677	0.33259	0.10676	
	Mean - 3 Sigma	-0.0394	0.5761	0.0787	0.9916	1.0227	0.9754	0.90292	0.65949	0.86806	
Mean + 3 Sigma	2.5393	2.0542	2.0805	2.8400	2.8608	1.6396	2.74351	2.65505	1.50860		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHA @ 400uA 3.6V, 3.6V			Vol_CHB @ 400uA 3.6V, 3.6V			Vol_CHC @ 400uA 3.6V, 3.6V			mV
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	24.1237	18.9552	18.4600	26.9242	19.4964	19.9500	20.82421	20.34876	20.67000	
	68	19.4403	19.6606	18.9400	20.3809	23.1124	29.4500	20.65553	21.19772	21.15000	
	11	21.6038	19.3173	18.6200	23.6807	20.2833	20.7200	21.61139	20.15525	22.17000	
	12	20.2969	19.5045	18.3700	22.0433	20.2458	19.6800	21.37399	20.45489	20.06000	
	13	18.8900	19.3485	18.6100	19.6497	20.8016	21.2800	21.26778	20.47361	20.06000	
	14	19.0401	18.8928	19.5400	21.5558	21.3387	22.0800	21.49894	20.11780	20.15000	
	15	18.9526	18.6056	18.3000	19.7747	20.9578	20.9400	21.36774	20.42367	19.91000	
	16	19.2152	18.4496	18.1300	19.9872	20.1022	21.3700	20.73050	20.42367	19.98000	
	71	19.2527	19.2174	18.3900	20.0622	21.9758	19.4500	20.52434	20.51107	20.14000	
	72	19.0276	18.9490	18.2100	20.6746	22.3567	20.8200	20.66178	20.39246	19.96000	
	73	18.8963	18.7617	18.2100	21.3371	21.1514	20.7400	20.54933	20.44240	19.93000	
	74	19.7967	18.9802	18.2400	21.9933	19.6400	20.2800	20.90543	20.83567	20.16000	
	75	19.0401	18.7305	17.9400	21.1121	21.1514	19.8900	20.67428	21.76577	20.21000	
	76	19.2965	19.4796	18.1800	20.8246	21.5448	20.8200	21.04912	20.71082	20.08000	
	Min	18.8900	18.4496	17.9400	19.6497	19.6400	19.4500	20.52434	20.11780	19.91000	
	Max	21.6038	19.5045	19.5400	23.6807	22.3567	22.0800	21.61139	21.76577	22.17000	
	Mean	19.4424	19.0197	18.3950	21.0580	20.9624	20.6725	21.01789	20.55892	20.23417	
	Std. Dev	0.7963	0.3505	0.4083	1.1686	0.7984	0.7510	0.39307	0.42767	0.61727	
	Mean - 3 Sigma	17.0534	17.9681	17.1702	17.5521	18.5671	18.4194	19.83868	19.27590	18.38235	
Mean + 3 Sigma	21.8313	20.0713	19.6198	24.5638	23.3578	22.9256	22.19709	21.84194	22.08599		
Limit: <100mV				Limit: <100mV				Limit: <100mV			

Wafer #	SN	Vol_CHD @ 400uA 3.6V, 3.6V			Vol_CHA @ 4mA 3.6V, 3.6V			Vol_CHB @ 4mA 3.6V, 3.6V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	21.1573	22.1269	20.9700	205.1397	191.2807	191.2700	232.64135	198.24396	202.10000	
	68	20.9511	22.0333	20.7400	193.7531	195.0698	194.1300	203.27462	230.08889	329.56000	
	11	20.9011	21.2406	20.6000	206.7342	189.9573	190.3900	228.56039	198.43756	207.10000	
	12	22.0324	21.3405	20.2500	205.5086	190.2944	190.3100	222.62329	199.58046	197.83000	
	13	21.5449	20.7476	20.3800	191.4520	190.6253	190.2400	198.00623	205.25122	212.21000	
	14	21.5074	20.7226	20.3300	191.4083	189.1396	192.3400	217.09867	214.76910	212.93000	
	15	21.4324	20.6415	20.4800	191.3207	188.3780	188.9600	200.54981	211.06562	212.62000	
	16	21.6324	20.5916	20.3700	192.2462	187.7600	189.2400	200.21233	202.90922	214.66000	
	71	20.9323	20.6977	20.4500	192.8527	191.1933	190.1900	201.54974	219.36566	197.87000	
	72	20.9448	20.3731	20.1000	193.0465	189.8450	189.3600	209.42420	226.66645	213.15000	
	73	20.7636	20.5916	20.1500	191.0331	188.0721	188.7300	215.43628	213.22650	211.75000	
	74	21.3136	21.2719	20.8600	193.9470	188.5278	189.1800	216.91744	197.01363	205.02000	
	75	21.0011	22.3703	20.9400	191.7334	188.2532	188.3100	212.21150	213.17029	203.30000	
	76	21.3886	21.2656	20.8800	192.8214	191.1246	188.9400	207.87430	210.50978	211.30000	
	Min	20.7636	20.3731	20.1000	191.0331	187.7600	188.3100	198.00623	197.01363	197.83000	
	Max	22.0324	22.3703	20.9400	206.7342	191.1933	192.3400	228.56039	226.66645	214.66000	
	Mean	21.2829	20.9879	20.4825	194.5087	189.4309	189.6825	210.87202	209.33046	208.31167	
	Std. Dev	0.3777	0.5462	0.2832	5.4981	1.2295	1.0847	9.68883	8.98706	5.99897	
	Mean - 3 Sigma	20.1497	19.3494	19.6330	178.0143	185.7423	186.4285	181.80551	182.36927	190.31474	
Mean + 3 Sigma	22.4160	22.6264	21.3320	211.0031	193.1195	192.9365	239.93852	236.29164	226.30859		
Limit: <100mV				Limit: <400mV				Limit: <400mV			

Wafer #	SN	Vol_CHC @ 4mA 3.6V, 3.6V			Vol_CHD @ 4mA 3.6V, 3.6V			Idd1 Quiescent 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	200.7255	199.7215	200.6800	205.4072	220.1741	206.0600	1.80461	1.75822	1.76961	
	68	202.4748	204.2035	211.1900	206.3135	214.8880	206.9300	1.78792	1.64673	1.74479	
	11	212.4270	197.2808	217.7400	205.6260	210.4131	204.8000	1.75863	1.67847	1.76066	
	12	203.3806	197.3182	199.3300	210.6451	207.8356	202.4500	1.76107	1.70288	1.79810	
	13	202.7497	198.2795	199.2800	207.3449	203.0612	202.9900	1.75374	1.68905	1.76229	
	14	205.3548	196.3195	197.9300	205.9823	204.6714	202.4000	1.80176	1.69556	1.79199	
	15	204.8488	197.8363	198.9800	206.3448	203.8850	203.6200	1.76799	1.68050	1.78548	
	16	202.7996	196.8813	198.6900	211.5951	202.0751	203.5000	1.78630	1.68742	1.77531	
	71	202.1062	200.2833	201.3800	205.7010	204.0722	205.6100	1.79199	1.69230	1.76595	
	72	202.6934	198.7914	199.5500	206.0885	202.5369	202.8700	1.79118	1.68783	1.79443	
	73	200.6005	196.6441	198.8300	205.0697	201.2013	202.6500	1.77490	1.70451	1.80623	
	74	204.4552	200.7640	201.3500	208.4637	206.5499	209.5100	1.76758	1.71183	1.78670	
	75	202.6684	202.4931	201.9800	206.5136	211.2120	210.5500	1.77856	1.71102	1.80786	
	76	203.8367	200.0336	201.0100	208.3699	206.5936	209.2300	1.75374	1.66951	1.76066	
	Min	200.6005	196.3195	197.9300	205.0697	201.2013	202.4000	1.75374	1.66951	1.76066	
	Max	212.4270	202.4931	217.7400	211.5951	211.2120	210.5500	1.80176	1.71183	1.80786	
	Mean	203.9934	198.5771	201.3375	207.3120	205.3423	205.0150	1.77395	1.69257	1.78297	
	Std. Dev	2.9513	1.9261	5.3214	2.0716	3.2262	3.0296	0.01614	0.01317	0.01757	
	Mean - 3 Sigma	195.1394	192.7988	185.3734	201.0973	195.8637	195.9263	1.72555	1.65307	1.73028	
Mean + 3 Sigma	212.8474	204.3554	217.3016	213.5267	215.0208	214.1037	1.82236	1.73207	1.83567		
Limit: <400mV				Limit: <400mV				Limit: <2.4mA			

Wafer #	SN	Idd2 Quiescent 3.3V, 3.3V			Idd1 Dynamic 2MBPS 3.3V, 3.3V			Idd2 Dynamic 2MBPS 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.7481	1.7814	1.7883	3.0752	3.0750	3.0505	3.08736	3.06580	3.06873	
	68	1.7676	1.7859	1.8034	3.0713	2.9642	3.0518	3.11597	3.09102	3.09338	
	11	1.7737	1.8062	1.7859	3.0821	2.9397	3.0485	3.09363	3.07300	3.06877	
	12	1.7599	1.7814	1.7753	3.0678	2.9175	3.0348	3.07442	3.04907	3.05839	
	13	1.7733	1.8164	1.7904	3.0732	2.9160	3.0237	3.09013	3.07402	3.07223	
	14	1.7407	1.7855	1.7786	3.0858	2.9465	3.0553	3.07869	3.06938	3.05786	
	15	1.7745	1.7944	1.8103	3.0841	2.9480	3.0545	3.11080	3.07812	3.08879	
	16	1.8083	1.8018	1.8156	3.0817	2.9264	3.0397	3.09721	3.06991	3.07231	
	71	1.7013	1.8034	1.8298	3.0806	2.9299	3.0494	3.03727	3.10189	3.09819	
	72	1.7822	1.8062	1.7863	3.1077	2.9568	3.0673	3.08968	3.06677	3.06502	
	73	1.7993	1.8250	1.8311	3.0864	2.9363	3.0450	3.14217	3.11776	3.11580	
	74	1.7757	1.8319	1.8152	3.0859	2.9297	3.0520	3.11141	3.09074	3.09456	
	75	1.8054	1.8437	1.8376	3.0962	2.9309	3.0477	3.11161	3.10461	3.09762	
	76	1.7660	1.8168	1.8254	3.0798	2.9318	3.0465	3.12012	3.10177	3.10840	
	Min	1.7013	1.7814	1.7753	3.0678	2.9160	3.0237	3.03727	3.04907	3.05786	
	Max	1.8083	1.8437	1.8376	3.1077	2.9568	3.0673	3.14217	3.11776	3.11580	
	Mean	1.7717	1.8094	1.8068	3.0843	2.9341	3.0470	3.09643	3.08309	3.08316	
	Std. Dev	0.0293	0.0184	0.0223	0.0102	0.0121	0.0110	0.02647	0.02005	0.01983	
	Mean - 3 Sigma	1.6836	1.7542	1.7398	3.0537	2.8977	3.0142	3.01702	3.02295	3.02367	
Mean + 3 Sigma	1.8597	1.8646	1.8738	3.1148	2.9705	3.0799	3.17584	3.14322	3.14266		
Limit: <2.3mA				Limit: <13mA				Limit: <13mA			

Wafer #	SN	Idd1 Dynamic 10MBPS 3.3V, 3.3V			Idd2 Dynamic 10MBPS 3.3V, 3.3V			Idd1 Dynamic 25MBPS 3.3V, 3.3V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	5.6892	5.7001	5.6276	5.6081	5.5612	5.5655	11.59217	11.57309	11.44406
	68	5.8212	5.6588	5.7178	5.7204	5.6324	5.6416	11.92555	11.65821	11.70757
	11	5.7896	5.3500	5.6316	5.6426	5.4684	5.5354	11.78695	10.86378	11.43206
	12	5.7007	5.2871	5.5336	5.6752	5.4784	5.5539	11.65821	10.71245	11.24333
	13	5.8203	5.3293	5.5768	5.7182	5.4981	5.5724	11.94801	10.83481	11.36610
	14	5.7214	5.3482	5.5778	5.6496	5.4769	5.5302	11.69809	10.84787	11.31943
	15	5.7953	5.4098	5.6320	5.7499	5.5408	5.6129	11.88770	11.03016	11.49024
	16	5.7489	5.2657	5.5368	5.6833	5.5416	5.5416	11.72730	10.62590	11.21318
	71	5.8525	5.3338	5.5962	5.7138	5.5160	5.5667	12.03895	10.81845	11.40113
	72	5.8464	5.3722	5.6185	5.6961	5.4923	5.5167	12.00310	10.89535	11.41611
	73	5.7682	5.2878	5.5215	5.7874	5.6059	5.6290	11.75961	10.67900	11.17522
	74	5.8447	5.3048	5.6165	5.6803	5.5094	5.5508	12.01950	10.77910	11.42636
	75	5.8486	5.2983	5.6064	5.6590	5.4468	5.5088	12.00868	10.70688	11.38481
	76	5.8652	5.3849	5.6253	5.7268	5.5529	5.6394	12.01531	10.94251	11.45924
	Min	5.7007	5.2657	5.5215	5.6426	5.4468	5.5088	11.65821	10.62590	11.17522
	Max	5.8652	5.4098	5.6320	5.7874	5.6059	5.6394	12.03895	11.03016	11.49024
	Mean	5.8001	5.3310	5.5894	5.6985	5.5037	5.5631	11.87928	10.81136	11.36060
	Std. Dev	0.0554	0.0440	0.0400	0.0428	0.0452	0.0430	0.14403	0.11696	0.10129
	Mean - 3 Sigma	5.6340	5.1990	5.4695	5.5701	5.3681	5.4340	11.44721	10.46049	11.05673
	Mean + 3 Sigma	5.9663	5.4630	5.7093	5.8269	5.6393	5.6923	12.31136	11.16222	11.66447
		Limit: <13mA			Limit: <13mA			Limit: <13mA		

Wafer #	SN	Idd2 Dynamic 25MBPS 3.3V, 3.3V			CHA Prop Delay Rise 3.3V, 3.3V			CHB Prop Delay Rise 3.3V, 3.3V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	11.3062	11.2361	11.3294	48.2020	48.2020	48.2020	48.51500	48.20200	48.20200
	68	11.6900	11.4404	11.4888	49.1410	49.1410	49.1410	49.14100	49.14100	49.14100
	11	11.4305	11.0176	11.2676	48.2020	49.1410	48.8280	49.45400	48.51500	49.14100
	12	11.5918	11.0203	11.2843	49.1410	48.2020	48.8280	48.82800	47.88900	48.51500
	13	11.7302	11.0840	11.3507	49.1410	48.5150	48.8280	48.82800	48.20200	48.82800
	14	11.4829	11.0066	11.1872	48.2020	47.5760	48.2020	47.88900	47.57600	47.88900
	15	11.7873	11.1884	11.4226	49.1410	48.2020	48.8280	48.51500	47.88900	48.51500
	16	11.6361	10.9854	11.2139	48.5150	47.5760	48.5150	48.51500	47.88900	48.51500
	71	11.6553	11.0776	11.2774	49.4540	48.5150	49.1410	48.51500	48.20200	48.82800
	72	11.6553	11.0586	11.1841	49.1410	48.2020	48.5150	48.20200	47.88900	48.20200
	73	11.8411	11.2977	11.4169	48.5150	47.8890	48.2020	48.51500	47.57600	48.20200
	74	11.5173	11.0216	11.2064	49.7670	49.1410	49.7670	49.45400	49.14100	49.45400
	75	11.4249	10.8582	11.0874	49.1410	48.5150	48.8280	48.82800	48.51500	48.82800
	76	11.6875	11.2099	11.4587	49.4540	48.8280	49.4540	49.14100	48.51500	49.14100
	Min	11.4249	10.8582	11.0874	48.2020	47.5760	48.2020	47.88900	47.57600	47.88900
	Max	11.8411	11.2977	11.4587	49.7670	49.1410	49.7670	49.45400	49.14100	49.45400
	Mean	11.6200	11.0688	11.2798	49.0628	48.2803	48.8280	48.72367	48.14983	48.67150
	Std. Dev	0.1350	0.1165	0.1132	0.4452	0.4647	0.4623	0.46871	0.45911	0.45260
	Mean - 3 Sigma	11.2151	10.7193	10.9403	47.7273	46.8860	47.4410	47.31754	46.77250	47.31371
	Mean + 3 Sigma	12.0250	11.4184	11.6193	50.3982	49.6745	50.2150	50.12979	49.52716	50.02929
		Limit: <13mA			Limit: 29-c<66ns			Limit: 29-c<66ns		

Wafer #	SN	CHC Prop Delay Rise 3.3V, 3.3V			CHD Prop Delay Rise 3.3V, 3.3V			CHA Prop Delay Fall 3.3V, 3.3V		
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal
CTRL	9	47.8890	47.8890	47.8890	48.2020	48.2020	48.2020	47.88900	47.88900	47.88900
	68	48.5150	48.5150	48.5150	48.5150	48.5150	48.5150	49.14100	49.14100	49.14100
	11	48.8280	48.2020	48.8280	49.1410	48.5150	48.8280	49.45400	48.20200	48.82800
	12	48.8280	48.2020	48.5150	48.5150	48.2020	48.5150	49.14100	48.20200	48.51500
	13	48.8280	48.2020	48.8280	48.8280	48.2020	48.8280	49.45400	48.20200	48.82800
	14	48.2020	47.8890	48.5150	48.2020	47.5760	48.2020	48.51500	47.57600	48.20200
	15	48.5150	48.2020	48.8280	48.5150	47.8890	48.5150	49.14100	48.51500	48.82800
	16	48.2020	47.5760	48.2020	48.5150	48.2020	48.5150	48.51500	47.57600	48.20200
	71	48.5150	48.2020	48.5150	48.5150	48.8280	48.8280	49.45400	48.51500	49.14100
	72	48.5150	48.8280	48.5150	48.5150	47.8890	48.2020	49.45400	48.20200	48.51500
	73	48.2020	47.5760	48.2020	48.2020	47.5760	47.8890	48.51500	47.57600	47.88900
	74	48.5150	48.2020	48.5150	48.5150	48.8280	48.2020	49.76700	48.82800	49.14100
	75	47.8890	47.5760	48.2020	48.2020	47.8890	48.5150	49.14100	48.20200	48.82800
	76	49.1410	48.8280	49.4540	48.8280	48.2020	48.8280	49.45400	48.51500	49.14100
	Min	47.8890	47.5760	48.2020	48.2020	47.5760	47.8890	48.51500	47.57600	47.88900
	Max	49.1410	48.8280	49.4540	49.1410	48.5150	48.8280	49.76700	48.82800	49.14100
	Mean	48.5150	48.1238	48.5933	48.5411	48.0716	48.5411	49.16708	48.17592	48.67150
	Std. Dev	0.3531	0.4247	0.3563	0.2818	0.3118	0.3118	0.43161	0.41046	0.41136
	Mean - 3 Sigma	47.4557	46.8497	47.5245	47.6957	47.1361	47.6056	47.87225	46.94454	47.43741
	Mean + 3 Sigma	49.5743	49.3978	49.6620	49.3865	49.0070	49.4765	50.46192	49.40730	49.90559
		Limit: 29-c<66ns			Limit: 29-c<66ns			Limit: 29-c<66ns		

Wafer #	SN	CHB Prop Delay Fall 3.3V, 3.3V			CHC Prop Delay Fall 3.3V, 3.3V			CHD Prop Delay Fall 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	48.8280	48.5150	48.5150	48.5150	48.5150	48.5150	49.76700	49.76700	49.76700	
	68	49.1410	49.4540	49.4540	48.8280	48.8280	48.8280	50.08000	50.08000	50.08000	
	11	50.0800	48.8280	49.4540	49.1410	48.5150	49.1410	50.08000	49.14100	49.76700	
	12	49.1410	47.8890	48.5150	49.1410	48.2020	48.8280	49.76700	48.82800	49.45400	
	13	49.4540	48.5150	49.1410	49.4540	48.5150	49.4540	50.39300	49.45400	50.08000	
	14	48.2020	47.5760	47.8890	48.8280	48.2020	48.8280	49.76700	48.82800	49.45400	
	15	48.8280	47.8890	48.5150	49.4540	48.5150	49.1410	49.76700	48.82800	49.76700	
	16	48.5150	47.5760	48.2020	48.8280	47.5760	48.8280	50.39300	48.82800	49.76700	
	71	49.1410	48.2020	48.8280	49.1410	48.5150	49.1410	49.45400	48.51500	49.14100	
	72	48.8280	48.2020	48.5150	48.8280	48.2020	48.8280	50.08000	49.14100	49.76700	
	73	48.8280	47.8890	48.5150	48.8280	47.8890	48.5150	49.14100	48.20200	48.82800	
	74	49.7670	48.8280	49.1410	48.8280	48.2020	48.8280	50.08000	49.76700	50.08000	
	75	48.8280	48.2020	48.5150	48.5150	47.8890	48.5150	49.45400	48.51500	49.14100	
	76	49.7670	48.8280	49.4540	49.4540	48.8280	49.4540	50.08000	49.14100	49.76700	
	Min	48.2020	47.5760	47.8890	48.5150	47.5760	48.5150	49.14100	48.20200	48.82800	
	Max	50.0800	48.8280	49.4540	49.4540	48.8280	49.4540	50.39300	49.76700	50.08000	
	Mean	49.1149	48.2020	48.7237	49.0367	48.2542	48.9323	49.87133	48.93233	49.58442	
	Std. Dev	0.5577	0.4623	0.4873	0.3082	0.3489	0.3359	0.38528	0.42903	0.38816	
Mean - 3 Sigma	47.4420	46.8150	47.2616	48.1120	47.2075	47.9247	48.71550	47.64526	48.41995		
Mean + 3 Sigma	50.7879	49.5890	50.1857	49.9613	49.3008	49.9400	51.02716	50.21941	50.74888		
Limit: 29<-x-66ns			Limit: 29<-x-66ns			Limit: 29<-x-66ns					

Wafer #	SN	CHA PWD 3.3V, 3.3V			CHB PWD 3.3V, 3.3V			CHC PWD 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.3130	0.3130	0.3130	0.3130	0.3130	0.3130	0.62600	0.62600	0.62600	
	68	0.0000	0.0000	0.0000	0.0000	0.3130	0.3130	0.31300	0.31300	0.31300	
	11	0.3130	0.0000	0.0000	0.6260	0.3130	0.3130	0.31300	0.31300	0.31300	
	12	0.0000	0.0000	0.3130	0.3130	0.0000	0.0000	0.31300	0.00000	0.31300	
	13	0.3130	0.3130	0.0000	0.6260	0.3130	0.3130	0.62600	0.31300	0.62600	
	14	0.3130	0.0000	0.0000	0.3130	0.0000	0.0000	0.62600	0.31300	0.31300	
	15	0.0000	0.3130	0.0000	0.3130	0.0000	0.0000	0.93900	0.31300	0.31300	
	16	0.0000	0.0000	0.3130	0.0000	0.3130	0.3130	0.62600	0.00000	0.31300	
	71	0.0000	0.0000	0.0000	0.6260	0.0000	0.0000	0.62600	0.31300	0.62600	
	72	0.3130	0.0000	0.0000	0.6260	0.3130	0.3130	0.31300	0.62600	0.31300	
	73	0.0000	0.3130	0.3130	0.3130	0.3130	0.3130	0.62600	0.31300	0.31300	
	74	0.0000	0.3130	0.6260	0.3130	0.3130	0.3130	0.31300	0.00000	0.31300	
	75	0.0000	0.3130	0.0000	0.0000	0.3130	0.3130	0.62600	0.31300	0.31300	
	76	0.0000	0.3130	0.3130	0.6260	0.3130	0.3130	0.31300	0.00000	0.00000	
	Min	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.31300	0.00000	0.00000	
	Max	0.3130	0.3130	0.6260	0.6260	0.3130	0.3130	0.93900	0.62600	0.62600	
	Mean	0.1043	0.1565	0.1565	0.3913	0.2087	0.2087	0.52167	0.23475	0.33908	
	Std. Dev	0.1541	0.1635	0.2110	0.2359	0.1541	0.1541	0.20387	0.19456	0.16117	
Mean - 3 Sigma	-0.3580	-0.3339	-0.4766	-0.3165	-0.2537	-0.2537	-0.08994	-0.34892	-0.14443		
Mean + 3 Sigma	0.5667	0.6469	0.7896	1.0990	0.6710	0.6710	1.13327	0.81842	0.82260		
Limit: <5ns			Limit: <5ns			Limit: <5ns					

Wafer #	SN	CHD PWD 3.3V, 3.3V			AB_Rise Match Codirection 3.3V, 3.3V			AB_Fall Match Codirection 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	1.5650	1.5650	1.5650	0.3130	0.0000	0.0000	0.93900	0.62600	0.62600	
	68	1.5650	1.5650	1.5650	0.0000	0.0000	0.0000	0.00000	0.31300	0.31300	
	11	0.9390	0.6260	0.9390	0.3130	0.3130	0.3130	0.62600	0.62600	0.62600	
	12	1.2520	0.6260	0.9390	0.3130	0.3130	0.3130	0.00000	0.31300	0.00000	
	13	1.5650	1.2520	1.2520	0.3130	0.3130	0.0000	0.00000	0.31300	0.31300	
	14	1.5650	1.2520	1.2520	0.3130	0.0000	0.3130	0.31300	0.00000	0.31300	
	15	1.2520	0.9390	1.2520	0.6260	0.3130	0.3130	0.31300	0.62600	0.31300	
	16	1.8780	0.6260	1.2520	0.0000	0.3130	0.0000	0.00000	0.00000	0.00000	
	71	0.9390	0.0000	0.3130	0.9390	0.3130	0.3130	0.31300	0.31300	0.31300	
	72	1.5650	1.2520	1.5650	0.9390	0.3130	0.3130	0.62600	0.00000	0.00000	
	73	0.9390	0.6260	0.9390	0.0000	0.3130	0.0000	0.31300	0.31300	0.62600	
	74	1.5650	1.5650	1.2520	0.3130	0.0000	0.3130	0.00000	0.00000	0.00000	
	75	1.2520	0.6260	0.6260	0.3130	0.0000	0.0000	0.31300	0.00000	0.31300	
	76	1.2520	0.9390	0.9390	0.3130	0.3130	0.3130	0.31300	0.31300	0.31300	
	Min	0.9390	0.0000	0.3130	0.0000	0.0000	0.0000	0.00000	0.00000	0.00000	
	Max	1.8780	1.5650	1.5650	0.9390	0.3130	0.3130	0.62600	0.62600	0.62600	
	Mean	1.3303	0.8608	1.0433	0.3913	0.2348	0.2087	0.26083	0.23475	0.26083	
	Std. Dev	0.3021	0.4247	0.3359	0.3021	0.1416	0.1541	0.22465	0.23593	0.22465	
Mean - 3 Sigma	0.4238	-0.4133	0.0357	-0.5152	-0.1899	-0.2537	-0.41313	-0.47305	-0.41313		
Mean + 3 Sigma	2.2367	2.1348	2.0510	1.2977	0.6594	0.6710	0.93479	0.94255	0.93479		
Limit: <5ns			Limit: <5ns			Limit: <5ns					

Wafer #	SN	CD_Rise Match Codirection 3.3V, 3.3V			CD_Fall Match Codirection 3.3V, 3.3V			AB_C Rise Match Opposing 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.3130	0.3130	0.3130	1.2520	1.2520	1.2520	0.62600	0.31300	0.31300	
	68	0.0000	0.0000	0.0000	1.2520	1.2520	1.2520	0.62600	0.62600	0.62600	
	11	0.3130	0.3130	0.0000	0.9390	0.6260	0.6260	0.62600	0.31300	0.31300	
	12	0.3130	0.0000	0.0000	0.6260	0.6260	0.6260	0.31300	0.31300	0.31300	
	13	0.0000	0.0000	0.0000	0.9390	0.9390	0.6260	0.31300	0.31300	0.00000	
	14	0.0000	0.3130	0.3130	0.9390	0.6260	0.6260	0.31300	0.31300	0.62600	
	15	0.0000	0.3130	0.3130	0.3130	0.3130	0.6260	0.62600	0.31300	0.31300	
	16	0.3130	0.6260	0.3130	1.5650	1.2520	1.2520	0.31300	0.31300	0.31300	
	71	0.0000	0.3130	0.3130	0.3130	0.0000	0.0000	0.93900	0.31300	0.62600	
	72	0.0000	0.9390	0.3130	1.2520	0.9390	0.9390	0.62600	0.93900	0.31300	
	73	0.0000	0.0000	0.3130	0.3130	0.3130	0.3130	0.31300	0.31300	0.00000	
	74	0.0000	0.0000	0.3130	1.2520	1.5650	1.2520	1.25200	0.93900	1.25200	
	75	0.3130	0.3130	0.3130	0.9390	0.6260	0.6260	1.25200	0.93900	0.62600	
	76	0.3130	0.6260	0.6260	0.6260	0.3130	0.3130	0.31300	0.31300	0.31300	
	Min	0.0000	0.0000	0.0000	0.3130	0.0000	0.0000	0.31300	0.31300	0.00000	
	Max	0.3130	0.9390	0.6260	1.5650	1.5650	1.2520	1.25200	0.93900	1.25200	
	Mean	0.1304	0.3130	0.2608	0.8347	0.6782	0.6521	0.59992	0.46950	0.41733	
	Std. Dev	0.1612	0.2984	0.1807	0.4077	0.4393	0.3645	0.36449	0.28312	0.33588	
Mean - 3 Sigma	-0.3531	-0.5823	-0.2813	-0.4935	-0.3885	-0.4414	-0.49355	-0.37986	-0.59030		
Mean + 3 Sigma	0.6139	1.2083	0.8030	2.0579	1.9960	1.7455	1.69338	1.31886	1.42496		
Limit: <5ns				Limit: <5ns				Limit: <7ns			

Wafer #	SN	AB_C Fall Match Opposing 3.3V, 3.3V			AB_D Rise Match Opposing 3.3V, 3.3V			AB_D Fall Match Opposing 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.6260	0.6260	0.6260	0.3130	0.0000	0.0000	1.87800	1.87800	1.87800	
	68	0.3130	0.6260	0.6260	0.6260	0.6260	0.6260	0.93900	0.93900	0.93900	
	11	0.9390	0.3130	0.3130	0.3130	0.3130	0.3130	0.62600	0.93900	0.93900	
	12	0.0000	0.3130	0.3130	0.6260	0.3130	0.3130	0.62600	0.93900	0.93900	
	13	0.0000	0.3130	0.6260	0.3130	0.3130	0.0000	0.93900	1.25200	1.25200	
	14	0.6260	0.6260	0.9390	0.3130	0.0000	0.3130	1.56500	1.25200	1.56500	
	15	0.6260	0.6260	0.6260	0.6260	0.3130	0.3130	0.93900	0.93900	1.25200	
	16	0.3130	0.0000	0.3130	0.0000	0.6260	0.0000	1.87800	1.25200	1.56500	
	71	0.3130	0.3130	0.3130	0.9390	0.3130	0.3130	0.31300	0.31300	0.31300	
	72	0.6260	0.0000	0.3130	0.6260	0.3130	0.3130	1.25200	0.93900	1.25200	
	73	0.3130	0.3130	0.6260	0.3130	0.3130	0.3130	0.62600	0.62600	0.93900	
	74	0.9390	0.6260	0.3130	1.2520	0.9390	0.9390	0.31300	0.93900	0.93900	
	75	0.6260	0.3130	0.3130	0.9390	0.6260	0.3130	0.62600	0.31300	0.62600	
	76	0.3130	0.3130	0.3130	0.6260	0.6260	0.6260	0.62600	0.62600	0.62600	
	Min	0.0000	0.0000	0.3130	0.0000	0.0000	0.0000	0.31300	0.31300	0.31300	
	Max	0.9390	0.6260	0.9390	1.2520	0.9390	0.9390	1.87800	1.25200	1.56500	
	Mean	0.4695	0.3391	0.4434	0.5738	0.4173	0.3391	0.86075	0.86075	1.01725	
	Std. Dev	0.3130	0.2093	0.2093	0.3489	0.2437	0.2482	0.48352	0.33031	0.38043	
Mean - 3 Sigma	-0.4695	-0.2887	-0.1844	-0.4728	-0.3137	-0.4055	-0.58980	-0.13017	-0.12404		
Mean + 3 Sigma	1.4085	0.9669	1.0712	1.6205	1.1483	1.0837	2.31130	1.85167	2.15854		
Limit: <7ns				Limit: <7ns				Limit: <7ns			

Wafer #	SN	ChA Input Leakage @ 0V 3.3V, 3.3V			ChB Input Leakage @ 0V 3.3V, 3.3V			ChC Input Leakage @ 0V 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0005	0.0001	-0.0013	-0.0012	0.0012	0.0004	-0.00048	-0.00048	-0.00033	
	68	-0.0017	0.0007	-0.0001	-0.0002	0.0005	-0.0006	-0.00117	-0.00086	0.00105	
	11	0.0005	-0.0013	0.0000	0.0014	-0.0002	0.0005	-0.00094	-0.00186	-0.00018	
	12	-0.0019	-0.0009	-0.0003	0.0003	0.0012	-0.0007	0.00098	-0.00040	0.00005	
	13	-0.0005	-0.0002	-0.0012	-0.0003	-0.0001	0.0004	-0.00033	0.00060	0.00082	
	14	0.0001	-0.0005	0.0007	-0.0001	-0.0009	-0.0006	0.00090	-0.00079	-0.00018	
	15	-0.0012	0.0001	0.0002	0.0006	-0.0009	-0.0006	-0.00071	-0.00071	-0.00178	
	16	0.0005	-0.0009	0.0002	-0.0002	0.0003	-0.0012	0.00029	-0.00056	-0.00087	
	71	-0.0006	0.0009	-0.0007	-0.0001	-0.0008	-0.0006	-0.00056	0.00021	-0.00071	
	72	0.0001	-0.0005	0.0011	-0.0001	0.0001	0.0001	-0.00125	-0.00086	-0.00140	
	73	-0.0001	-0.0015	0.0011	0.0007	-0.0002	-0.0013	-0.00025	0.00060	-0.00064	
	74	0.0012	-0.0009	-0.0001	-0.0007	-0.0005	-0.0013	0.00160	-0.00094	0.00013	
	75	-0.0009	-0.0005	0.0000	0.0003	-0.0003	-0.0010	0.00121	-0.00109	0.00196	
	76	0.0006	0.0003	0.0005	-0.0005	-0.0002	-0.0002	-0.00010	0.00029	0.00036	
	Min	-0.0019	-0.0015	-0.0012	-0.0007	-0.0009	-0.0013	-0.00125	-0.00186	-0.00178	
	Max	0.0012	0.0009	0.0011	0.0014	0.0012	0.0005	0.00160	0.00060	0.00196	
	Mean	-0.0002	-0.0005	0.0001	0.0001	-0.0002	-0.0005	0.00007	-0.00046	-0.00020	
	Std. Dev	0.0009	0.0007	0.0007	0.0006	0.0006	0.0006	0.00092	0.00075	0.00100	
Mean - 3 Sigma	-0.0028	-0.0026	-0.0019	-0.0017	-0.0020	-0.0024	-0.00268	-0.00271	-0.00321		
Mean + 3 Sigma	0.0024	0.0016	0.0022	0.0019	0.0015	0.0013	0.00282	0.00179	0.00280		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

Wafer #	SN	ChD Input Leakage @ 0V 3.3V, 3.3V			ChA Input Leakage @ Vdd1 3.3V, 3.3V			ChB Input Leakage @ Vdd1 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	-0.0005	0.0005	0.0000	0.0024	0.0021	0.0026	0.00048	0.00137	0.00297	
	68	-0.0002	-0.0004	0.0003	0.0027	0.0015	0.0004	0.00010	0.00275	0.00236	
	11	0.0001	-0.0010	0.0002	0.0021	0.0008	0.0030	0.00071	0.00068	0.00274	
	12	0.0004	-0.0008	-0.0003	0.0027	0.0013	0.0036	0.00125	0.00236	-0.00002	
	13	-0.0005	-0.0007	-0.0009	0.0032	0.0022	0.0032	0.00140	0.00114	0.00105	
	14	-0.0016	-0.0015	0.0007	0.0021	0.0015	0.0028	0.00094	0.00052	0.00197	
	15	0.0007	0.0002	-0.0004	0.0018	0.0006	0.0021	0.00210	0.00213	0.00197	
	16	-0.0002	-0.0006	0.0007	0.0017	0.0007	0.0030	0.00171	0.00206	0.00113	
	71	0.0010	-0.0018	0.0011	0.0031	0.0022	0.0022	0.00148	0.00091	0.00006	
	72	-0.0002	-0.0004	0.0003	0.0031	0.0008	0.0012	0.00102	0.00137	0.00098	
	73	-0.0001	-0.0002	0.0000	0.0028	0.0003	0.0016	0.00040	0.00075	-0.00033	
	74	0.0017	-0.0007	0.0007	0.0025	0.0027	0.0013	0.00025	0.00060	0.00213	
	75	0.0001	-0.0004	-0.0018	0.0015	0.0007	0.0027	0.00140	0.00091	0.00105	
	76	-0.0011	-0.0005	-0.0001	0.0031	0.0022	0.0030	0.00117	0.00114	0.00213	
	Min	-0.0016	-0.0018	-0.0018	0.0015	0.0003	0.0012	0.00025	0.00052	-0.00033	
	Max	0.0017	0.0002	0.0011	0.0032	0.0027	0.0036	0.00236	0.00274	0.00274	
	Mean	0.0000	-0.0007	0.0000	0.0025	0.0015	0.0025	0.00115	0.00121	0.00124	
	Std. Dev	0.0009	0.0005	0.0008	0.0006	0.0009	0.0008	0.00053	0.00064	0.00098	
	Mean - 3 Sigma	-0.0026	-0.0023	-0.0024	0.0006	-0.0011	0.0001	-0.00043	-0.00069	-0.00169	
Mean + 3 Sigma	0.0026	0.0009	0.0024	0.0044	0.0041	0.0048	0.00274	0.00312	0.00417		
Limit: +/-10uA				Limit: +/-10uA				Limit: +/-10uA			

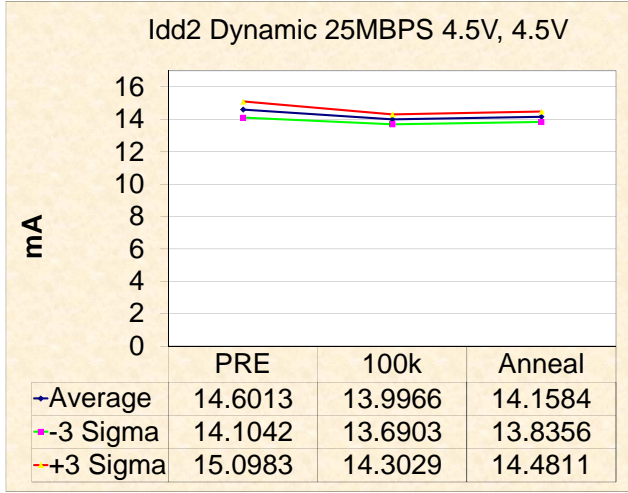
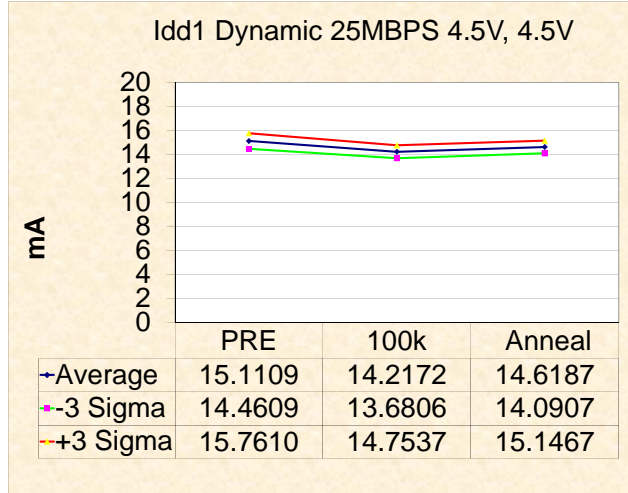
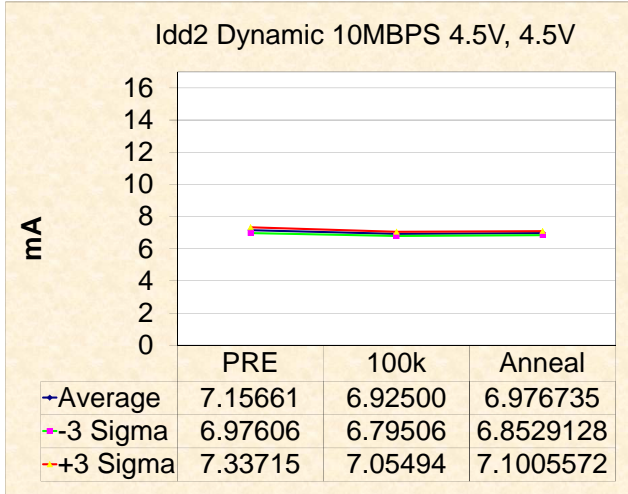
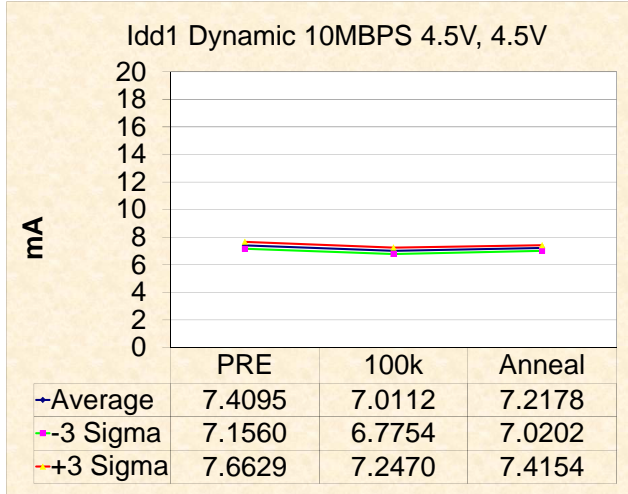
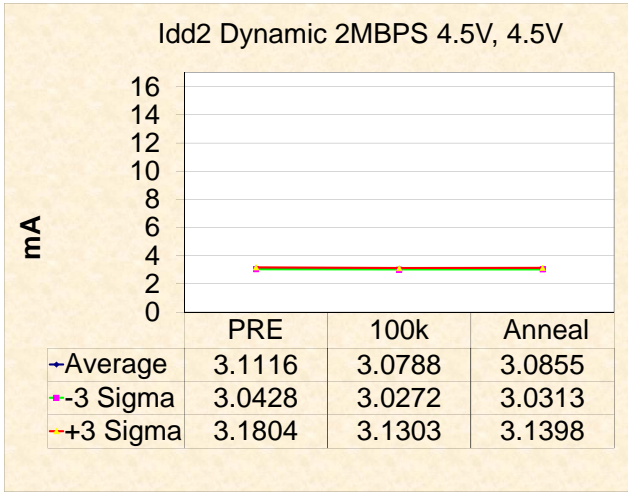
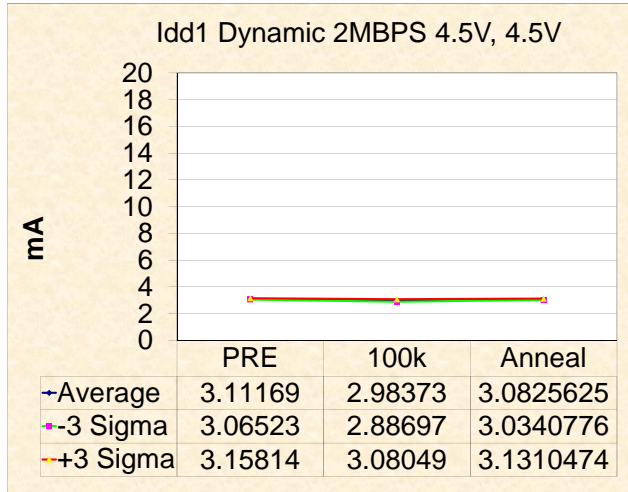
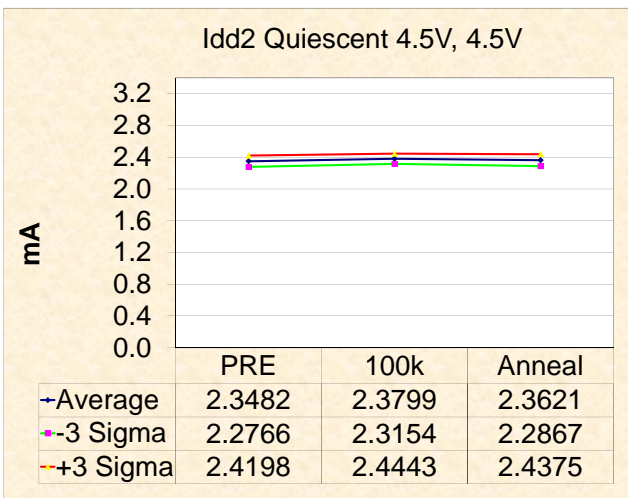
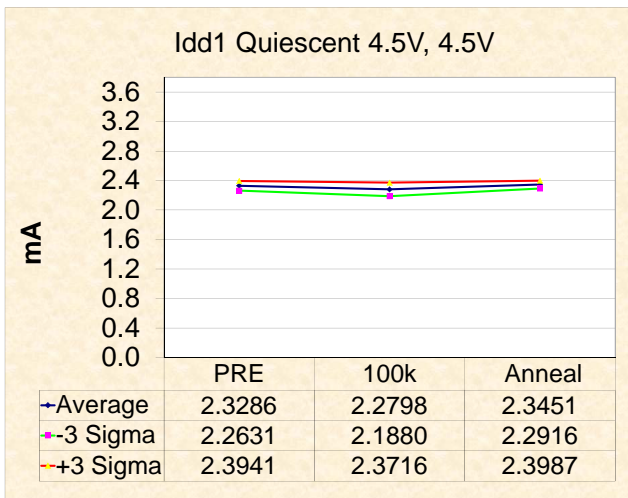
Wafer #	SN	ChC Input Leakage @ Vdd2 3.3V, 3.3V			ChD Input Leakage @ Vdd2 3.3V, 3.3V			Voh_A @ -20uA 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	0.0001	-0.0005	0.0000	0.0004	0.0005	0.0003	3.29754	3.29610	3.29287	
	68	0.0008	0.0005	0.0004	0.0004	0.0008	-0.0004	3.29776	3.29671	3.29294	
	11	0.0008	0.0007	0.0000	-0.0014	0.0018	0.0010	3.29642	3.29633	3.29314	
	12	0.0007	0.0015	-0.0003	0.0020	0.0009	0.0000	3.29778	3.29664	3.29287	
	13	0.0004	-0.0001	0.0001	0.0012	0.0005	-0.0004	3.29794	3.29550	3.29325	
	14	0.0018	0.0003	0.0004	0.0004	0.0000	0.0000	3.29779	3.29668	3.29289	
	15	0.0004	-0.0001	0.0005	0.0004	-0.0001	0.0010	3.29795	3.29713	3.29285	
	16	-0.0008	0.0009	0.0010	-0.0001	0.0012	0.0005	3.29786	3.29739	3.29155	
	71	0.0001	0.0001	0.0001	0.0002	0.0014	0.0029	3.29773	3.29683	3.29227	
	72	-0.0016	0.0005	0.0014	0.0017	-0.0006	-0.0004	3.29770	3.29705	3.29276	
	73	0.0006	0.0001	0.0014	0.0005	0.0005	-0.0007	3.29776	3.29739	3.29212	
	74	-0.0004	0.0004	-0.0005	0.0004	0.0016	-0.0001	3.29771	3.29664	3.29277	
	75	-0.0003	0.0010	-0.0009	-0.0006	-0.0001	0.0003	3.29656	3.29698	3.29290	
	76	0.0010	-0.0005	-0.0017	-0.0003	0.0012	-0.0007	3.29750	3.29650	3.29290	
	Min	-0.0016	-0.0005	-0.0017	-0.0014	-0.0006	-0.0007	3.29642	3.29550	3.29155	
	Max	0.0018	0.0015	0.0014	0.0020	0.0018	0.0029	3.29795	3.29739	3.29325	
	Mean	0.0002	0.0004	0.0001	0.0004	0.0007	0.0003	3.29756	3.29676	3.29269	
	Std. Dev	0.0009	0.0006	0.0009	0.0009	0.0008	0.0010	0.00051	0.00052	0.00048	
	Mean - 3 Sigma	-0.0024	-0.0012	-0.0026	-0.0025	-0.0016	-0.0027	3.29602	3.29520	3.29125	
Mean + 3 Sigma	0.0028	0.0021	0.0029	0.0032	0.0030	0.0033	3.29910	3.29831	3.29412		
Limit: +/-10uA				Limit: +/-10uA				Limit: >3.2V			

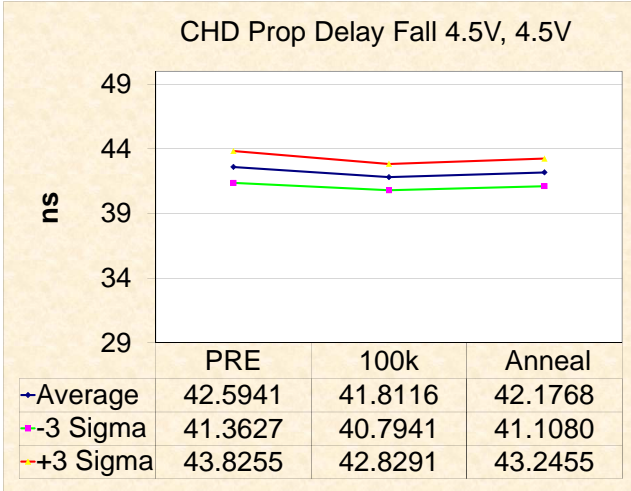
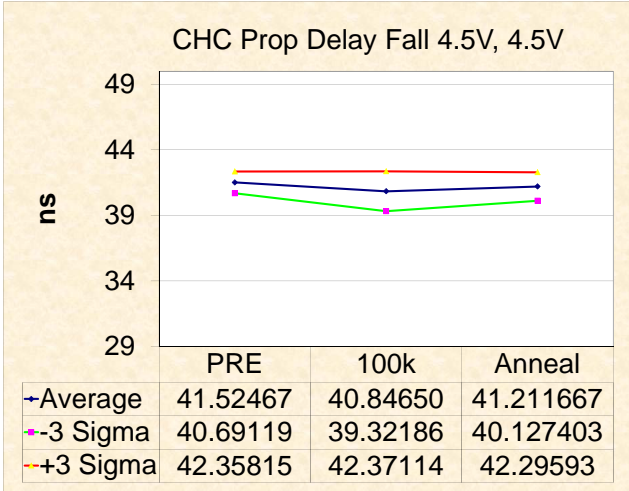
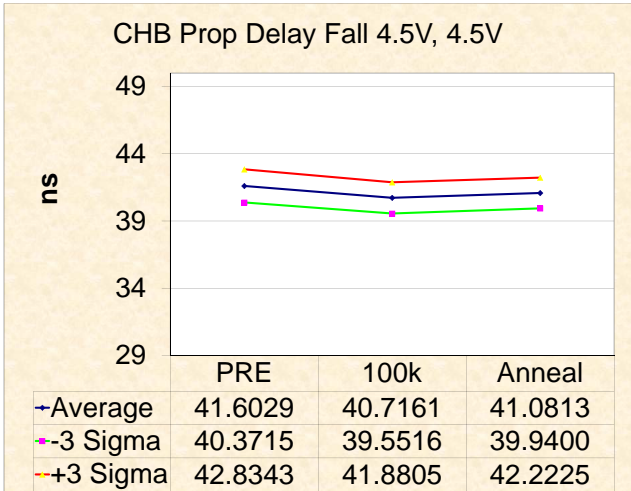
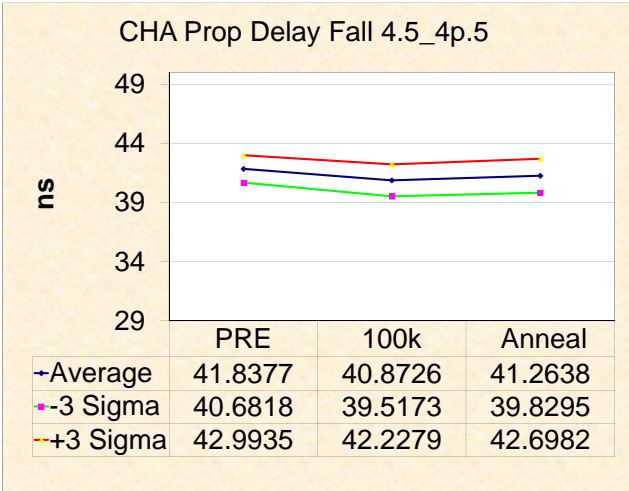
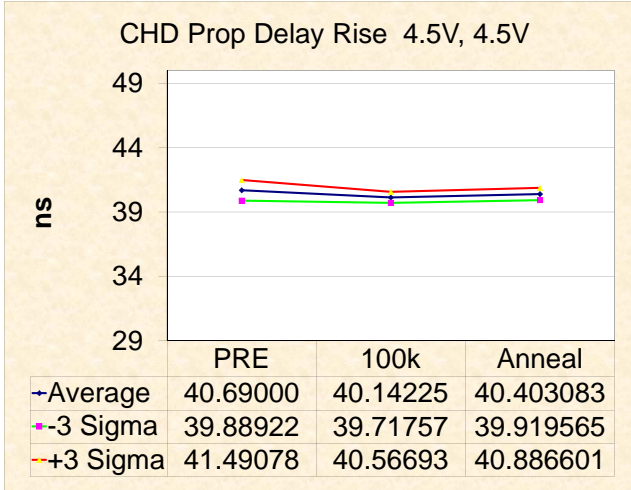
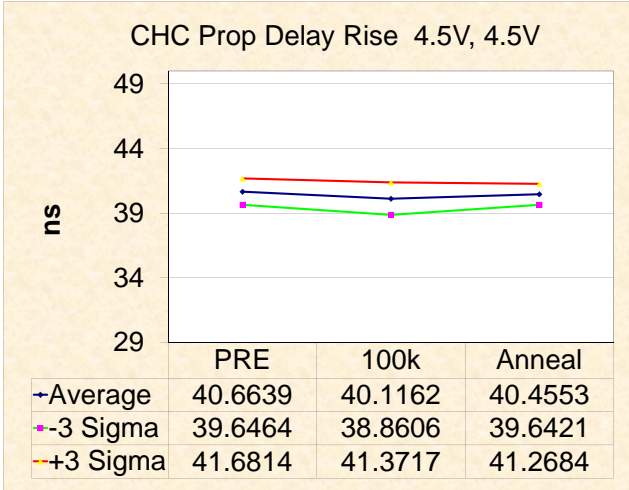
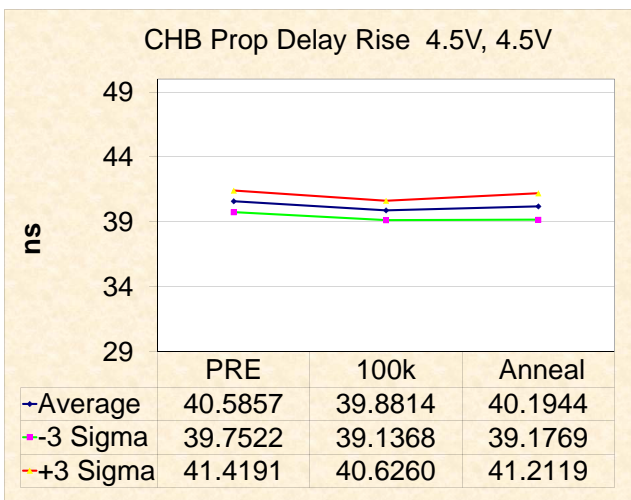
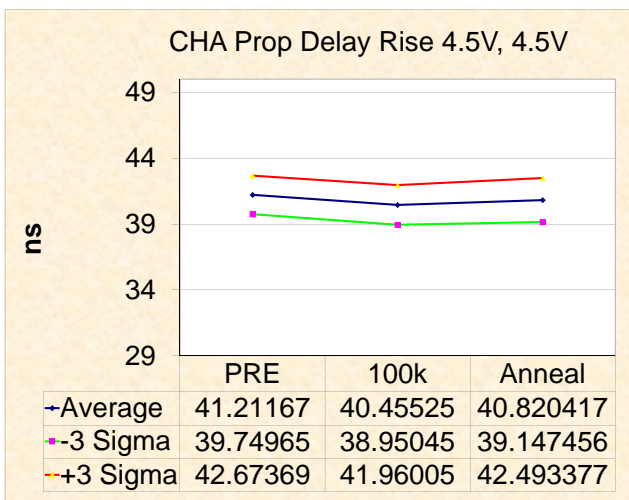
Wafer #	SN	Voh_B @ -20uA 3.3V, 3.3V			Voh_C @ -20uA 3.3V, 3.3V			Voh_D @ -20uA 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	3.2971	3.2957	3.2954	3.2965	3.2962	3.2948	3.29640	3.29514	3.29455	
	68	3.2975	3.2967	3.2950	3.2974	3.2943	3.2950	3.29715	3.29331	3.29455	
	11	3.2964	3.2964	3.2952	3.2922	3.2951	3.2948	3.29194	3.29377	3.29473	
	12	3.2978	3.2966	3.2952	3.2973	3.2942	3.2949	3.29728	3.29376	3.29449	
	13	3.2977	3.2957	3.2954	3.2973	3.2948	3.2948	3.29708	3.29401	3.29474	
	14	3.2976	3.2967	3.2954	3.2970	3.2952	3.2947	3.29710	3.29387	3.29442	
	15	3.2977	3.2970	3.2950	3.2973	3.2951	3.2948	3.29713	3.29403	3.29434	
	16	3.2979	3.2967	3.2939	3.2973	3.2936	3.2950	3.29725	3.29248	3.29454	
	71	3.2976	3.2972	3.2948	3.2972	3.2937	3.2949	3.29702	3.29222	3.29461	
	72	3.2977	3.2972	3.2953	3.2973	3.2916	3.2949	3.29707	3.29101	3.29470	
	73	3.2974	3.2968	3.2944	3.2974	3.2949	3.2950	3.29712	3.29377	3.29470	
	74	3.2975	3.2972	3.2949	3.2973	3.2946	3.2948	3.29729	3.29396	3.29453	
	75	3.2964	3.2965	3.2955	3.2972	3.2948	3.2930	3.29711	3.29357	3.29266	
	76	3.2971	3.2966	3.2951	3.2972	3.2948	3.2949	3.29707	3.29363	3.29443	
	Min	3.2964	3.2957	3.2939	3.2922	3.2916	3.2930	3.29194	3.29101	3.29266	
	Max	3.2979	3.2972	3.2955	3.2974	3.2952	3.2950	3.29729	3.29403	3.29474	
	Mean	3.2974	3.2967	3.2950	3.2968	3.2944	3.2947	3.29671	3.29334	3.29441	
	Std. Dev	0.0005	0.0004	0.0005	0.0015	0.0010	0.0005	0.00150	0.00094	0.00057	
	Mean - 3 Sigma	3.2959	3.2954	3.2936	3.2925	3.2913	3.2931	3.29220	3.29052	3.29271	
Mean + 3 Sigma	3.2989	3.2980	3.2964	3.3012	3.2974	3.2963	3.30121	3.29616	3.29611		
Limit: >3.2V				Limit: >3.2V				Limit: >3.2V			

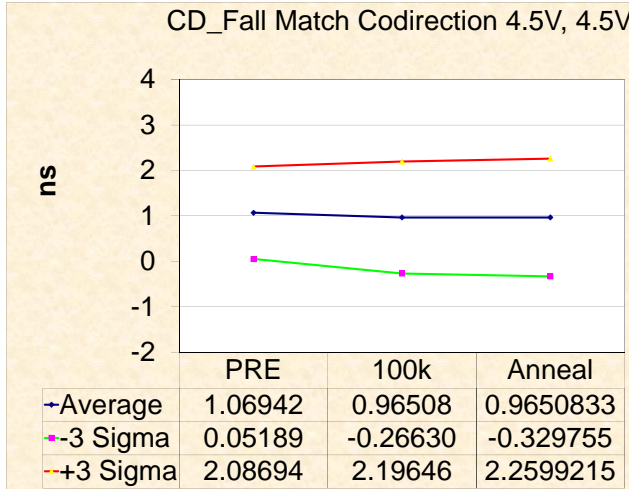
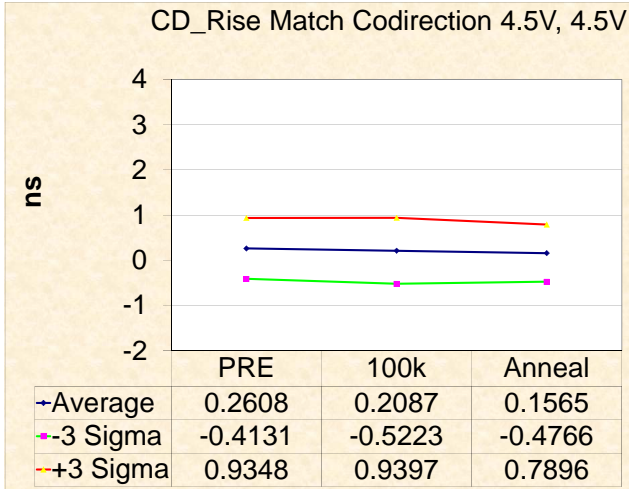
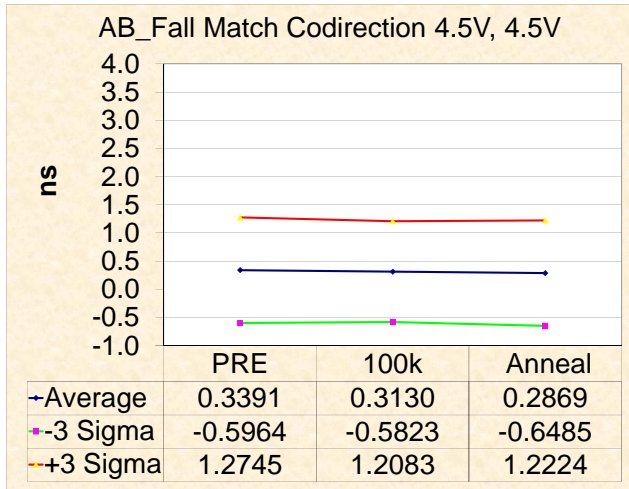
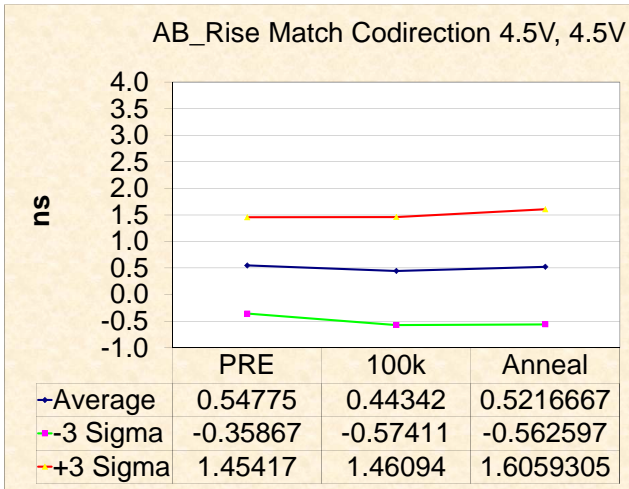
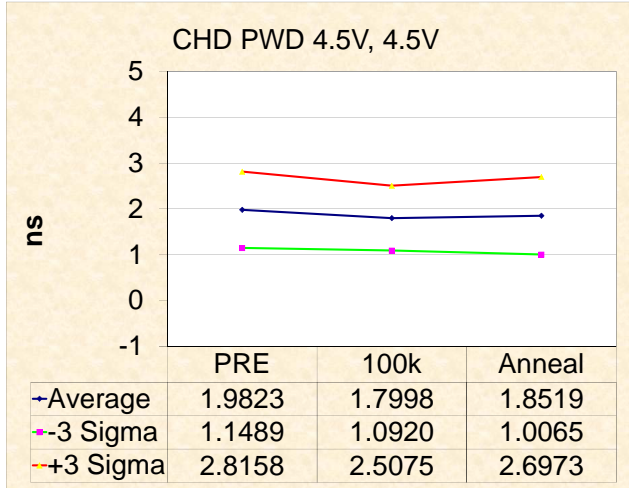
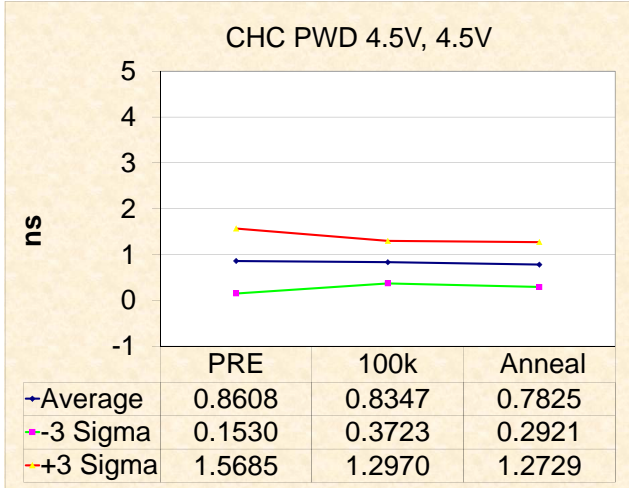
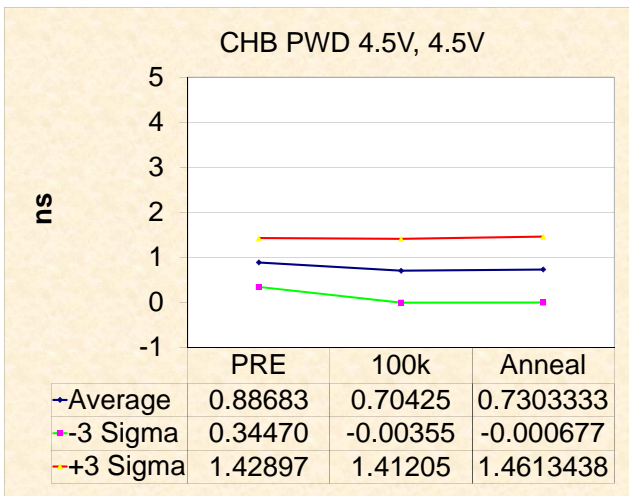
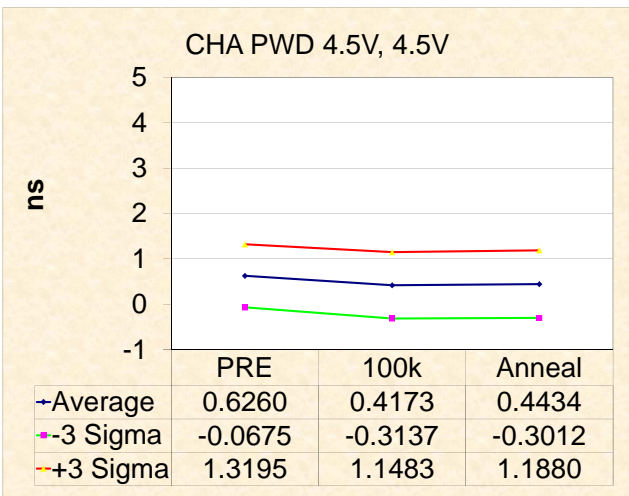
Wafer #	SN	Vol_CHB @ 400uA 3.3V, 3.3V			Vol_CHC @ 400uA 3.3V, 3.3V			Vol_CHD @ 400uA 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	27.0055	20.1709	20.6200	21.3240	21.0729	20.8900	21.73862	22.68857	21.38000	
	68	20.9496	23.5184	25.9300	21.2553	21.7783	21.8100	21.60111	22.60119	21.14000	
	11	23.8932	20.7579	21.3000	22.2674	20.8232	22.5800	21.40735	21.67127	21.19000	
	12	22.6370	20.8703	20.1400	21.8425	20.9543	20.6600	22.51992	21.67127	20.76000	
	13	20.2059	21.2825	21.8500	21.7051	20.9855	20.5700	21.99489	21.17823	20.85000	
	14	22.0996	21.8821	22.5400	21.9113	20.5298	20.5300	21.91988	21.20320	20.83000	
	15	20.4059	21.4511	21.5200	21.8613	20.8856	20.6100	21.88238	21.30929	20.97000	
	16	20.5184	20.5643	21.7600	21.2490	20.8419	20.4700	22.01364	21.12830	20.80000	
	71	20.6746	22.3130	20.1800	21.1428	21.1728	20.6500	21.40109	21.19696	21.02000	
	72	21.1934	23.4746	21.5100	21.1741	21.0229	20.5300	21.40735	21.10958	20.77000	
	73	21.8683	21.7509	21.5400	20.9929	20.7296	20.4900	21.26984	20.82873	20.68000	
	74	22.6495	20.2958	20.7700	21.5177	21.3912	20.6700	21.80737	21.67752	21.40000	
	75	21.7058	21.8009	20.4600	21.1991	22.2964	20.8700	21.48235	22.81963	21.44000	
	76	21.3371	22.0007	21.2700	21.6426	21.3725	20.8300	21.87613	21.64631	21.43000	
	Min	20.2059	20.2958	20.1400	20.9929	20.5298	20.4700	21.26984	20.82873	20.68000	
	Max	23.8932	23.4746	22.5400	22.2674	22.2964	22.5800	22.51992	22.81963	21.44000	
	Mean	21.5991	21.5370	21.2367	21.5421	21.0838	20.7883	21.74852	21.45336	21.01167	
	Std. Dev	1.1036	0.8747	0.7224	0.3922	0.4557	0.5777	0.36264	0.51239	0.28261	
	Mean - 3 Sigma	18.2882	18.9129	19.0694	20.3656	19.7167	19.0552	20.66061	19.91620	20.16383	
Mean + 3 Sigma	24.9100	24.1612	23.4040	22.7187	22.4509	22.5215	22.83643	22.99051	21.85950		
			Limit: <100mV			Limit: <100mV			Limit: <100mV		

Wafer #	SN	Vol_CHA @ 4mA 3.3V, 3.3V			Vol_CHB @ 4mA 3.3V, 3.3V			Vol_CHC @ 4mA 3.3V, 3.3V			
		PRE	100k	Anneal	PRE	100k	Anneal	PRE	100k	Anneal	
CTRL	9	211.3301	197.8976	197.7900	239.6034	204.7891	208.3900	206.94794	206.11364	206.84000	
	68	200.4875	201.6055	200.8300	209.9492	236.7402	242.8000	209.16578	210.84531	217.53000	
	11	213.3623	196.2433	196.8000	231.6227	204.5455	213.2700	218.96176	203.58549	224.11000	
	12	212.0804	196.6616	196.8400	229.8728	205.9507	204.2100	209.45317	203.27963	205.53000	
	13	197.9363	196.8925	196.5300	204.8433	211.4778	218.8100	209.00334	204.22846	205.53000	
	14	197.9926	195.2945	198.7700	223.8232	221.1768	219.4700	211.10873	202.01868	203.93000	
	15	198.1614	194.6079	195.5100	206.8431	217.5233	219.3300	211.13372	203.82270	205.14000	
	16	198.7242	193.8900	195.5700	206.9181	208.8048	220.8600	209.07207	202.77400	204.76000	
	71	199.4308	197.3794	196.5000	208.2493	225.5111	204.1800	208.83466	206.53811	207.85000	
	72	199.5246	196.1497	195.6100	215.9113	233.8798	219.2300	209.19701	204.98377	206.06000	
	73	197.6424	194.3644	195.0600	222.2983	219.7716	218.2000	206.97917	202.68661	205.09000	
	74	200.2999	194.6890	195.5900	222.3296	203.1528	211.2300	211.04626	207.11240	208.11000	
	75	198.1864	194.4830	194.7600	218.6986	219.2782	209.0100	209.35321	208.84152	208.71000	
	76	199.3870	197.2484	195.4600	213.9426	216.7614	217.6700	210.39028	206.26345	207.46000	
	Min	197.6424	193.8900	194.7600	204.8433	203.1528	204.1800	206.97917	202.01868	203.93000	
	Max	213.3623	197.3794	198.7700	231.6227	233.8798	220.8600	218.96176	208.84152	224.11000	
	Mean	201.0607	195.6586	196.0833	217.1127	215.6528	214.6225	210.37778	204.67790	207.69000	
	Std. Dev	5.5115	1.2418	1.0839	9.1701	9.2089	6.0881	2.95515	2.09054	5.38419	
	Mean - 3 Sigma	184.5261	191.9333	192.8315	189.6024	188.0262	196.3581	201.51233	198.40629	191.53742	
Mean + 3 Sigma	217.5953	199.3840	199.3351	244.6230	243.2795	232.8869	219.24323	210.94952	223.84258		
			Limit: <400mV			Limit: <400mV			Limit: <400mV		

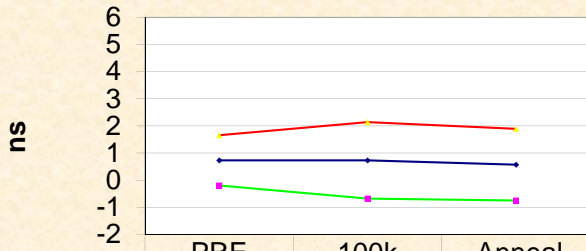
Wafer #	SN	Vol_CHD @ 4mA 3.3V, 3.3V		mV
		PRE	100k	
CTRL	9	211.6952	226.4901	212.3000
	68	212.9765	221.4723	213.2300
	11	212.1452	214.8255	211.2000
	12	216.7767	213.8644	208.5900
	13	213.6890	209.2960	209.1500
	14	212.0514	210.6191	208.3800
	15	212.7265	209.9638	210.1000
	16	217.6705	208.1601	209.6500
	71	212.4389	210.1635	211.8100
	72	212.5702	208.5970	209.3400
	73	211.5326	207.2926	209.0600
	74	215.0141	212.7473	216.0400
	75	213.1202	217.3032	217.0600
	76	215.0954	212.7223	215.5200
	Min	211.5326	207.2926	208.3800
	Max	217.6705	217.3032	217.0600
	Mean	213.7359	211.2962	211.3250
	Std. Dev	1.9742	3.0143	3.1222
	Mean - 3 Sigma	207.8132	202.2534	201.9583
Mean + 3 Sigma	219.6586	220.3390	220.6917	
			Limit: <400mV	





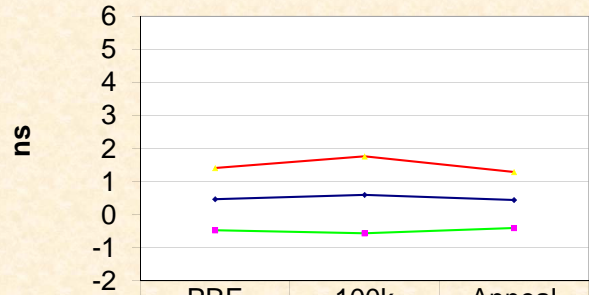


AB_C Rise Match Opposing 4.5V, 4.5V



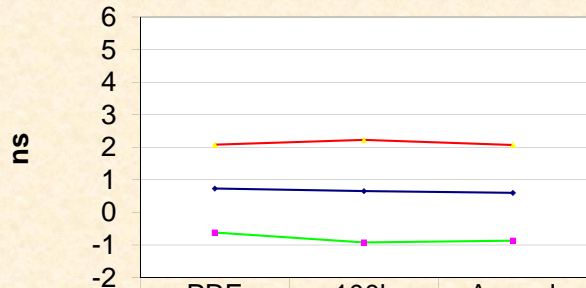
	PRE	100k	Anneal
→Average	0.7303	0.7303	0.5738
→-3 Sigma	-0.1943	-0.6758	-0.7440
→+3 Sigma	1.6550	2.1365	1.8917

AB_C Fall Match Opposing 4.5V, 4.5V



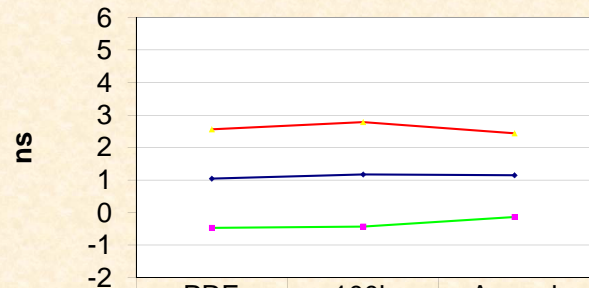
	PRE	100k	Anneal
→Average	0.4695	0.5999	0.4434
→-3 Sigma	-0.4695	-0.5645	-0.4020
→+3 Sigma	1.4085	1.7644	1.2888

AB_D Rise Match Opposing 4.5V, 4.5V



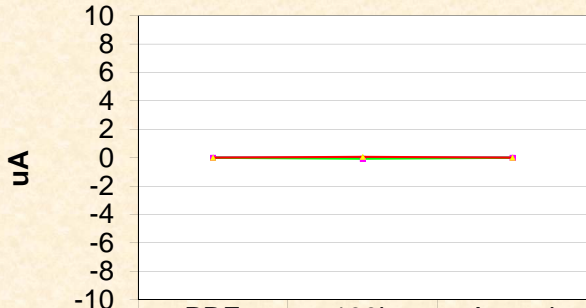
	PRE	100k	Anneal
→Average	0.73033	0.65208	0.5999167
→-3 Sigma	-0.61758	-0.92214	-0.868942
→+3 Sigma	2.07825	2.22630	2.0687749

AB_D Fall Match Opposing 4.5V, 4.5V



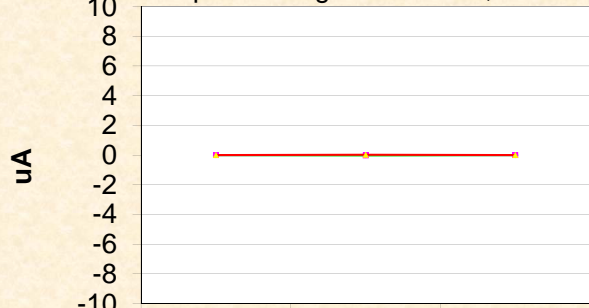
	PRE	100k	Anneal
→Average	1.0433	1.1738	1.1477
→-3 Sigma	-0.4725	-0.4341	-0.1394
→+3 Sigma	2.5592	2.7816	2.4347

ChA Input Leakage @ 0V 4.5V, 4.5V



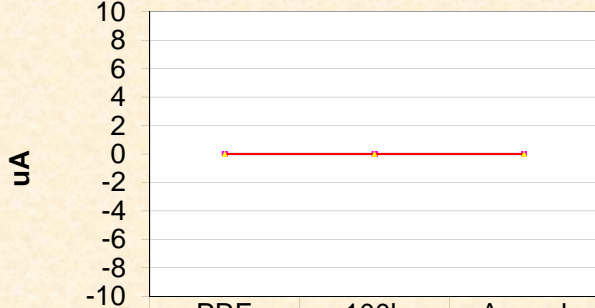
	PRE	100k	Anneal
→Average	0.0001	-0.0068	-0.0003
→-3 Sigma	-0.0018	-0.0735	-0.0027
→+3 Sigma	0.0020	0.0599	0.0020

ChB Input Leakage @ 0V 4.5V, 4.5V



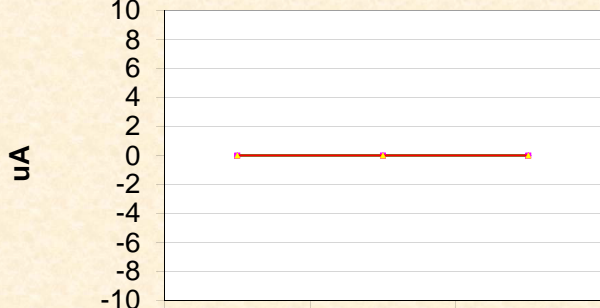
	PRE	100k	Anneal
→Average	-0.00031	-0.00254	-0.000615
→-3 Sigma	-0.00270	-0.02820	-0.001899
→+3 Sigma	0.00207	0.02311	0.0006687

ChC Input Leakage @ 0V 4.5V, 4.5V



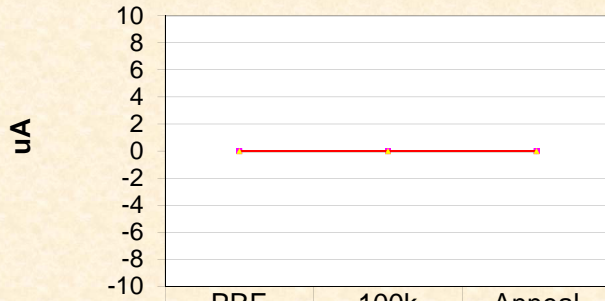
	PRE	100k	Anneal
→Average	-0.0003	0.0002	-0.0006
→-3 Sigma	-0.0031	-0.0013	-0.0032
→+3 Sigma	0.0024	0.0017	0.0021

ChD Input Leakage @ 0V 4.5V, 4.5V



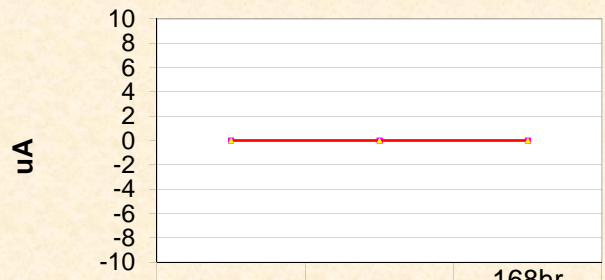
	PRE	100k	Anneal
→Average	-0.0002	-0.0005	-0.0001
→-3 Sigma	-0.0031	-0.0024	-0.0018
→+3 Sigma	0.0027	0.0013	0.0016

ChA Input Leakage @ Vdd1 4.5V, 4.5V



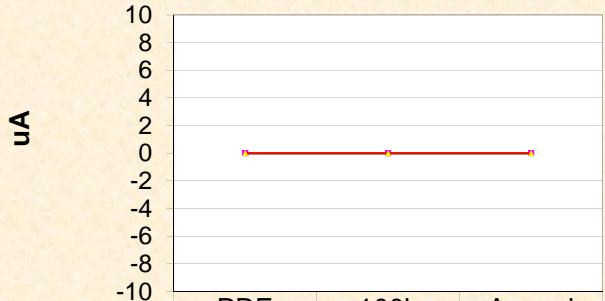
	PRE	100k	Anneal
→Average	0.00430	0.00367	0.0037525
→-3 Sigma	0.00103	0.00092	0.0018917
→+3 Sigma	0.00756	0.00642	0.0056133

ChB Input Leakage @ Vdd1 4.5V, 4.5V



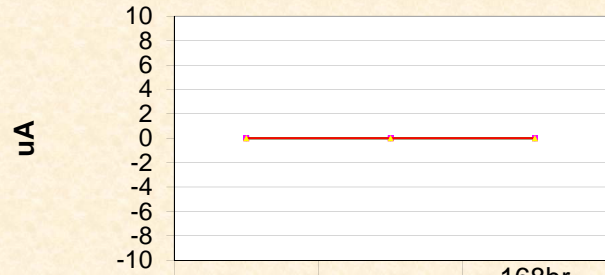
	PRE	100k	168hr Anneal
→Average	0.0036	0.0027	0.0026
→-3 Sigma	0.0010	0.0003	-0.0003
→+3 Sigma	0.0061	0.0051	0.0054

ChC Input Leakage @ Vdd2 4.5V, 4.5V



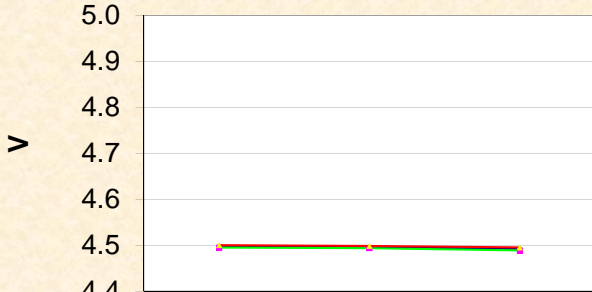
	PRE	100k	Anneal
→Average	0.0005	0.0011	0.0005
→-3 Sigma	-0.0021	-0.0009	-0.0018
→+3 Sigma	0.0032	0.0032	0.0028

ChD Input Leakage @ Vdd2 4.5V, 4.5V



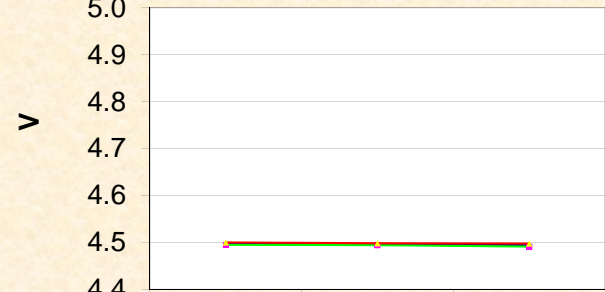
	PRE	100k	168hr Anneal
→Average	0.00064	0.00078	0.0008719
→-3 Sigma	-0.00079	-0.00122	-0.001408
→+3 Sigma	0.00207	0.00278	0.0031514

Voh_A @ -20uA 4.5V, 4.5V



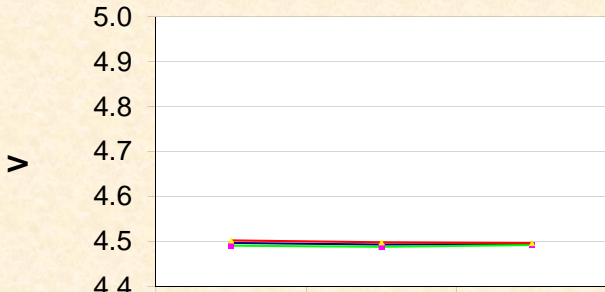
	PRE	100k	Anneal
→Average	4.4972	4.4957	4.4918
→-3 Sigma	4.4946	4.4932	4.4885
→+3 Sigma	4.4998	4.4983	4.4950

Voh_B @ -20uA 4.5V, 4.5V



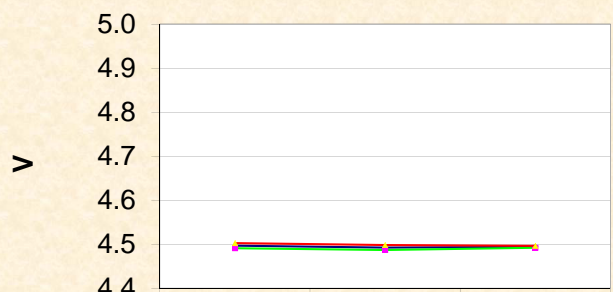
	PRE	100k	Anneal
→Average	4.4971	4.4960	4.4942
→-3 Sigma	4.4945	4.4939	4.4910
→+3 Sigma	4.4998	4.4981	4.4973

Voh_C @ -20uA 4.5V, 4.5V

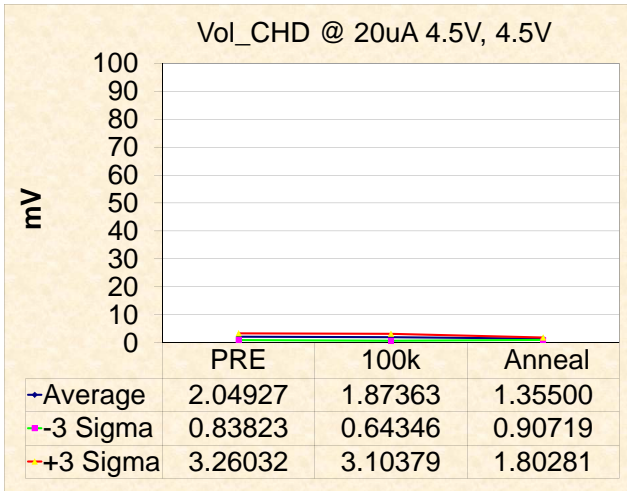
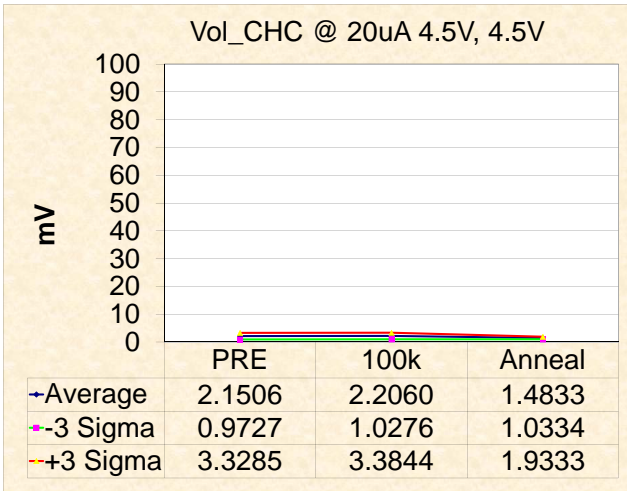
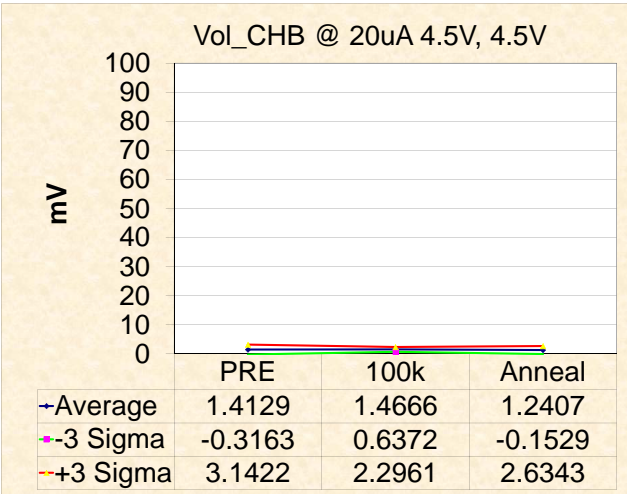
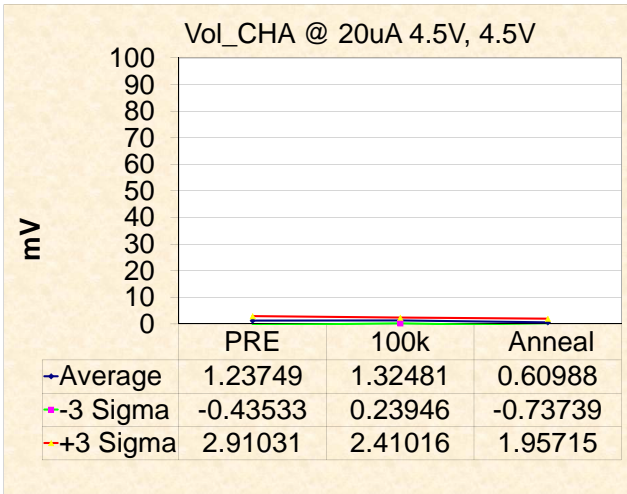
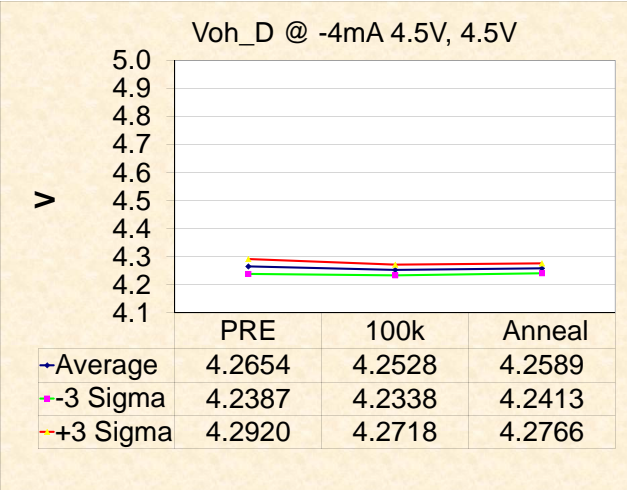
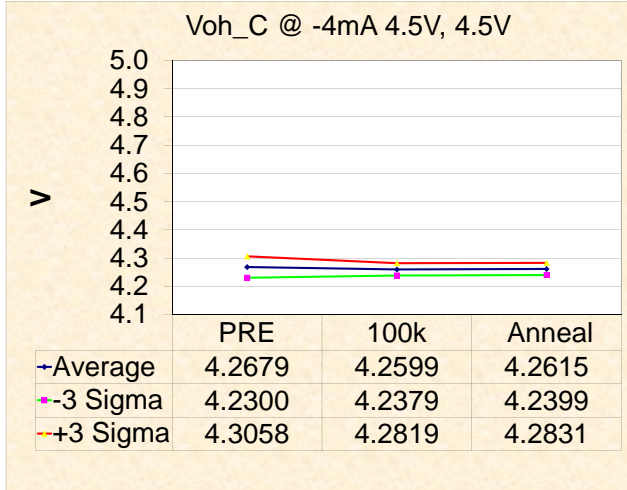
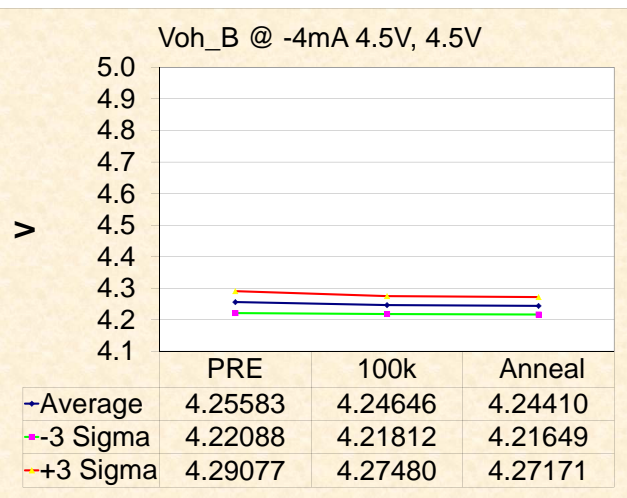
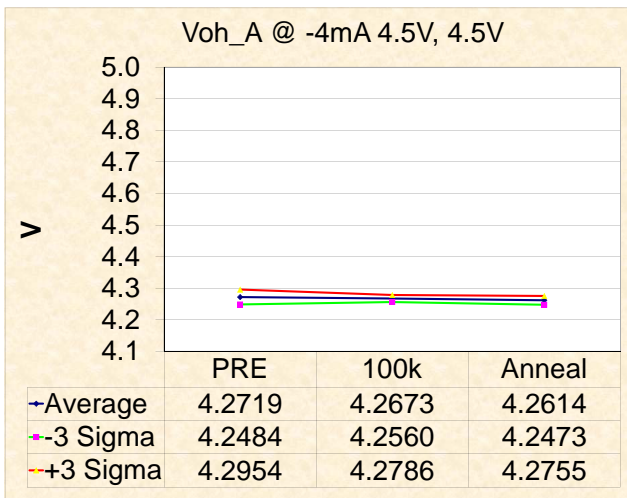


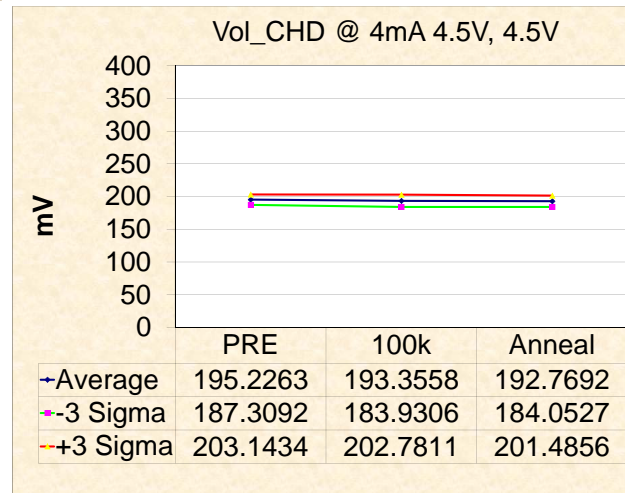
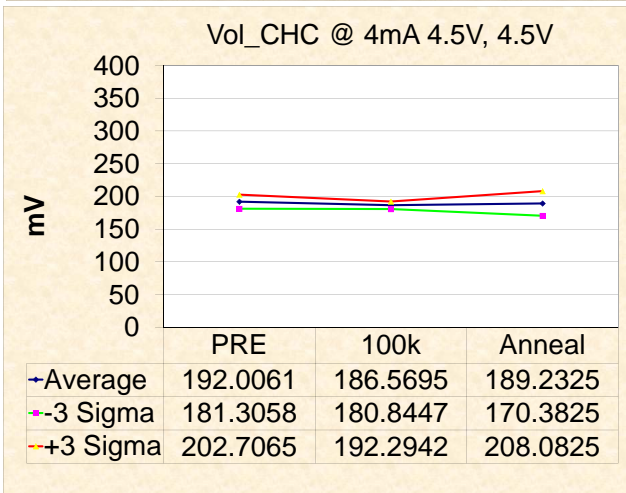
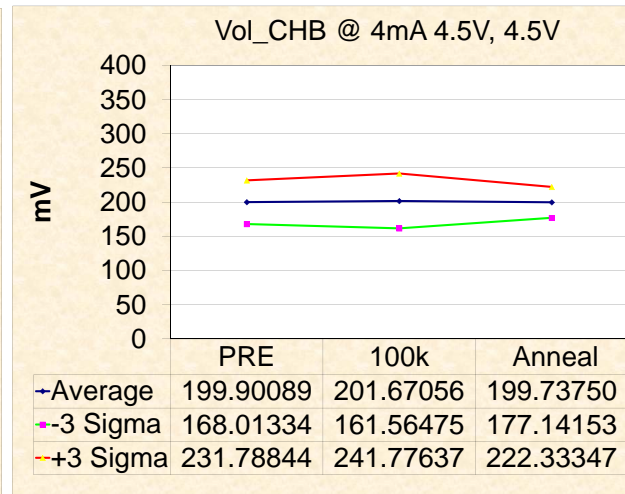
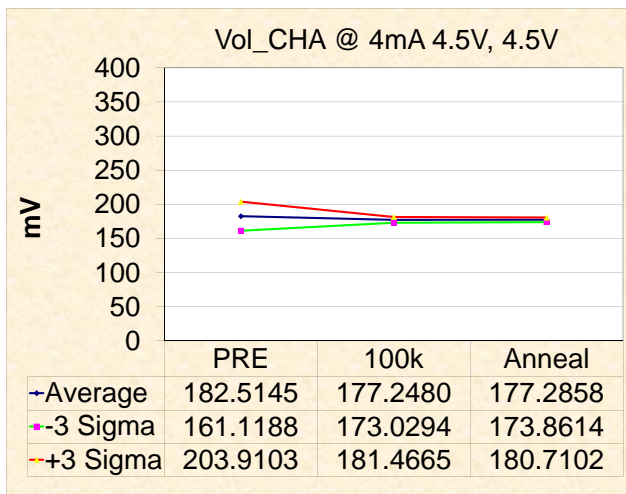
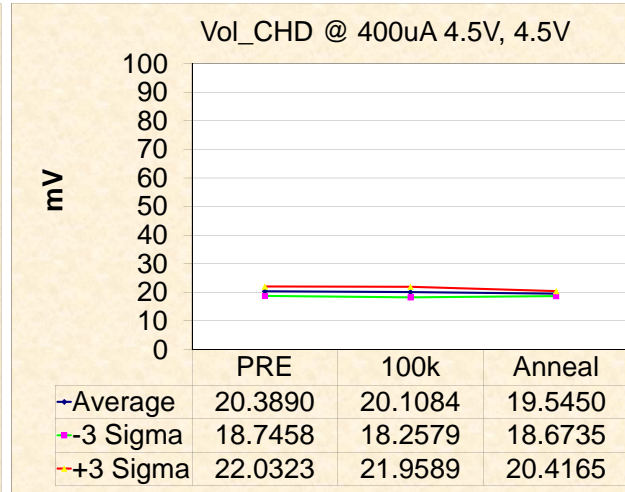
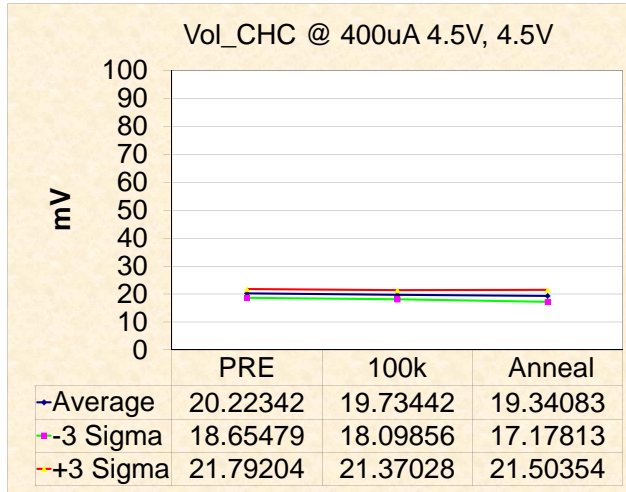
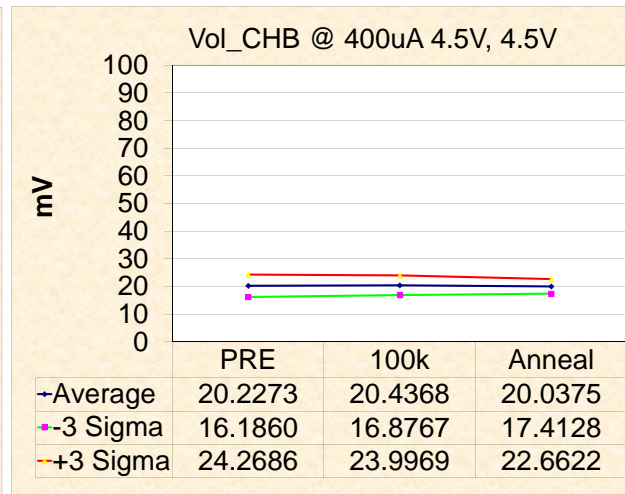
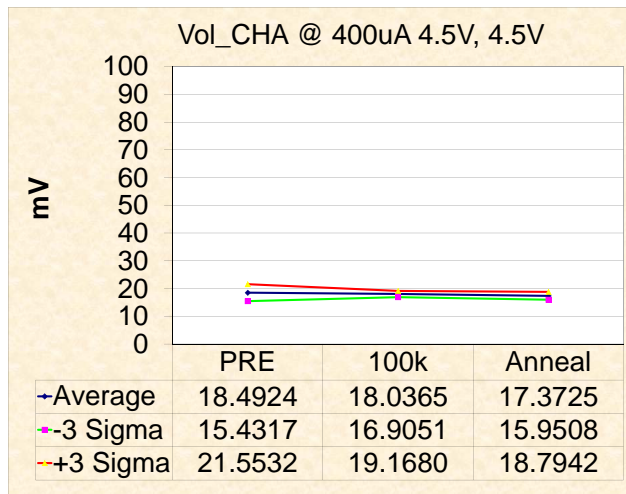
	PRE	100k	Anneal
→Average	4.49662	4.49319	4.4941808
→-3 Sigma	4.49082	4.48809	4.4920541
→+3 Sigma	4.50241	4.49829	4.4963075

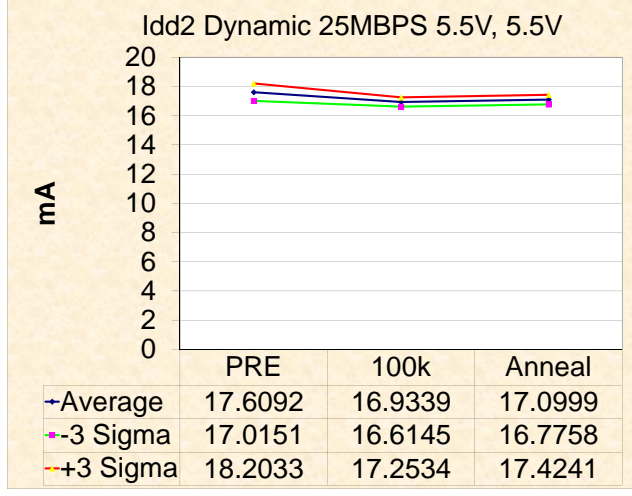
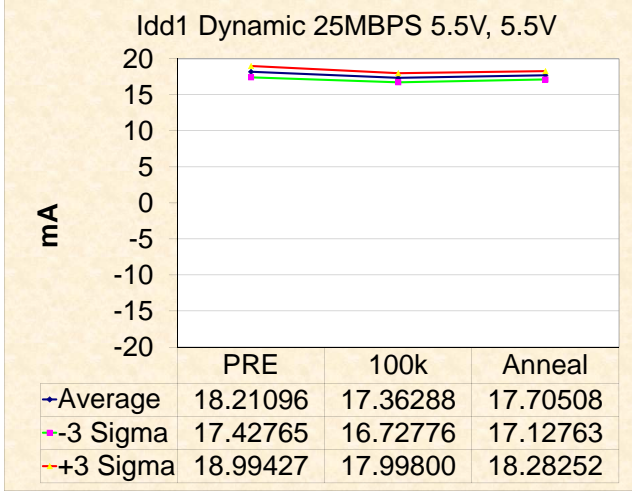
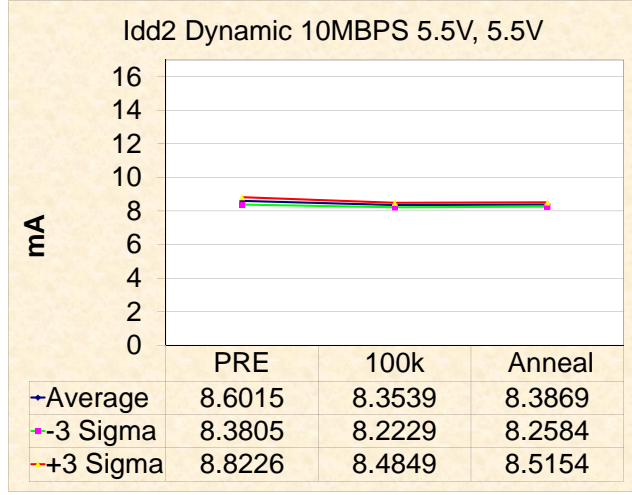
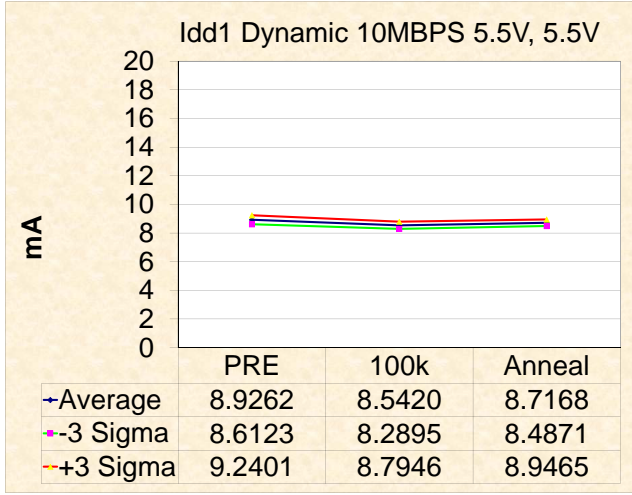
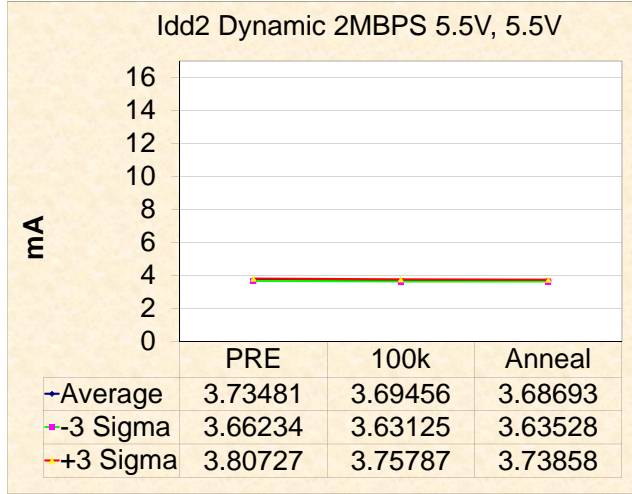
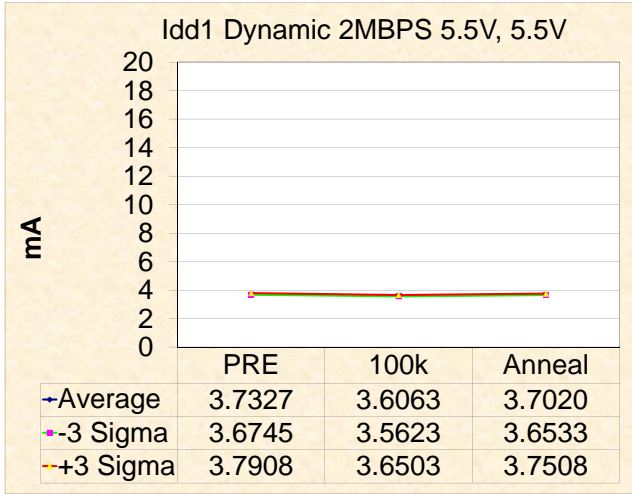
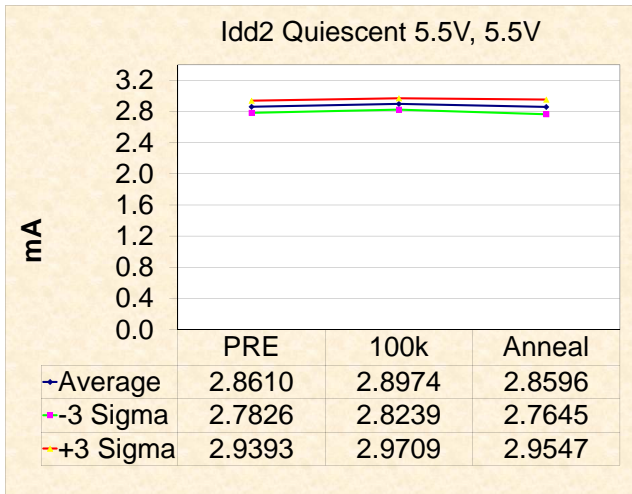
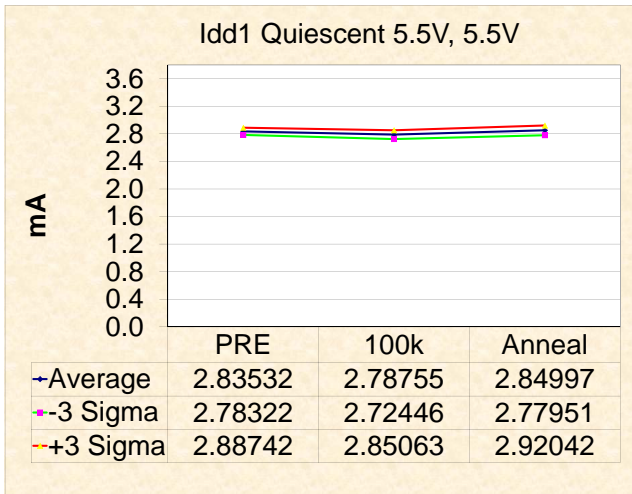
Voh_D @ -20uA 4.5V, 4.5V

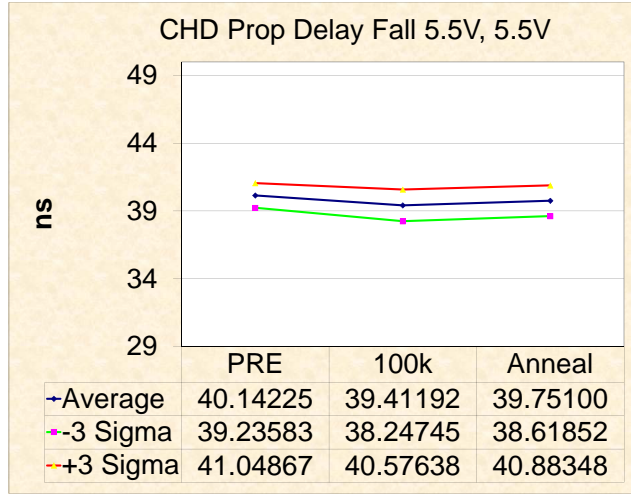
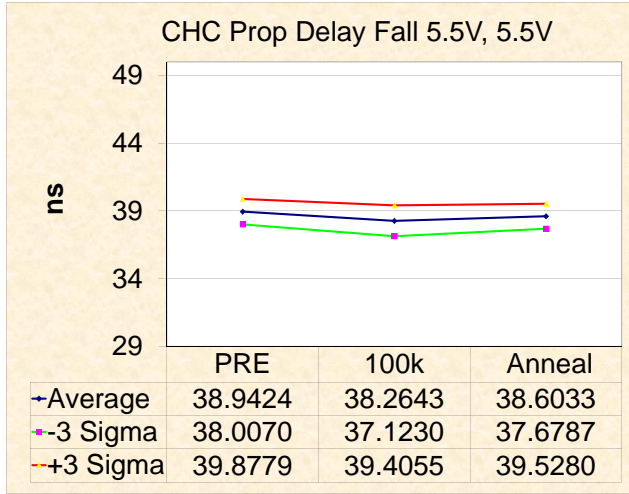
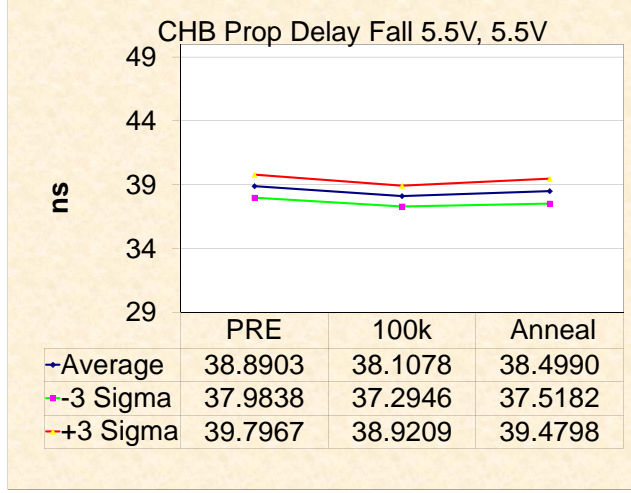
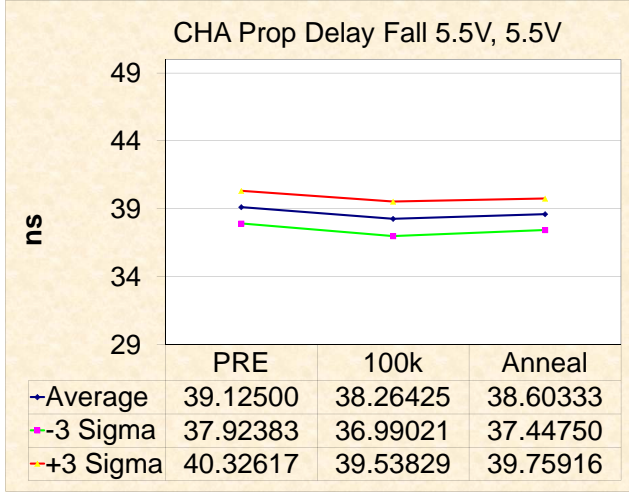
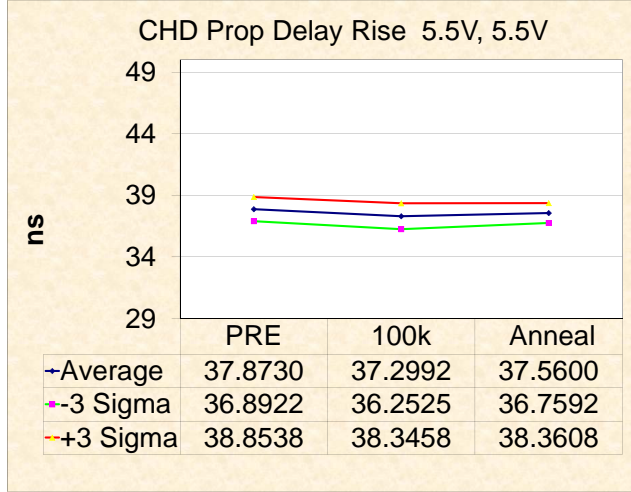
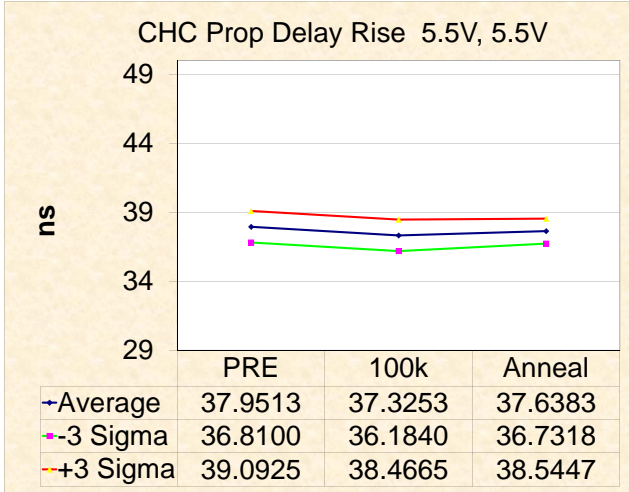
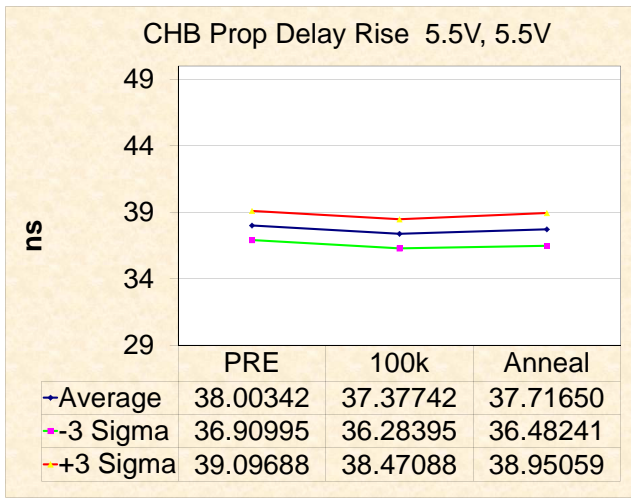
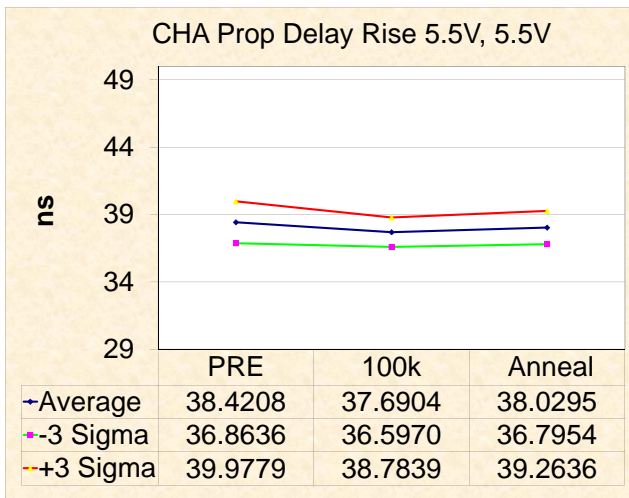


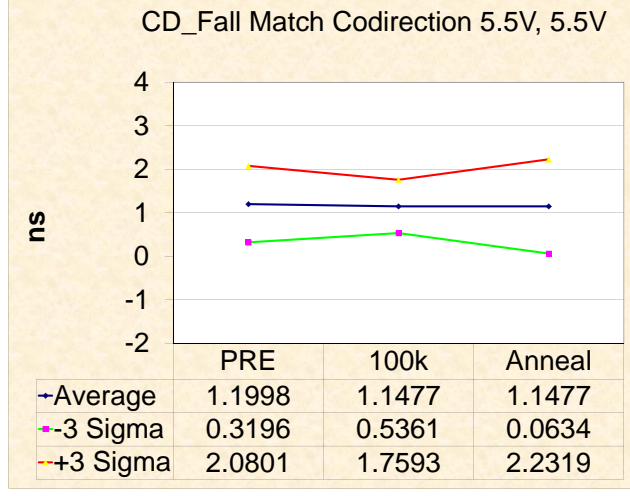
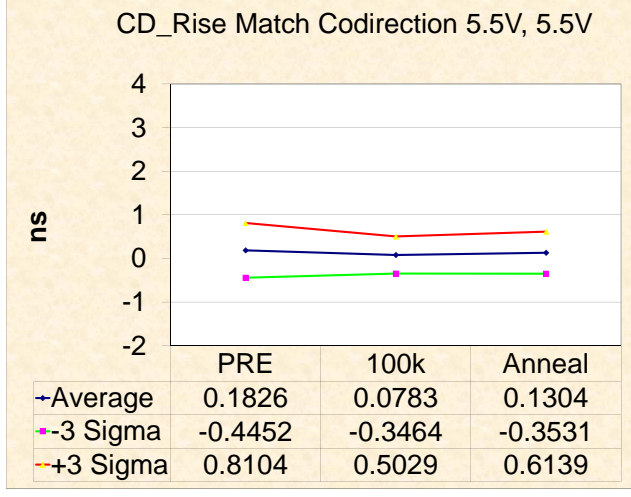
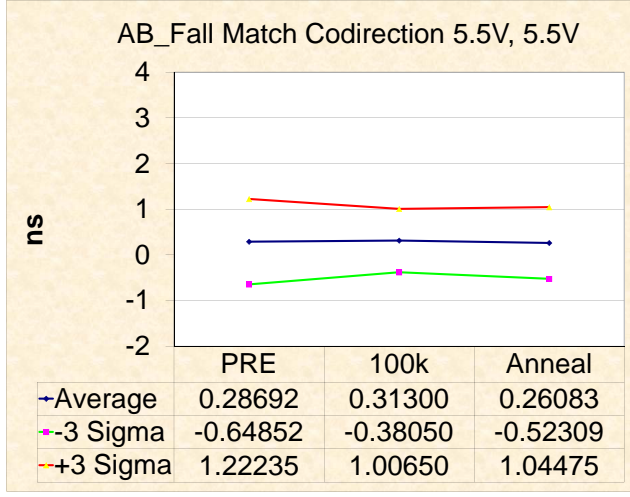
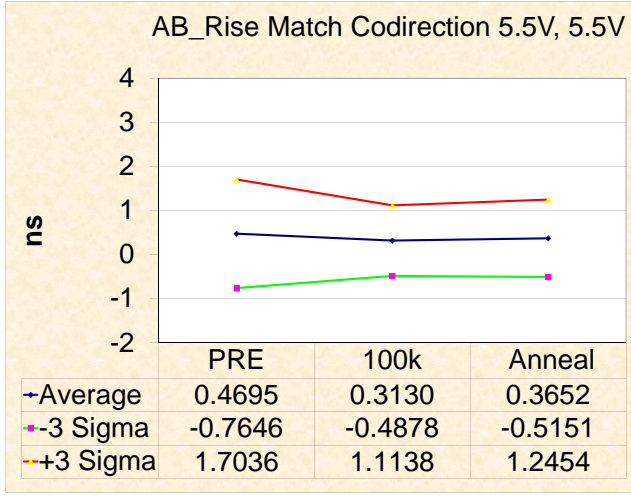
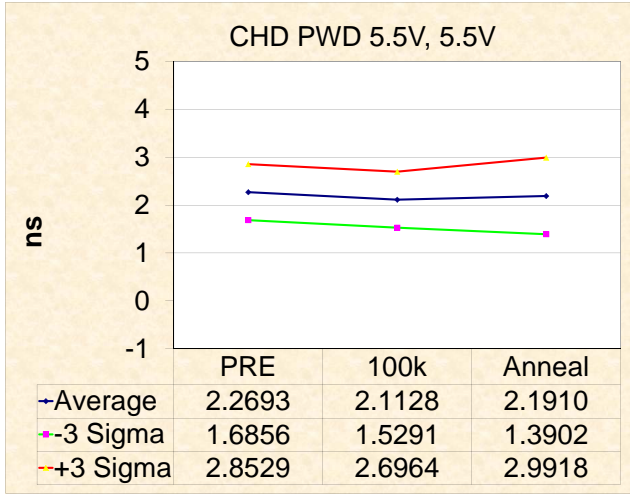
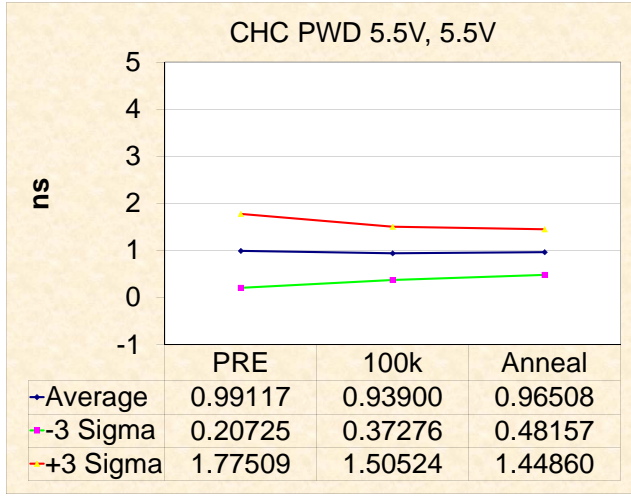
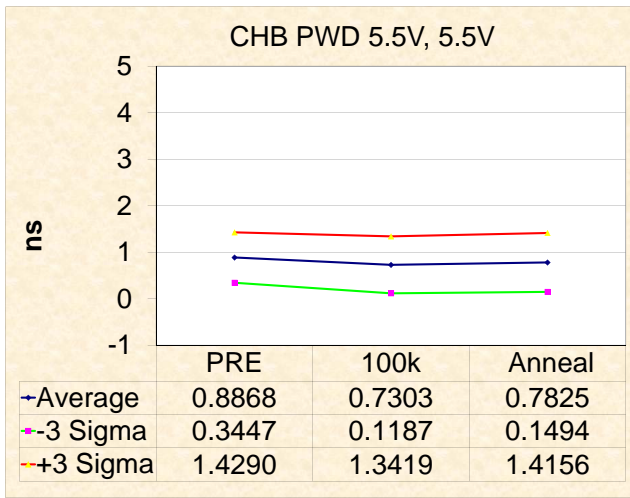
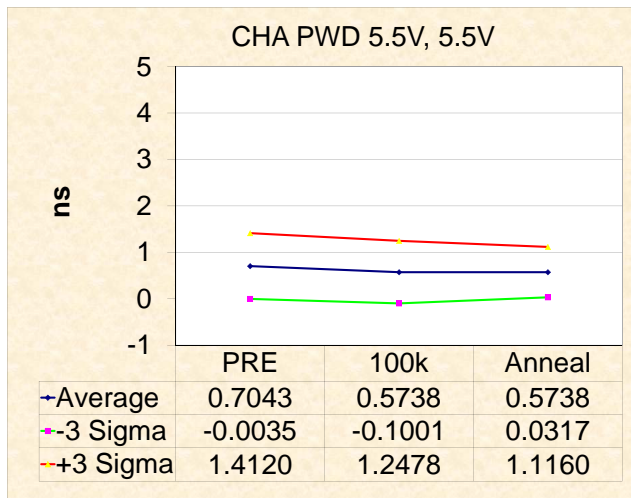
	PRE	100k	Anneal
→Average	4.4964	4.4923	4.4939
→-3 Sigma	4.4905	4.4867	4.4918
→+3 Sigma	4.5022	4.4979	4.4961

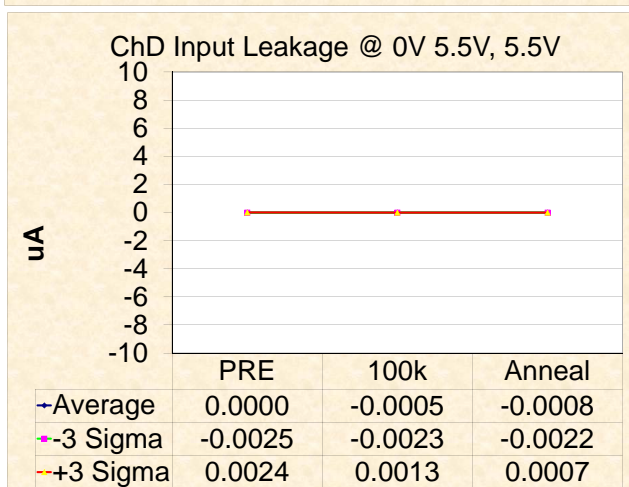
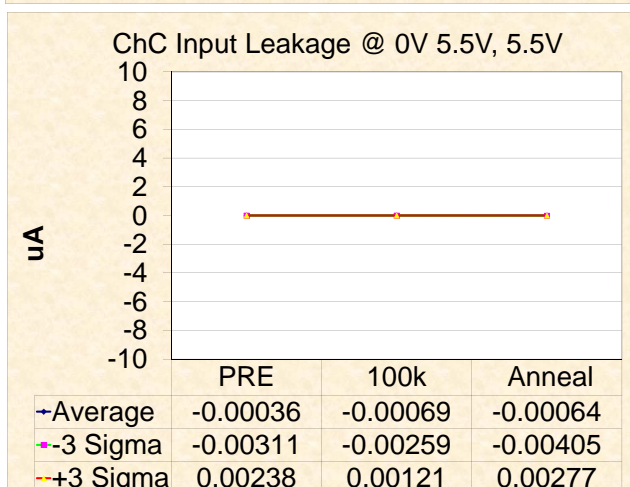
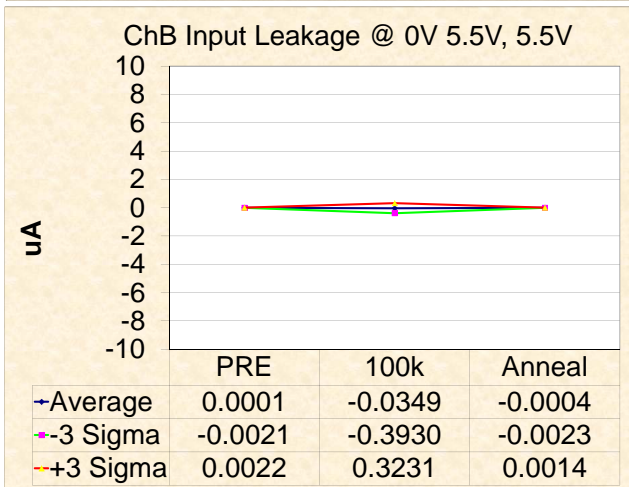
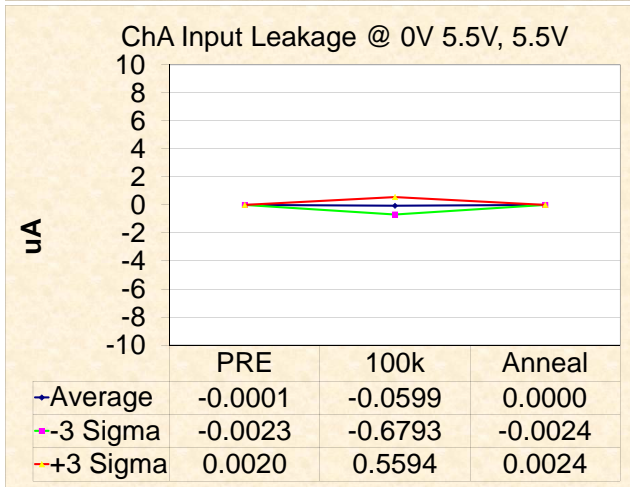
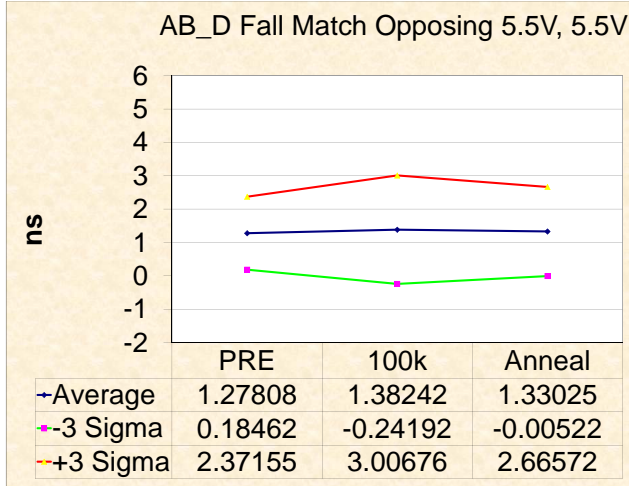
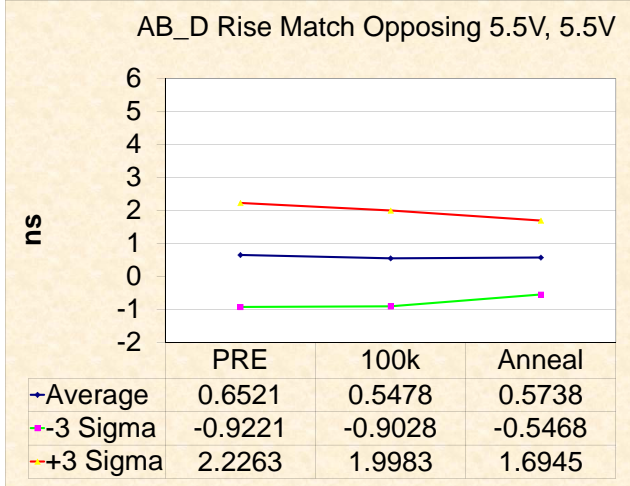
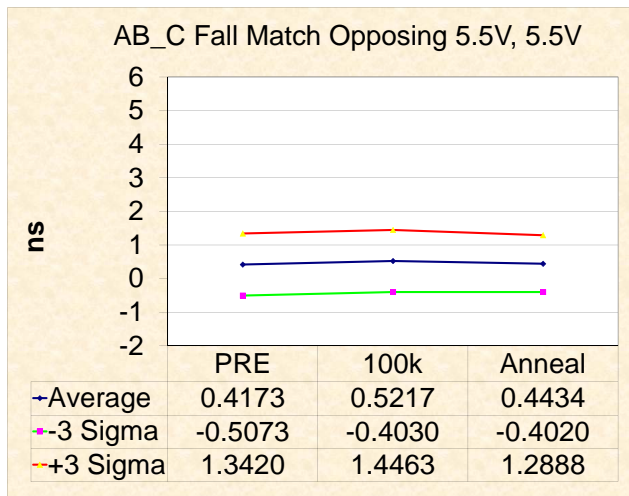
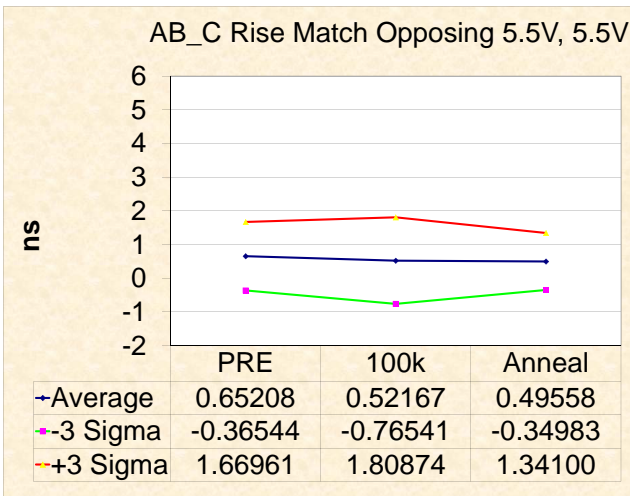


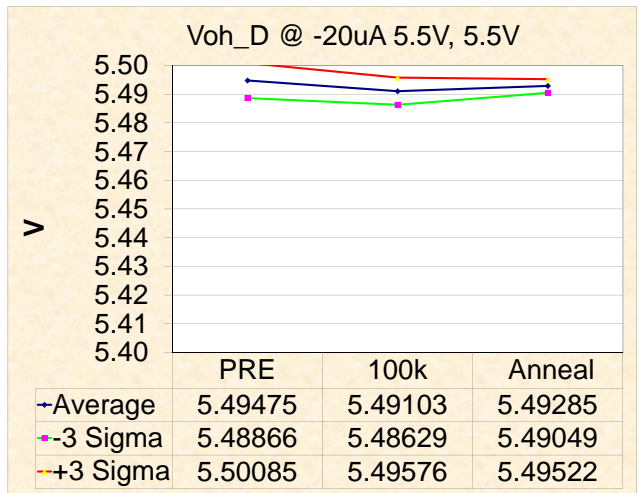
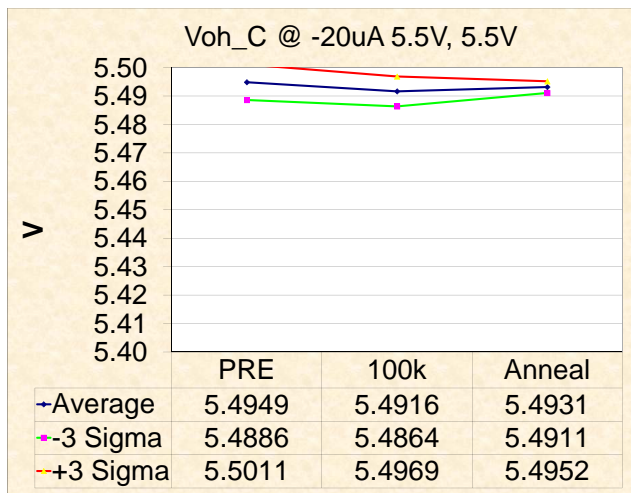
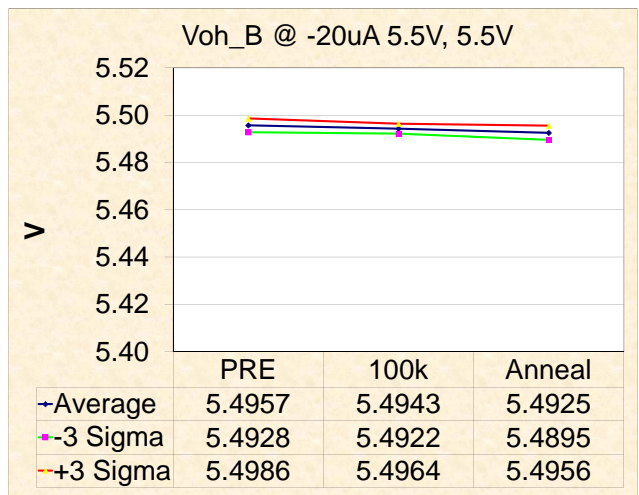
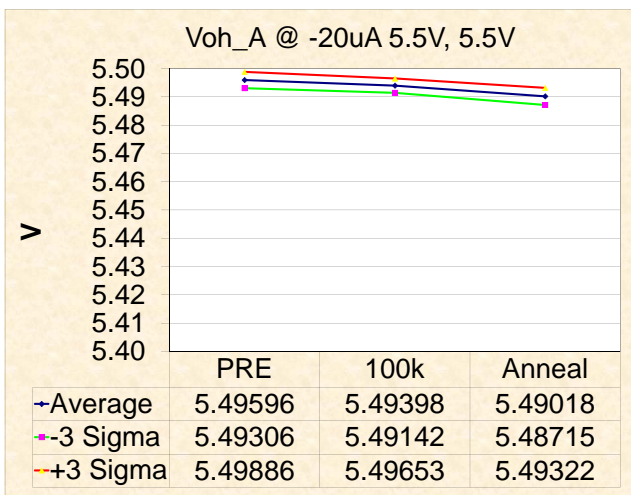
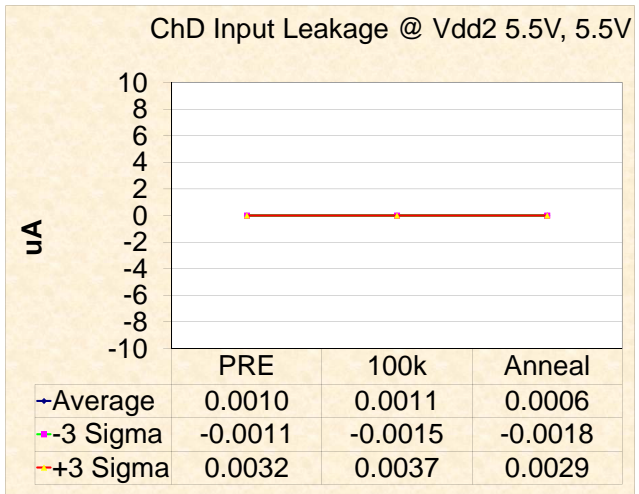
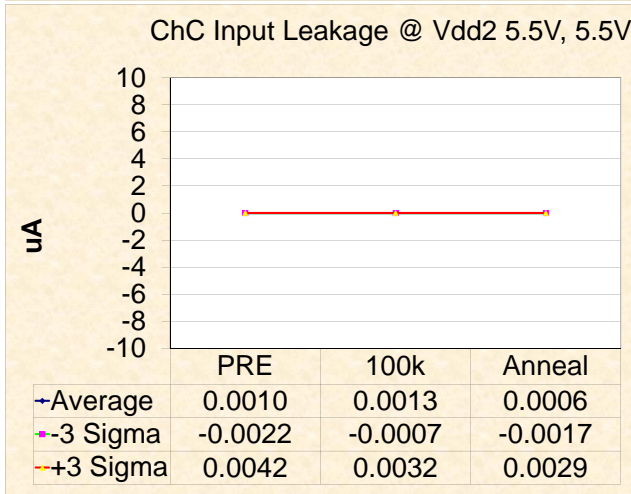
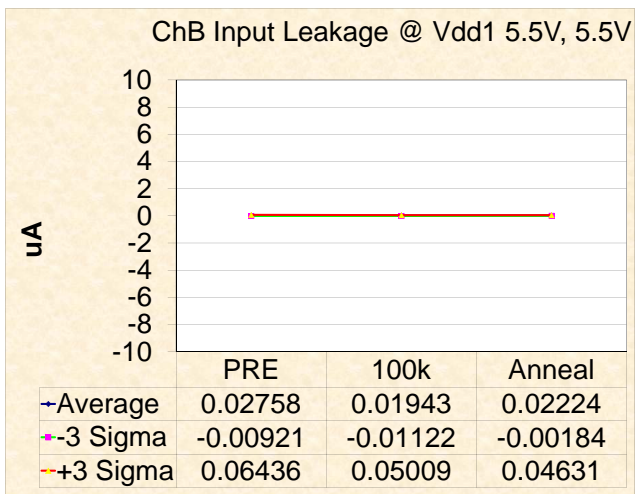
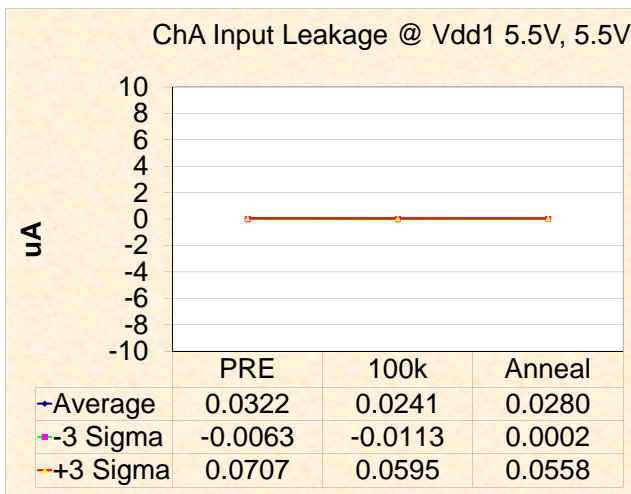


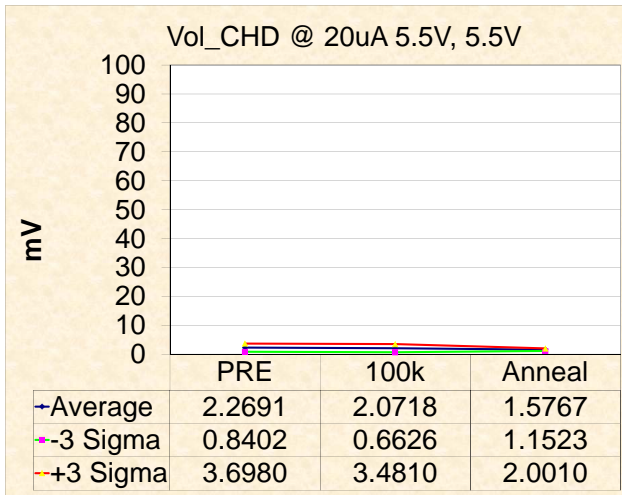
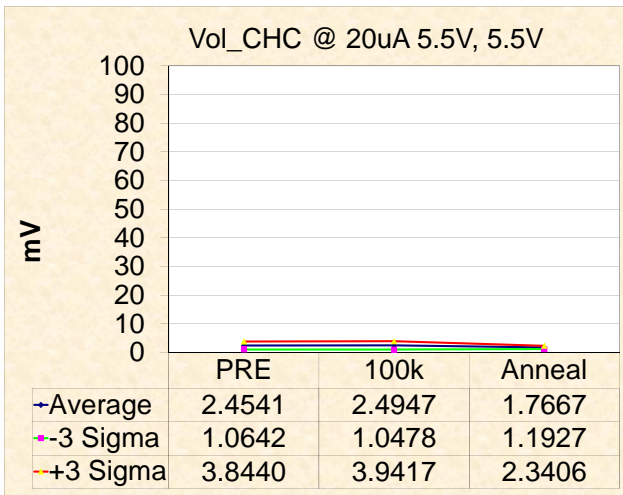
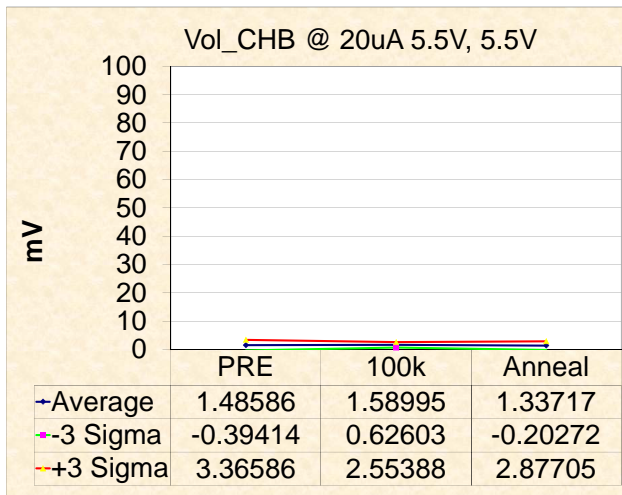
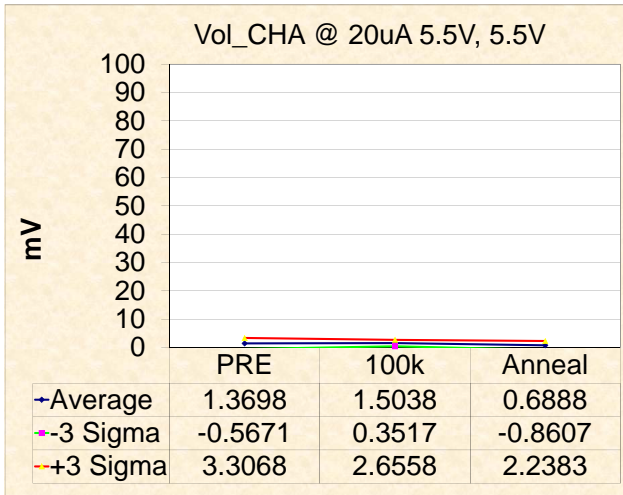
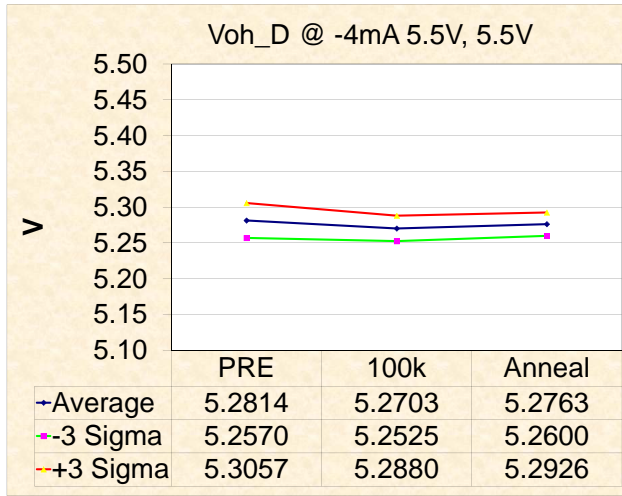
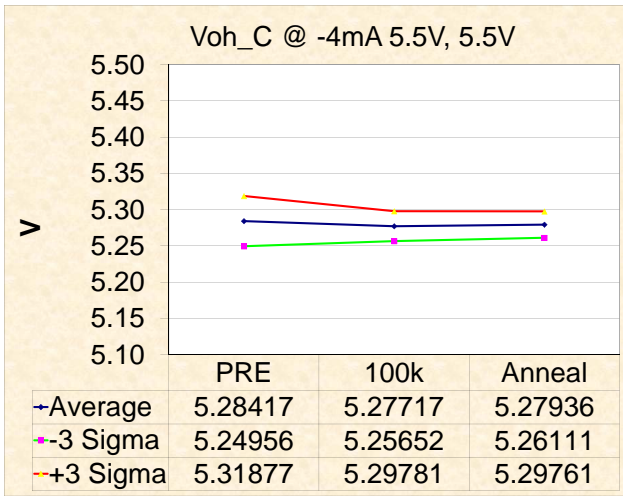
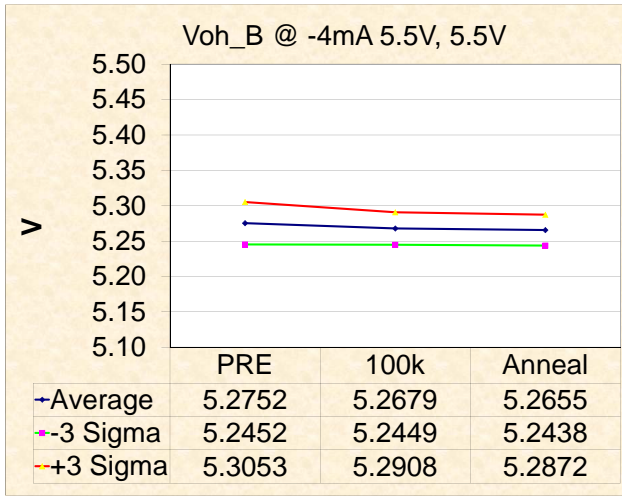
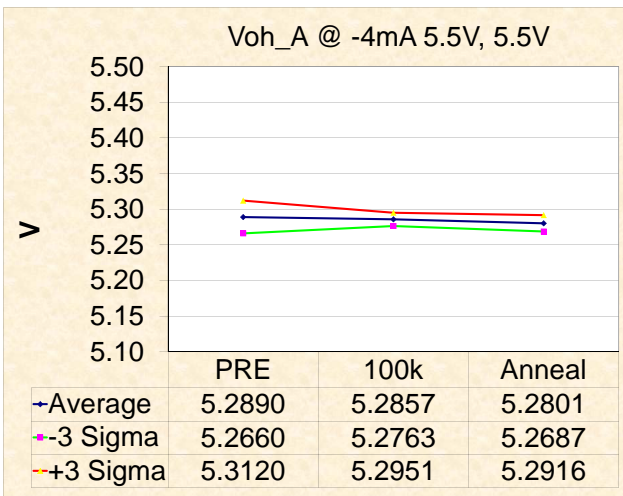


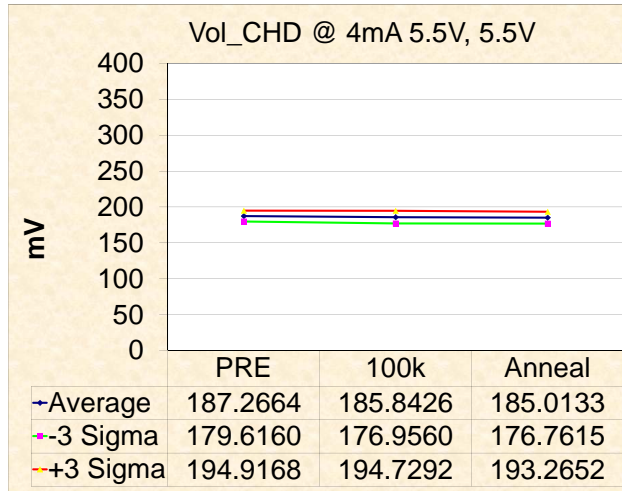
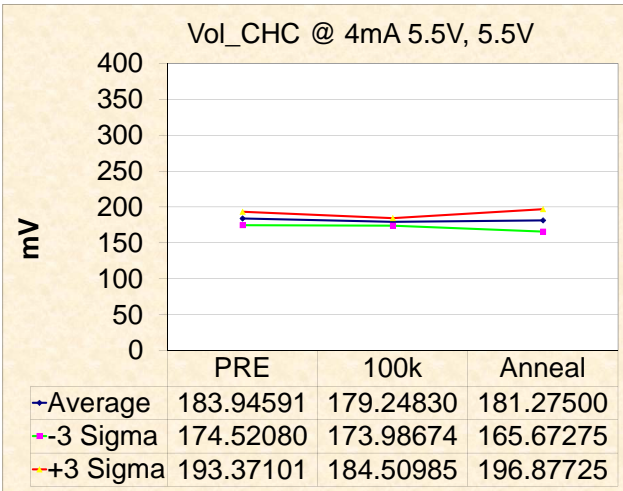
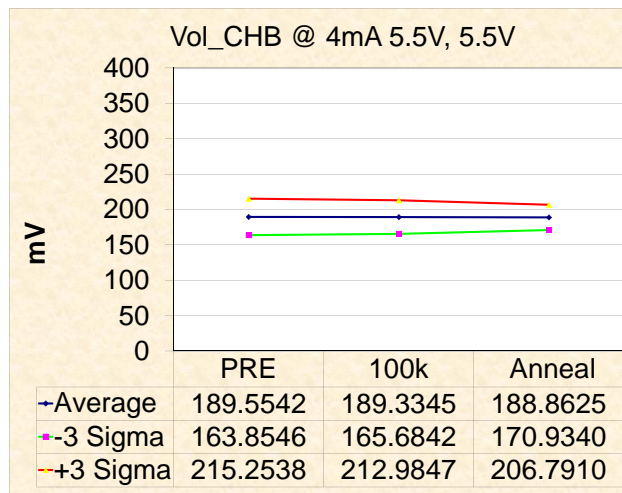
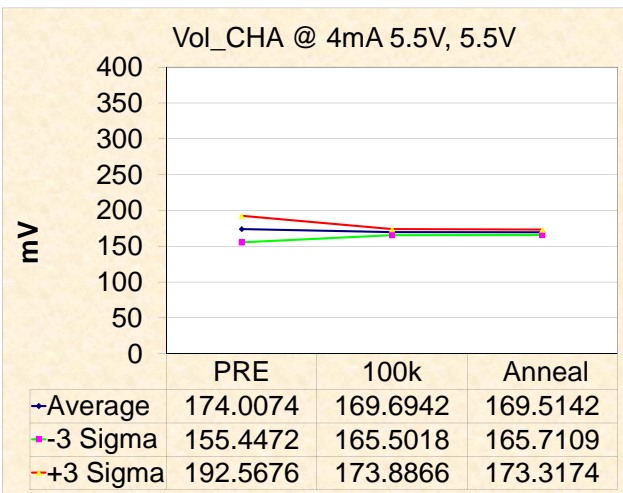
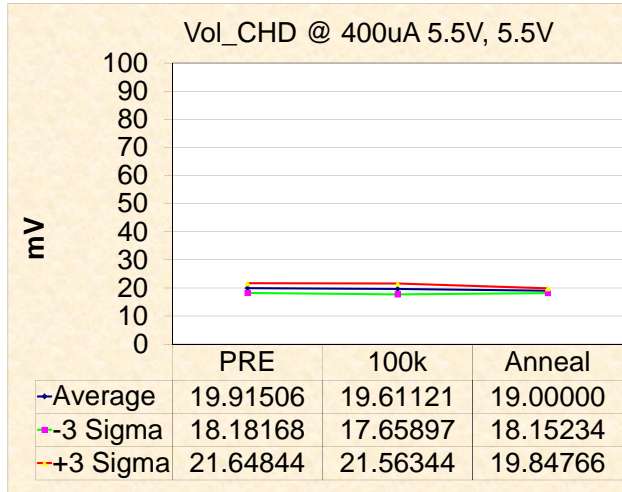
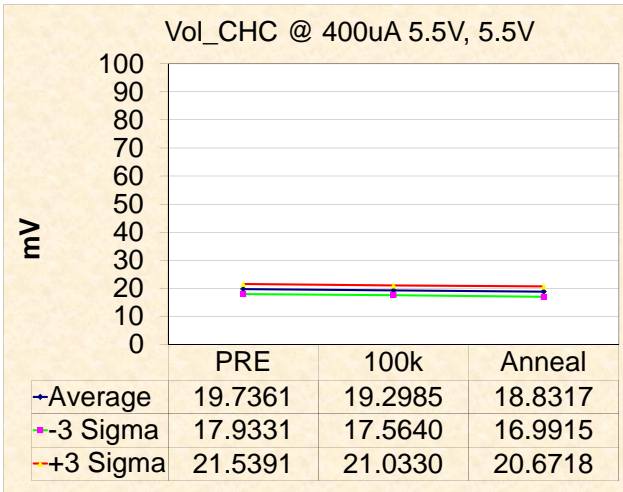
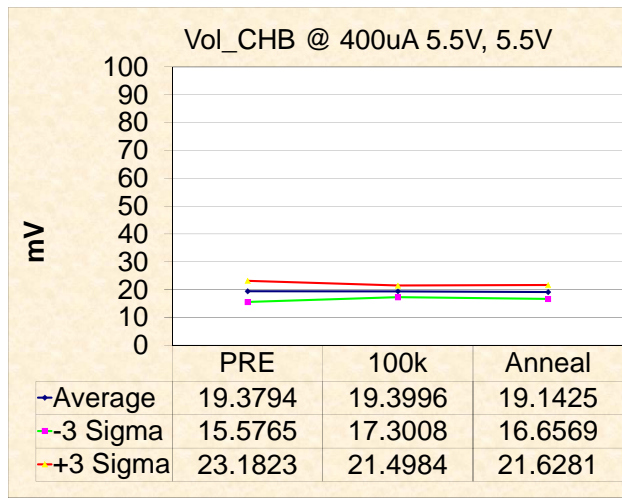
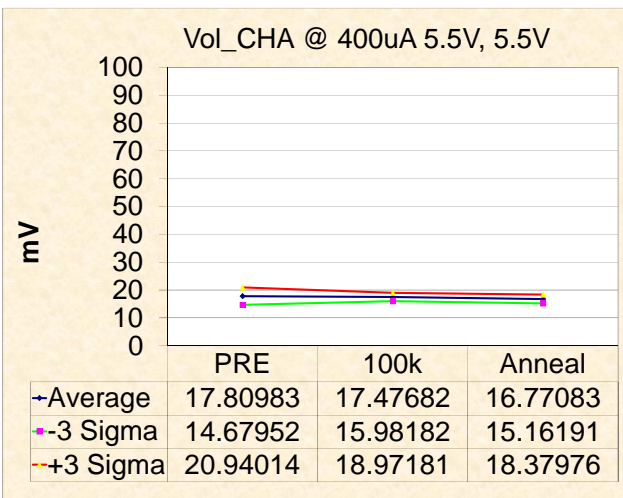


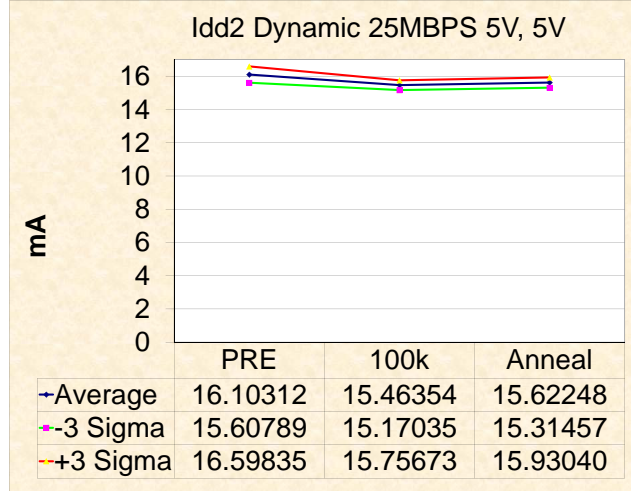
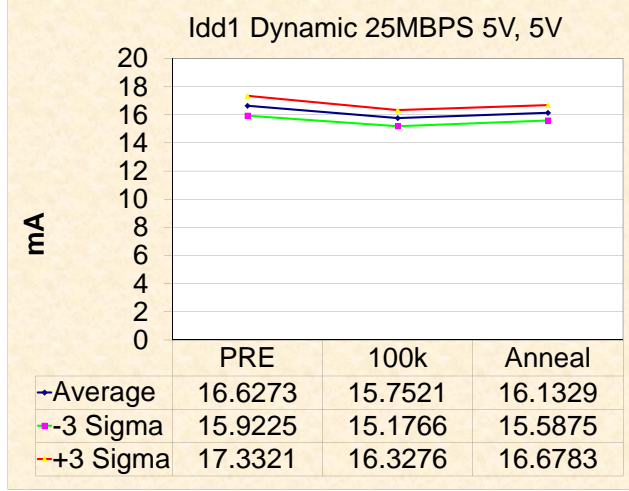
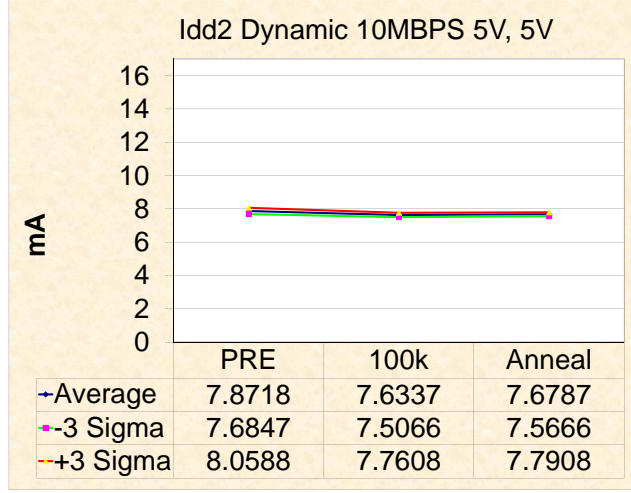
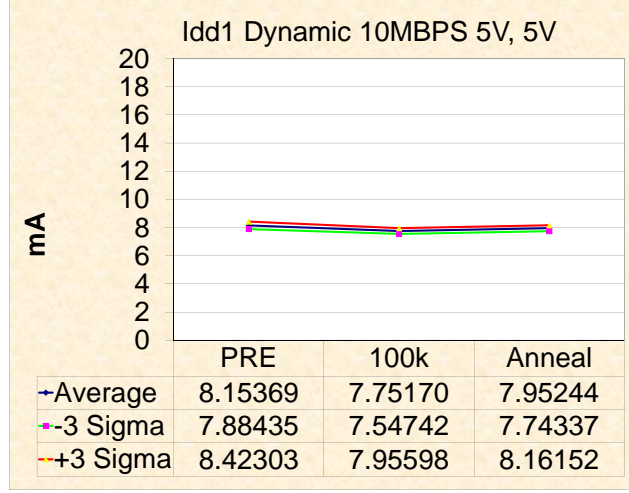
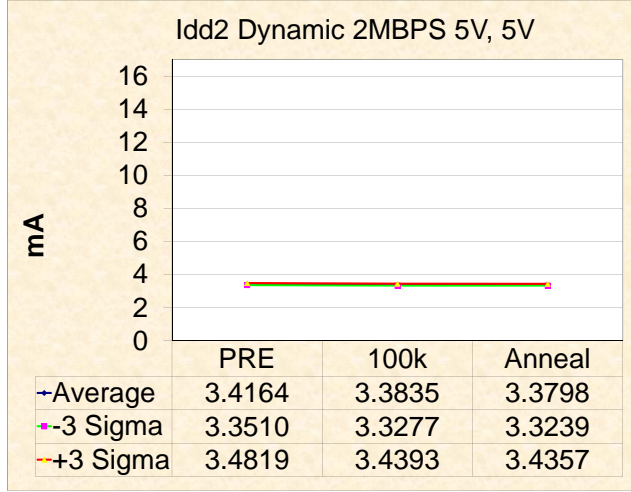
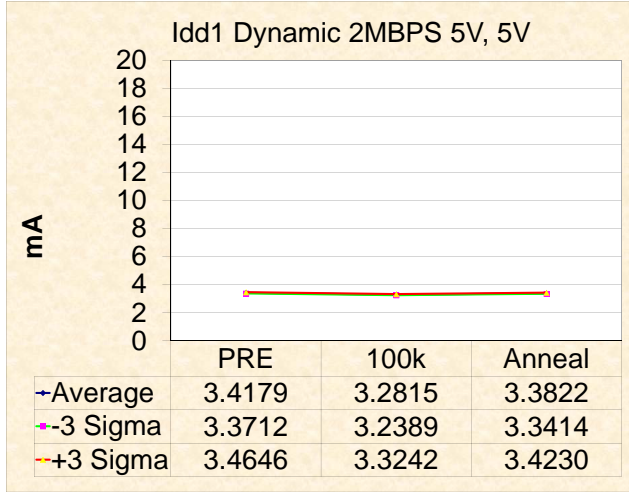
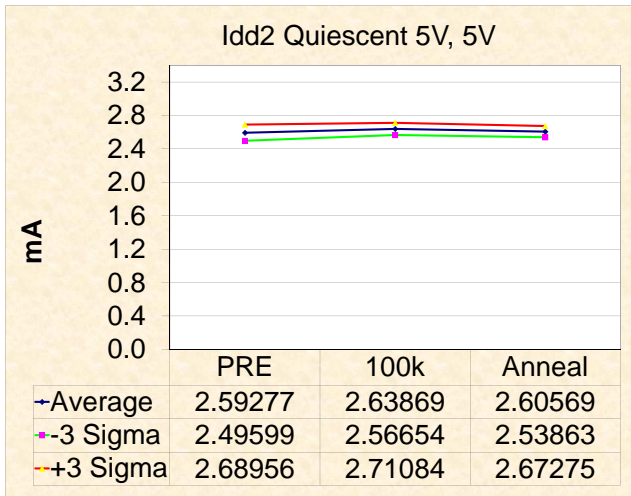
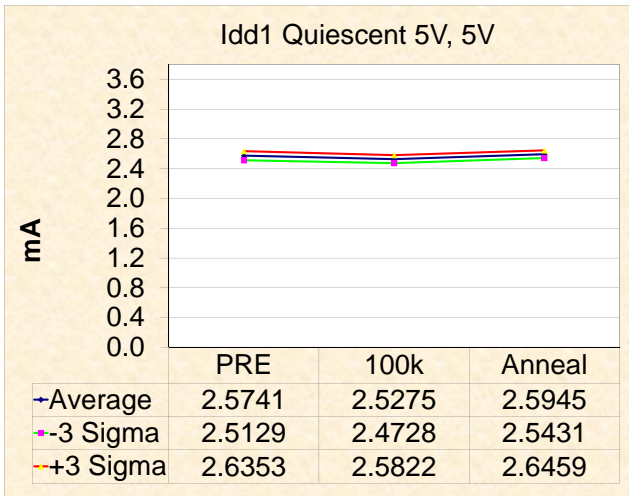


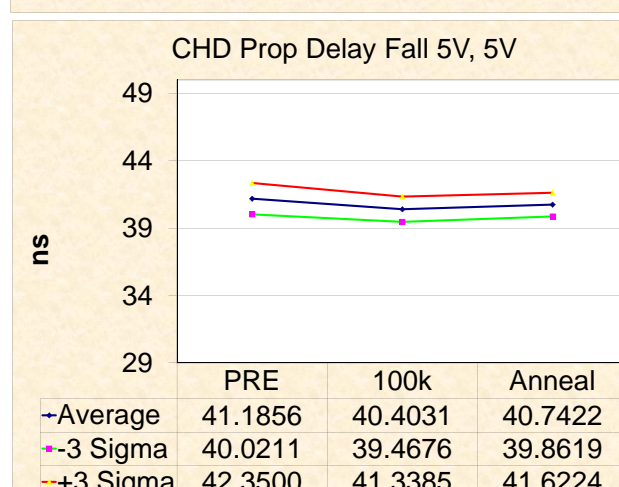
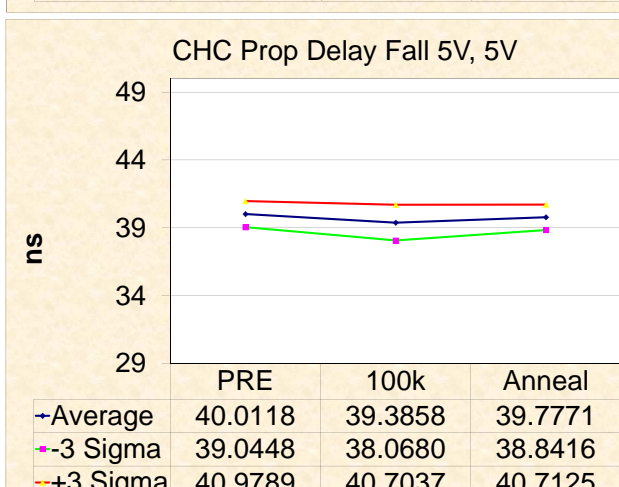
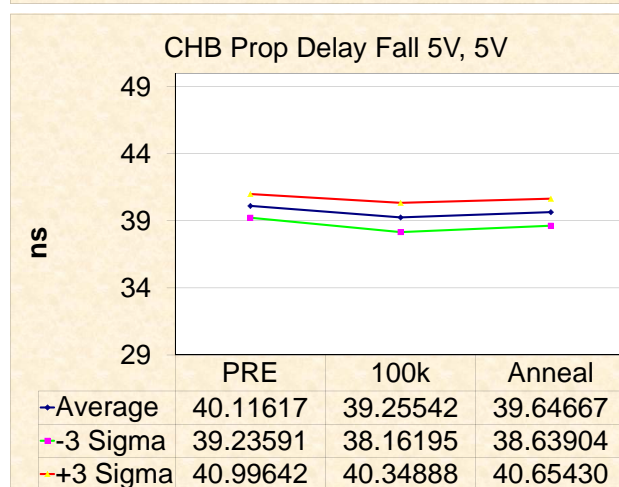
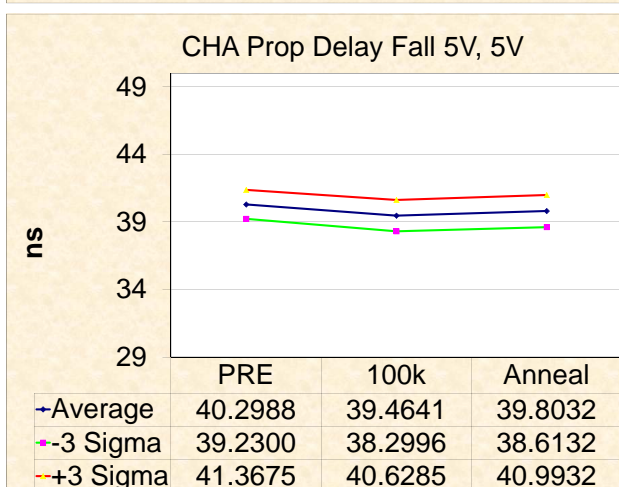
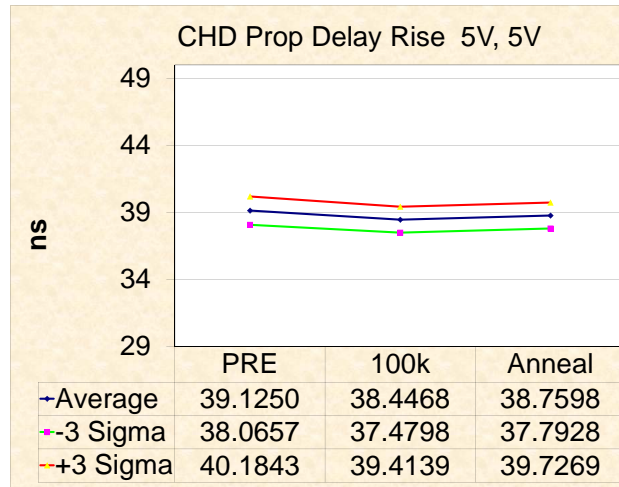
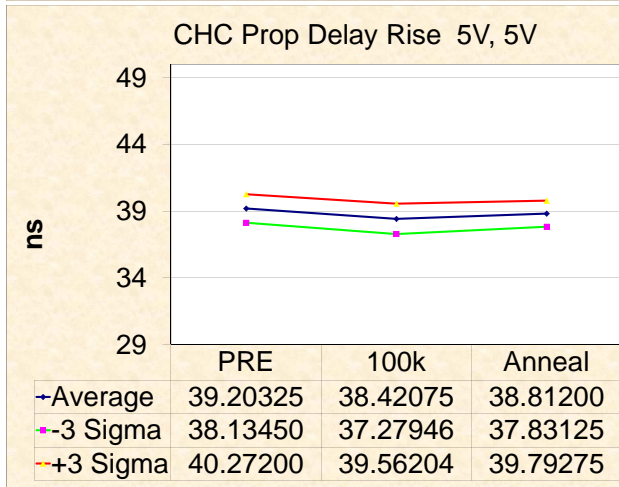
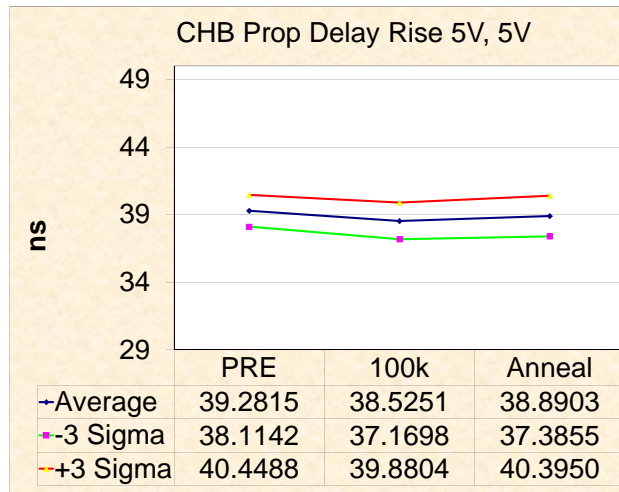
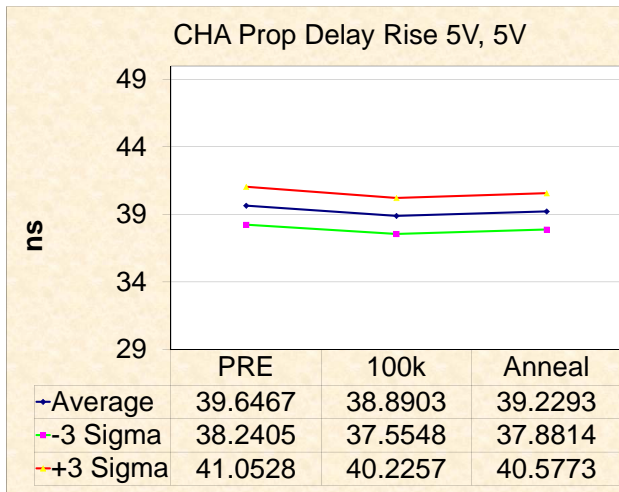


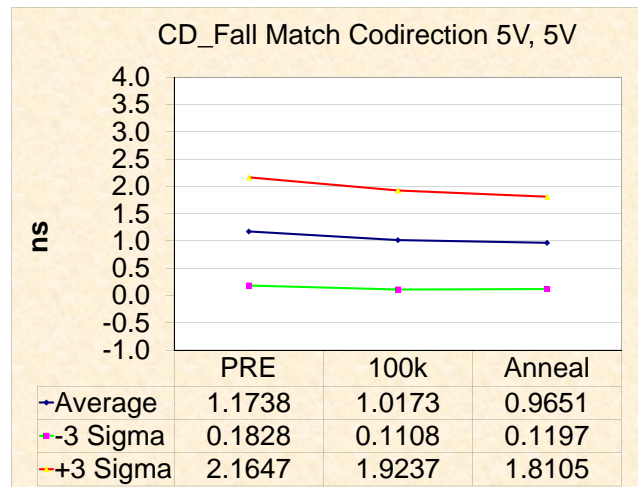
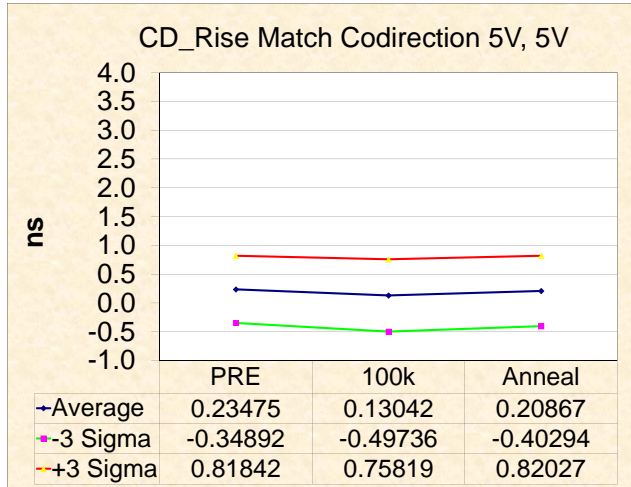
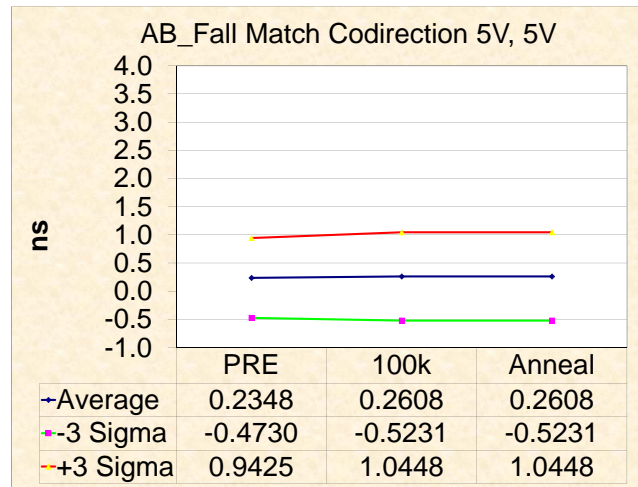
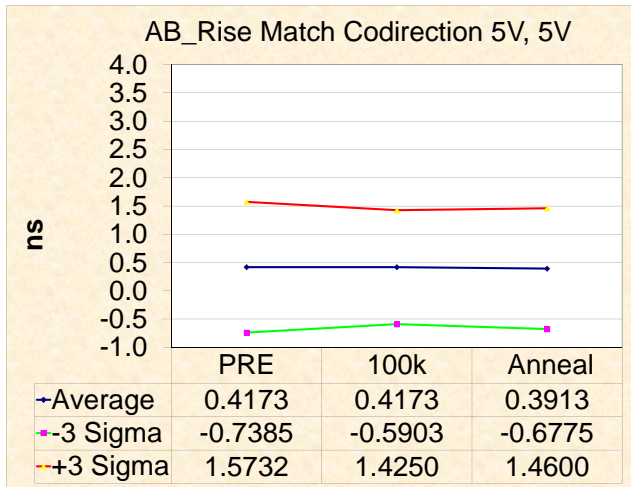
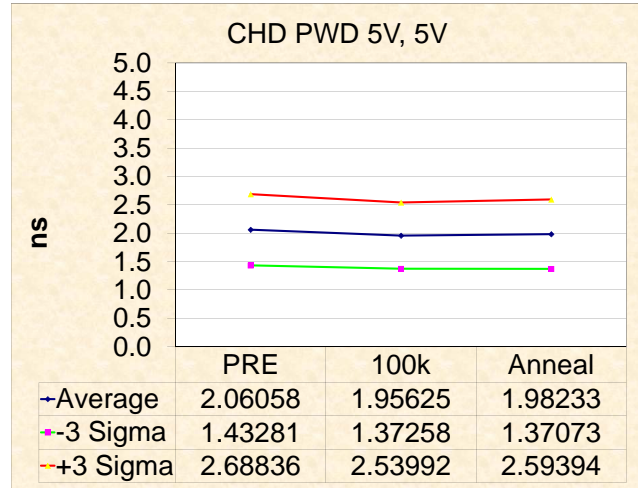
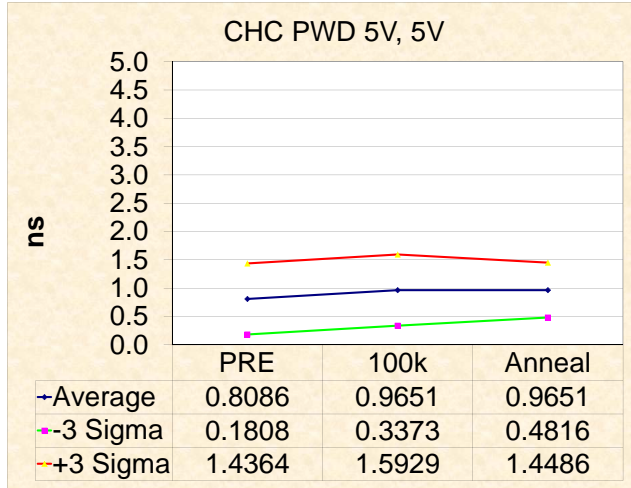
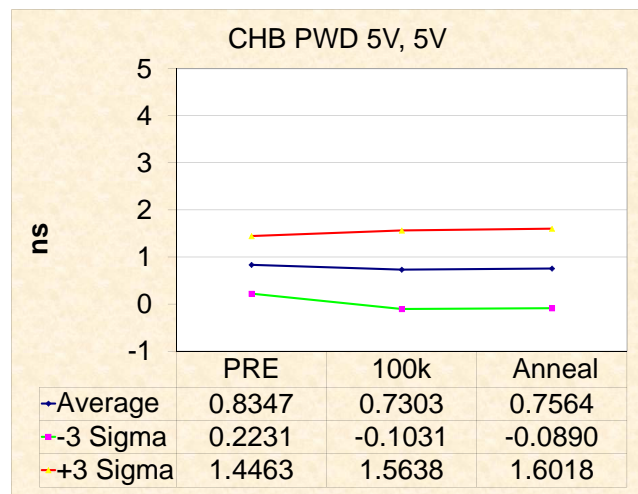
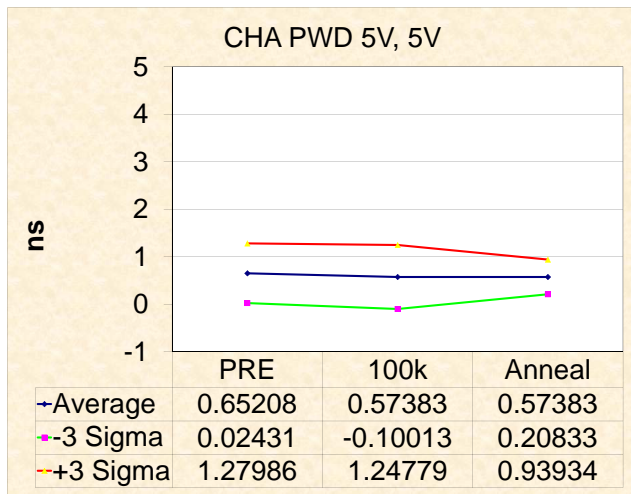


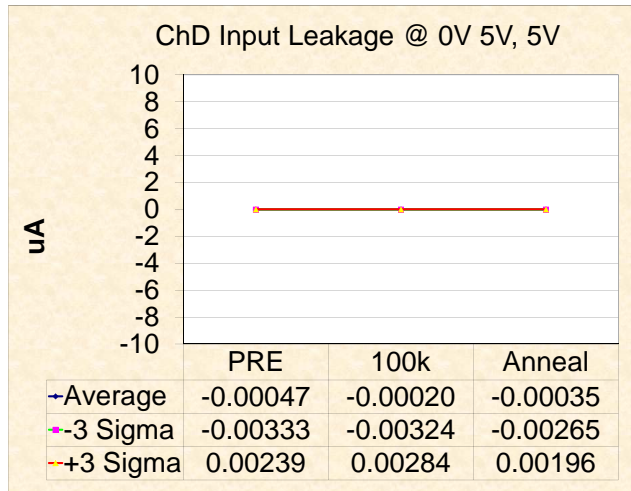
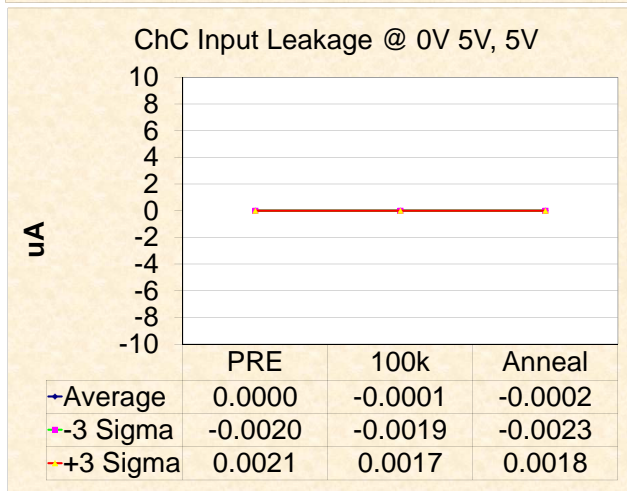
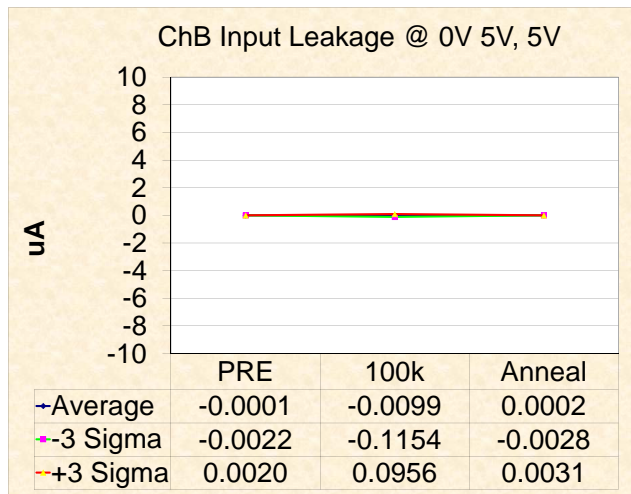
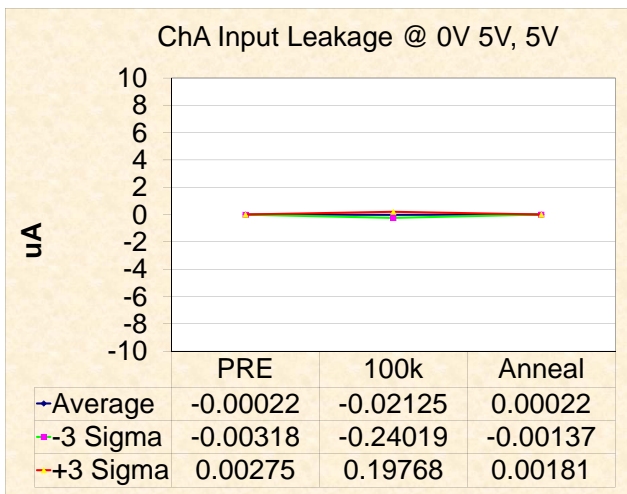
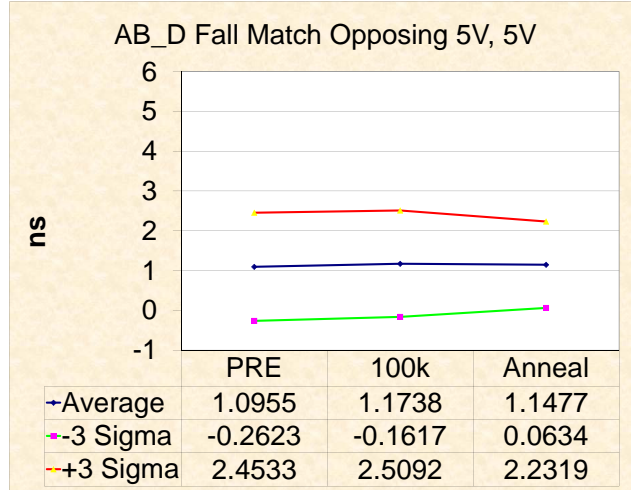
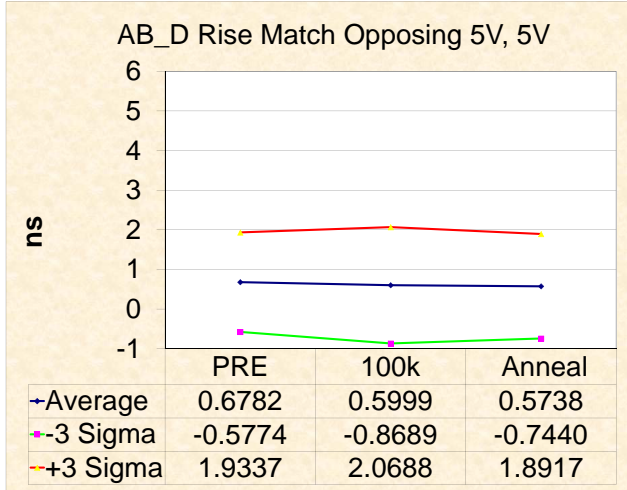
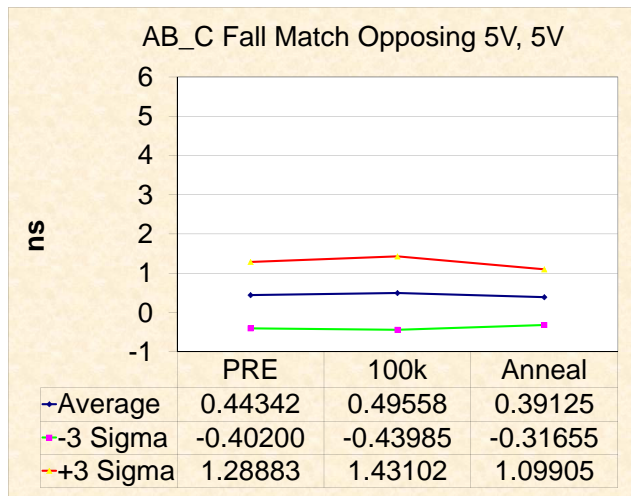
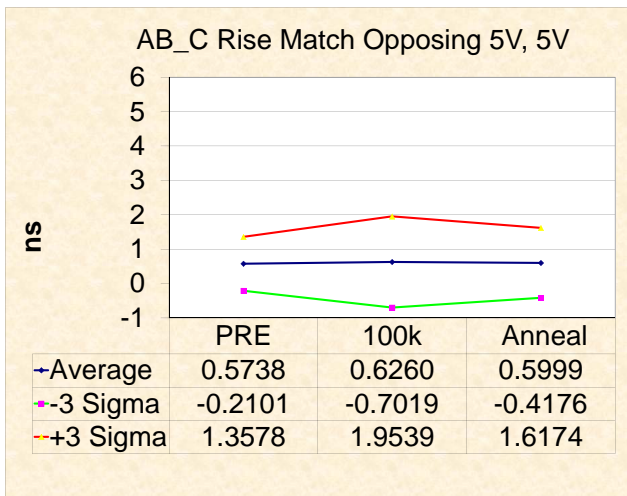




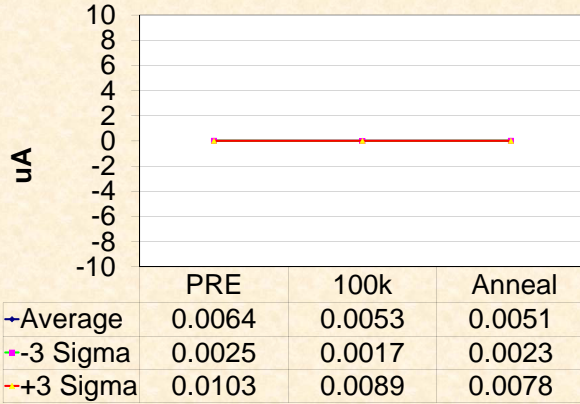




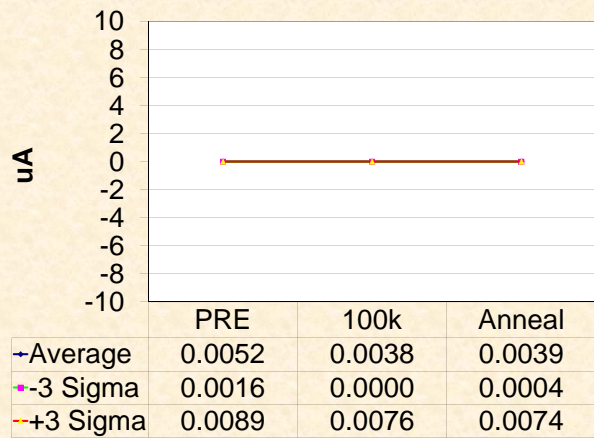




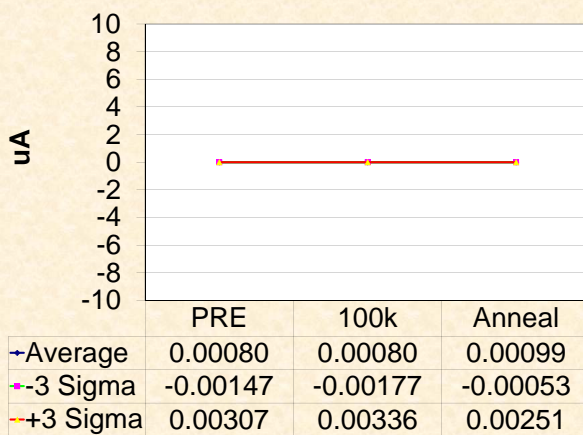
ChA Input Leakage @ Vdd1 5V, 5V



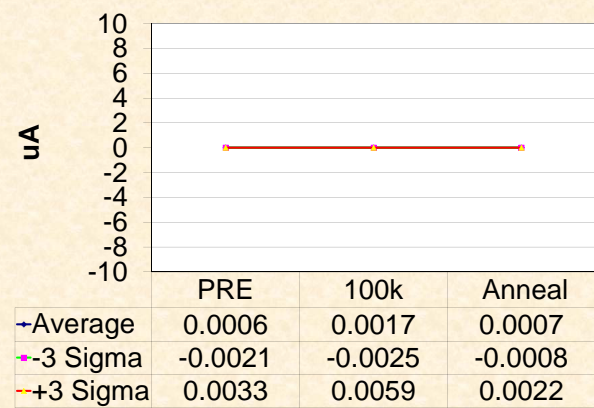
ChB Input Leakage @ Vdd1 5V, 5V



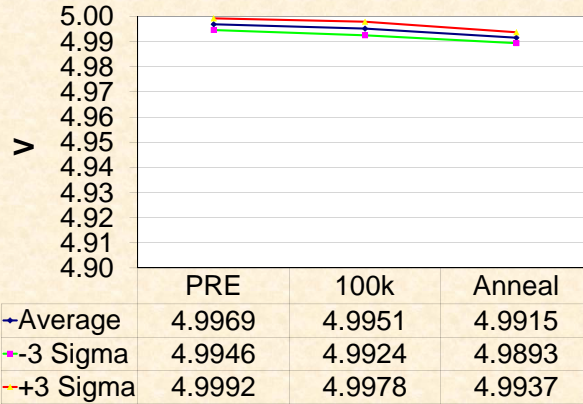
ChC Input Leakage @ Vdd2 5V, 5V



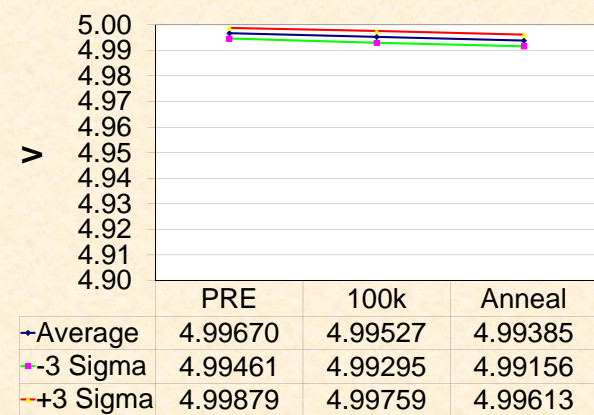
ChD Input Leakage @ Vdd2 5V, 5V



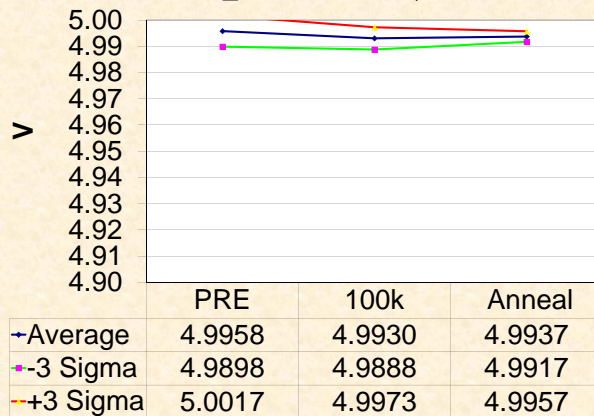
Voh_A @ -20uA 5V, 5V



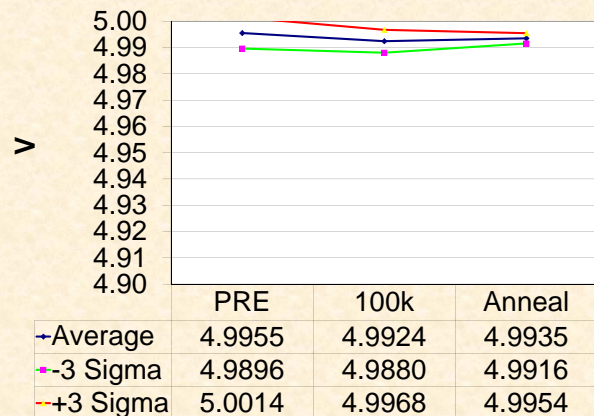
Voh_B @ -20uA 5V, 5V

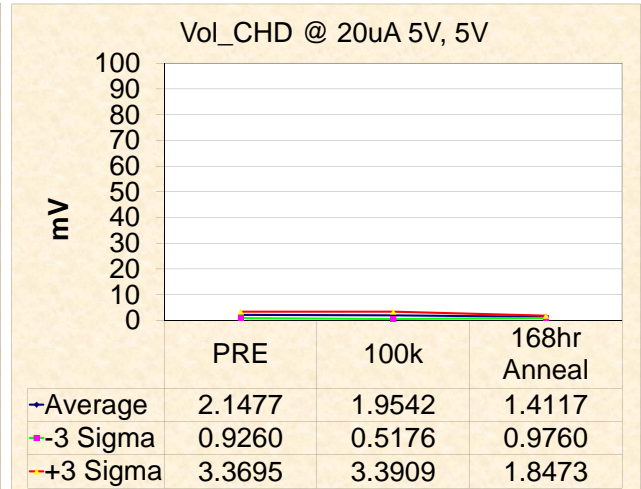
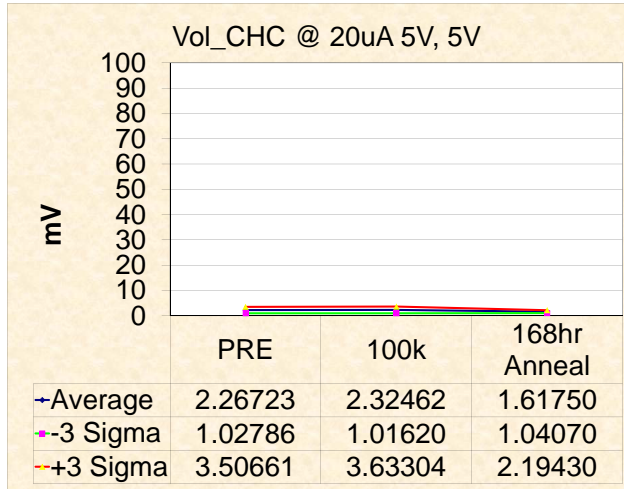
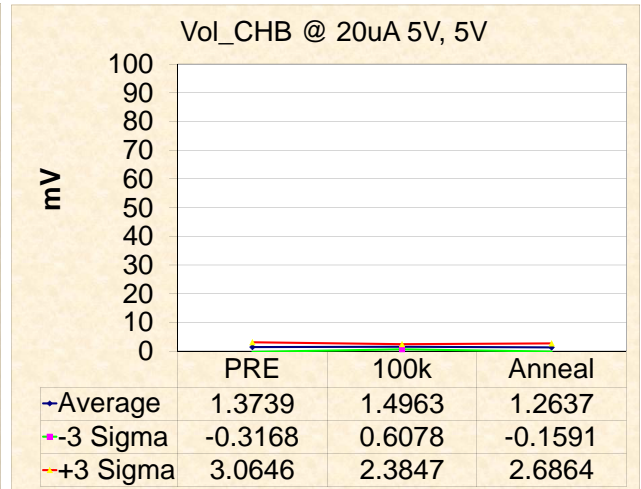
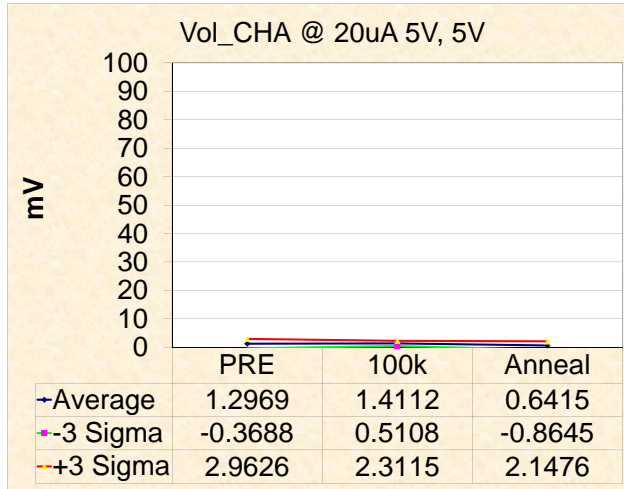
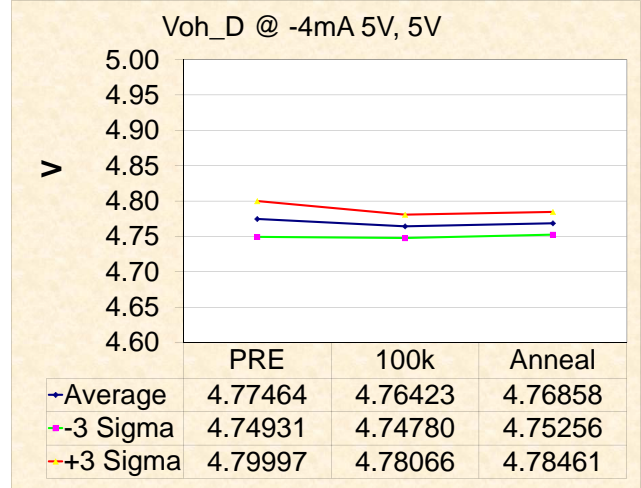
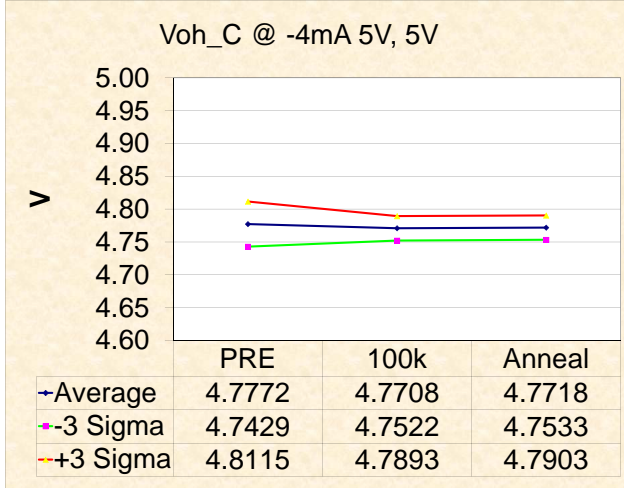
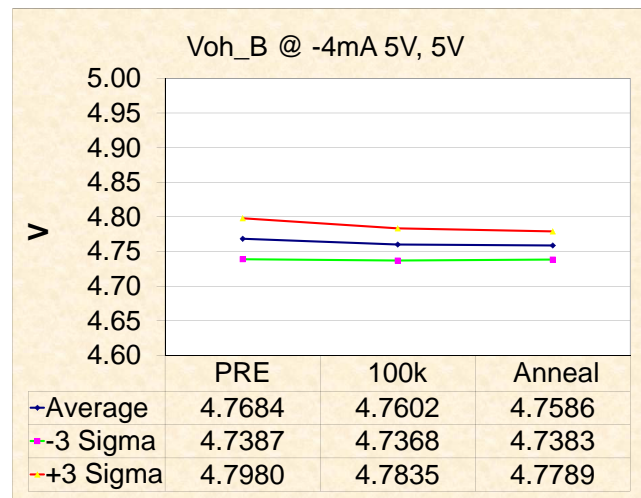
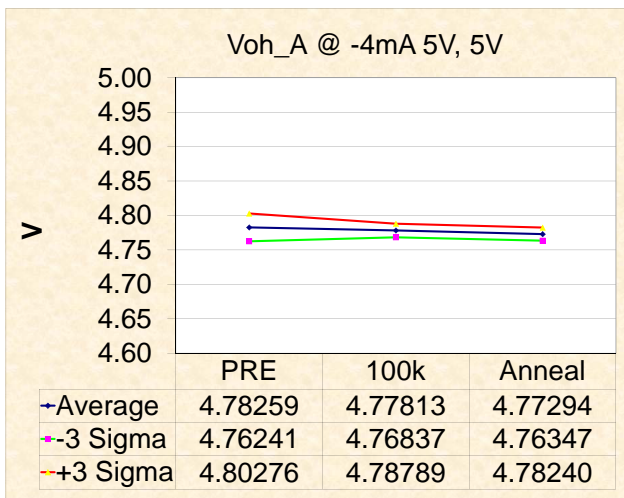


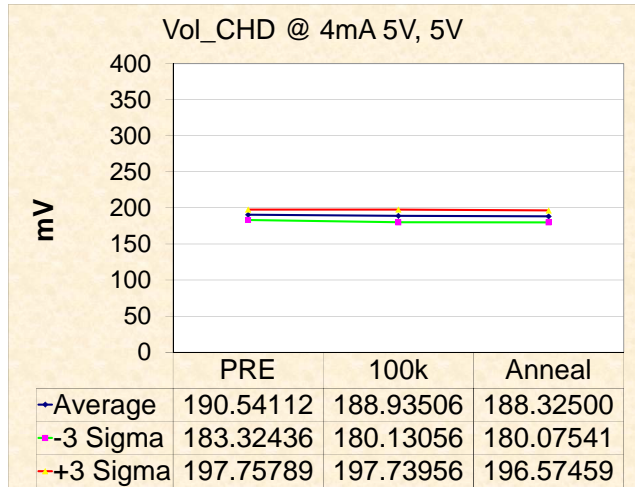
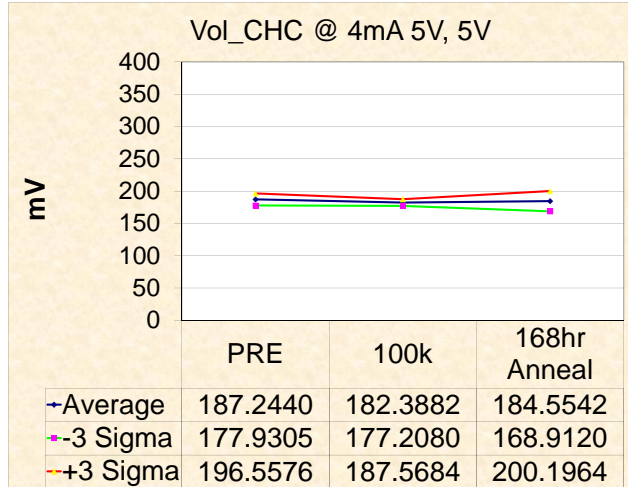
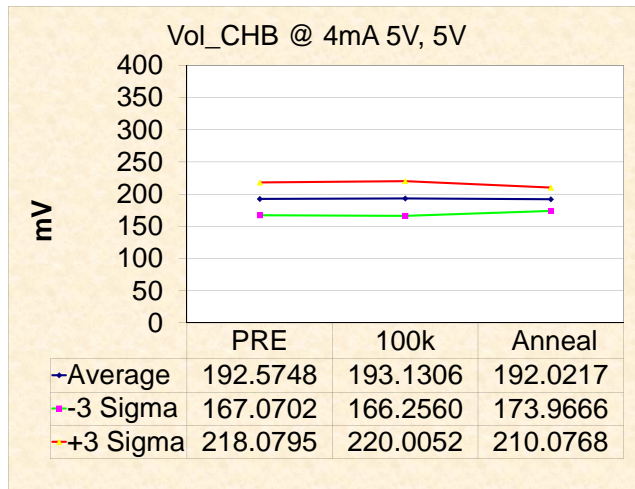
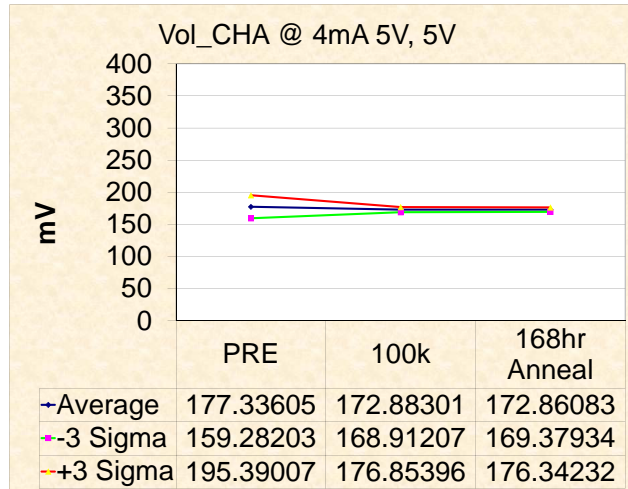
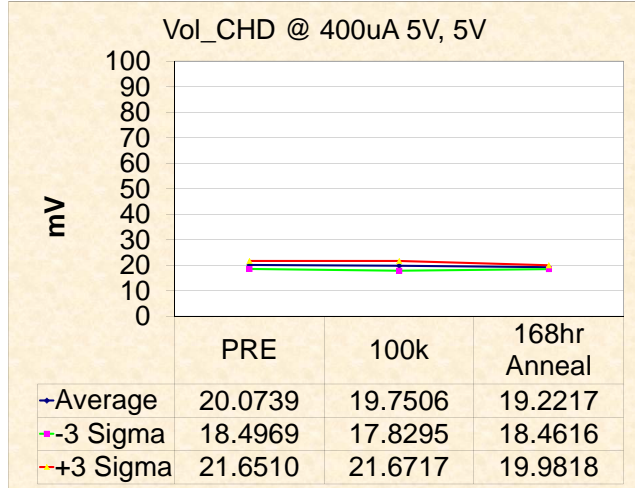
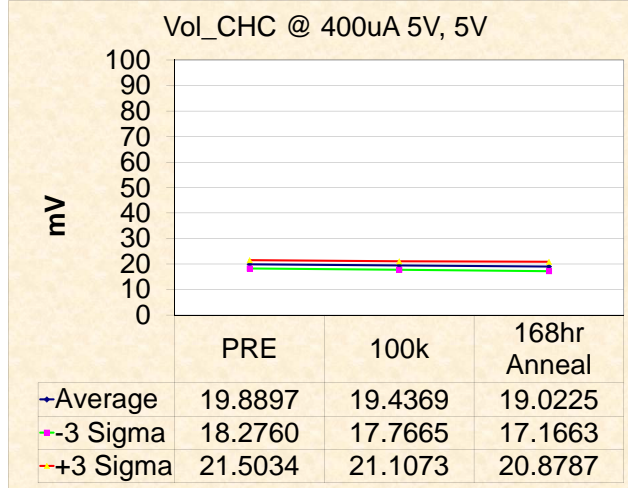
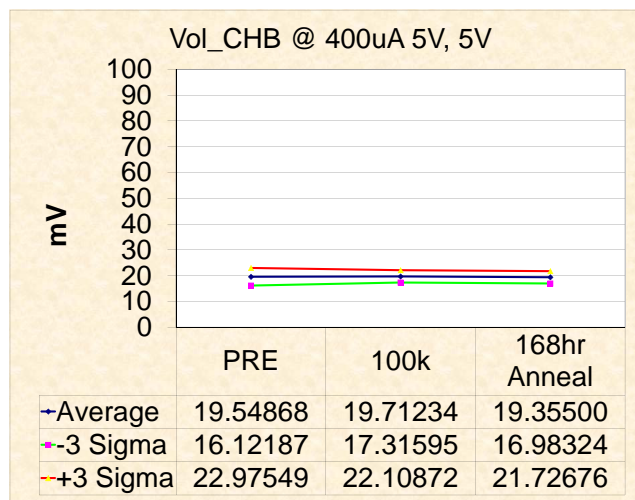
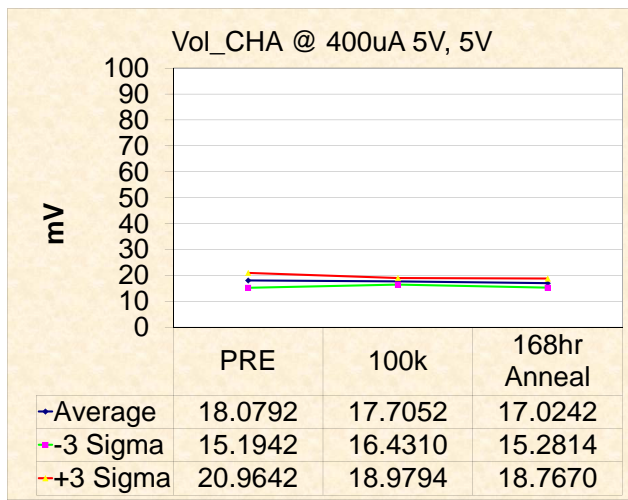
Voh_C @ -20uA 5V, 5V

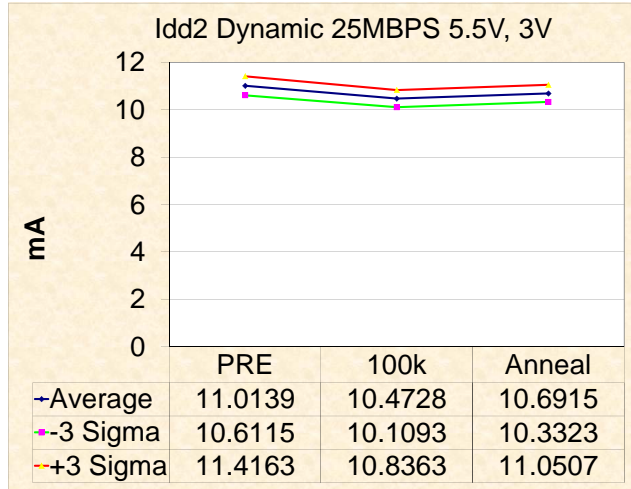
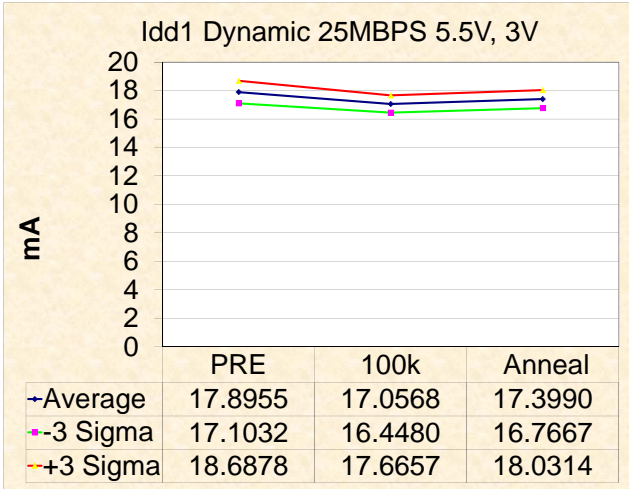
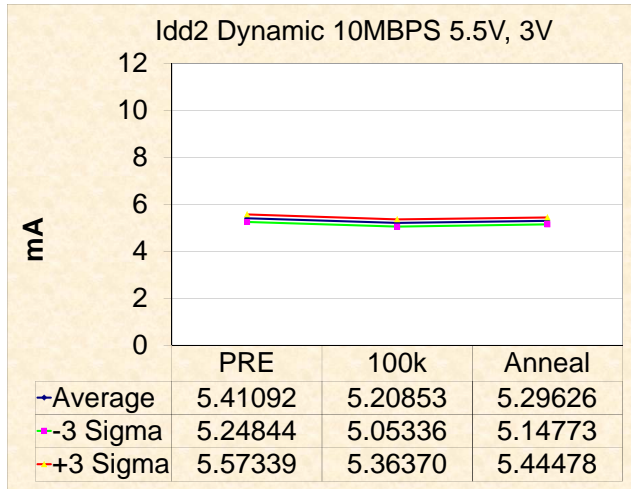
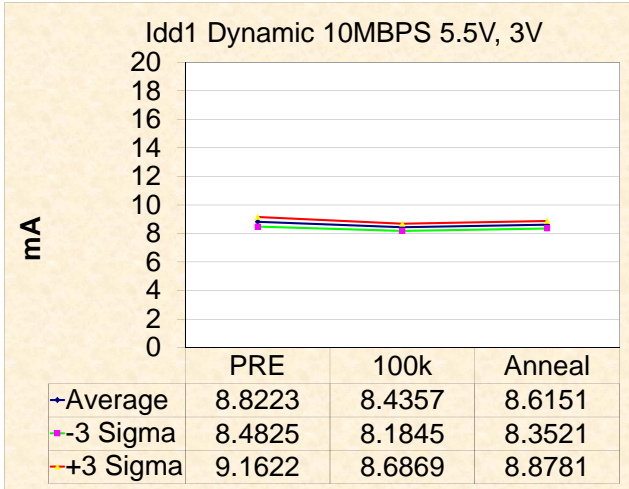
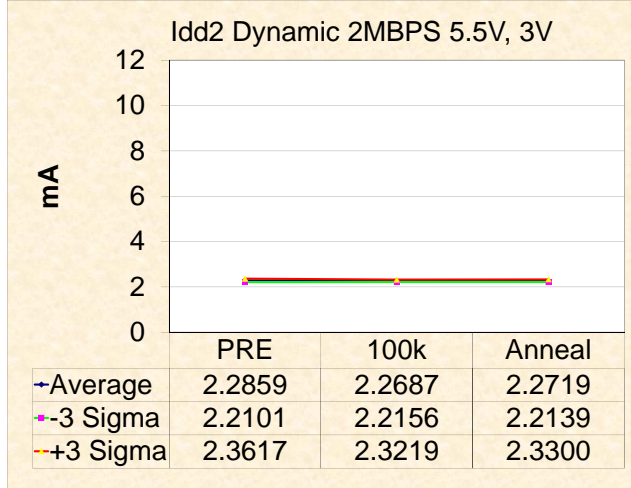
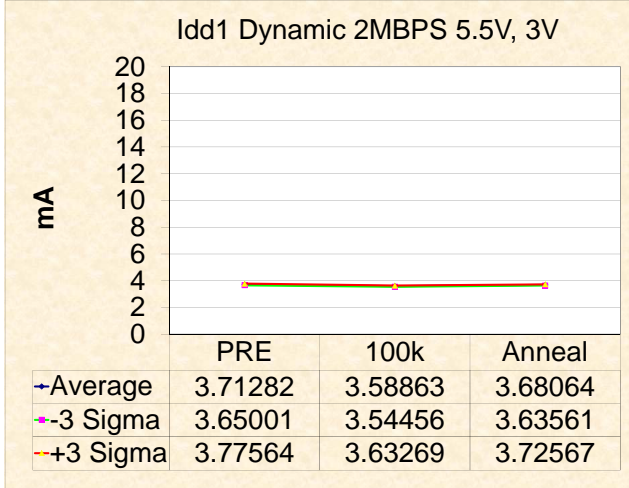
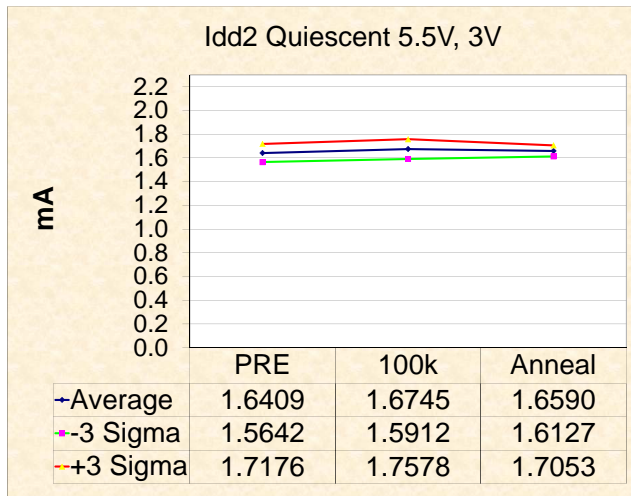
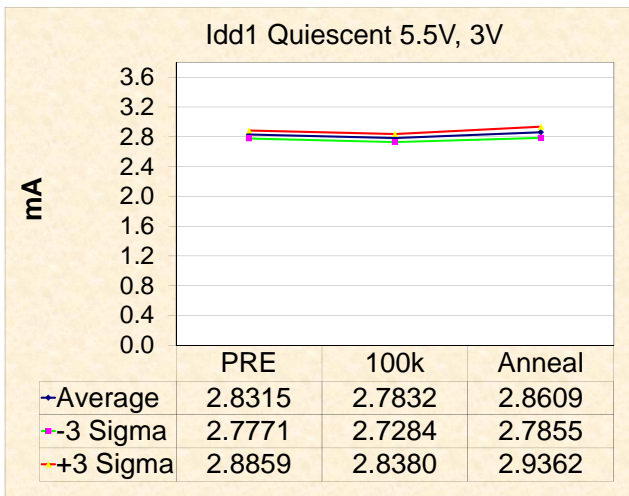


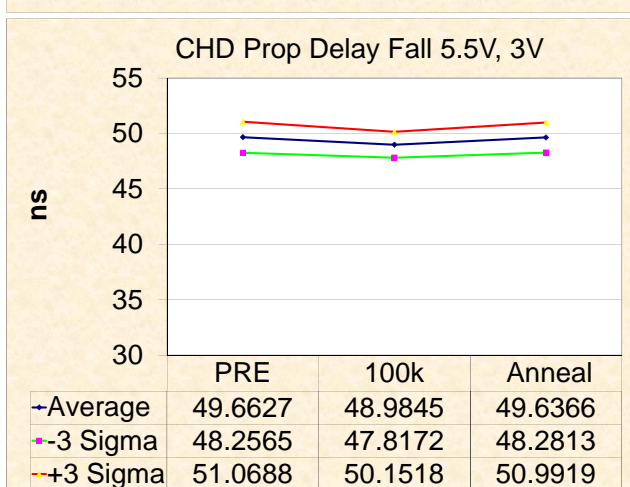
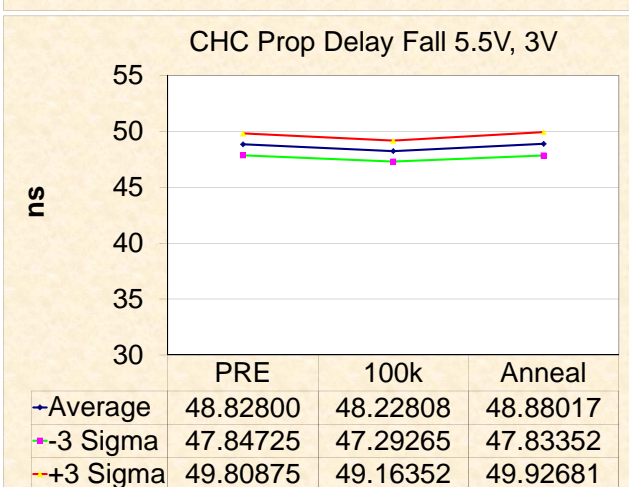
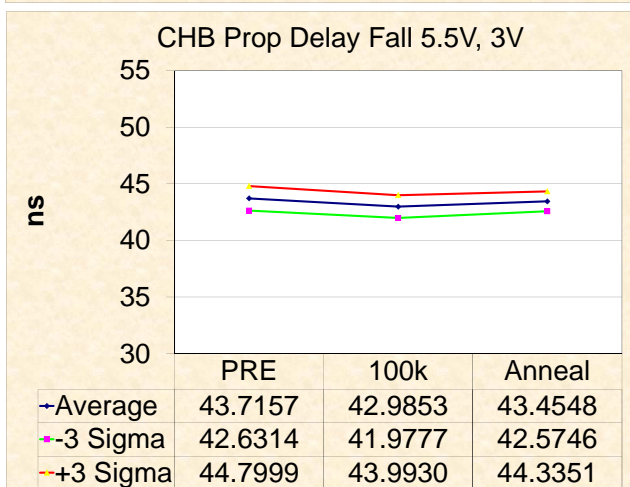
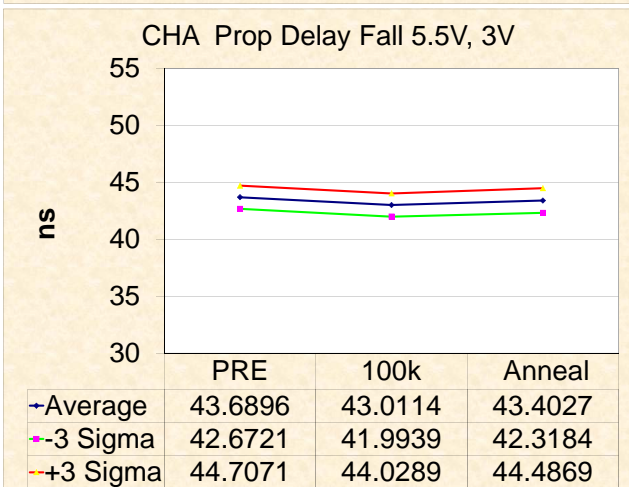
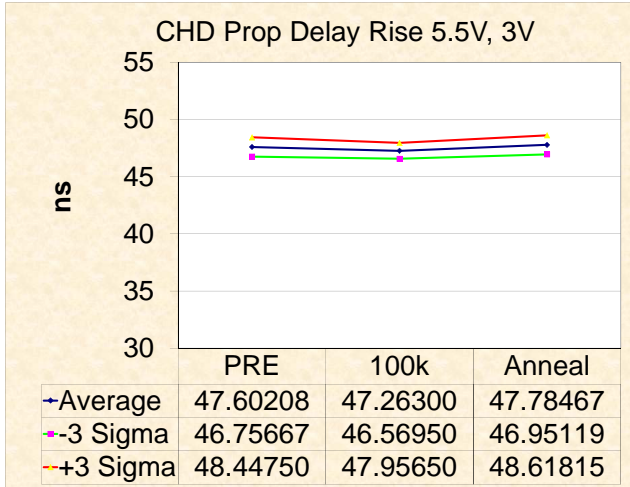
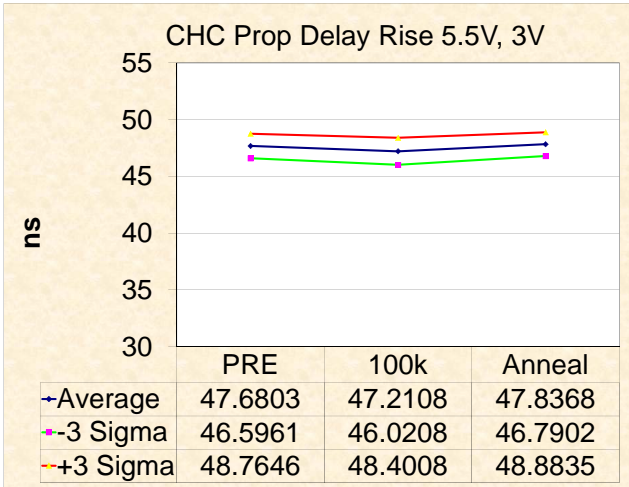
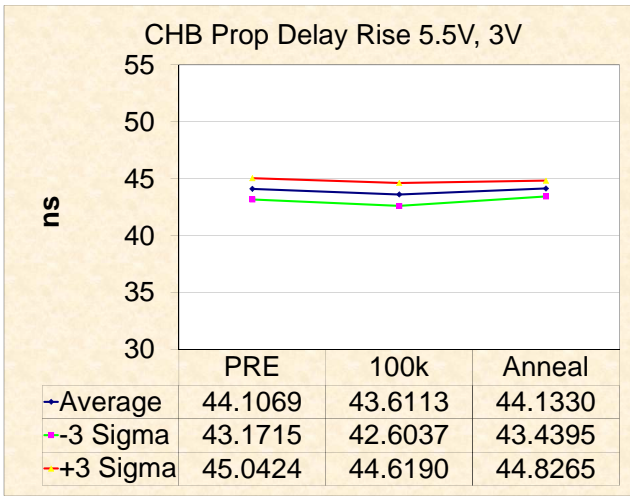
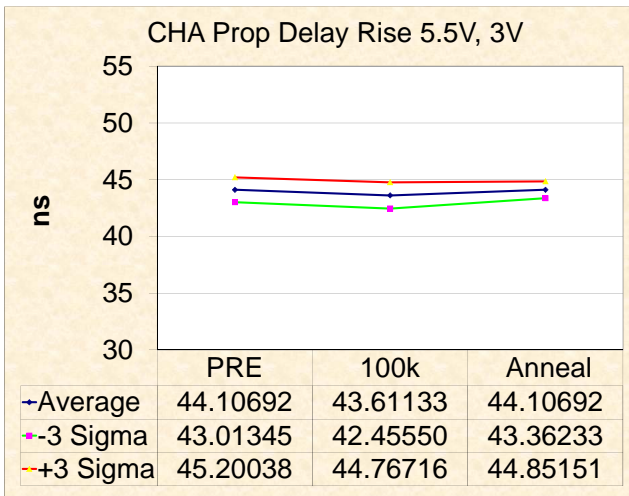
Voh_D @ -20uA 5V, 5V

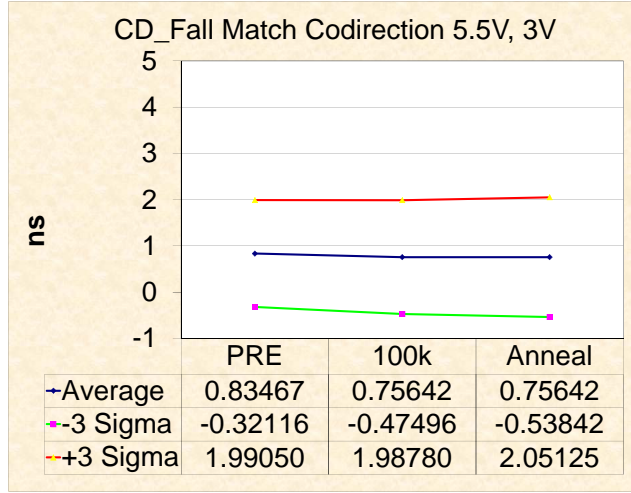
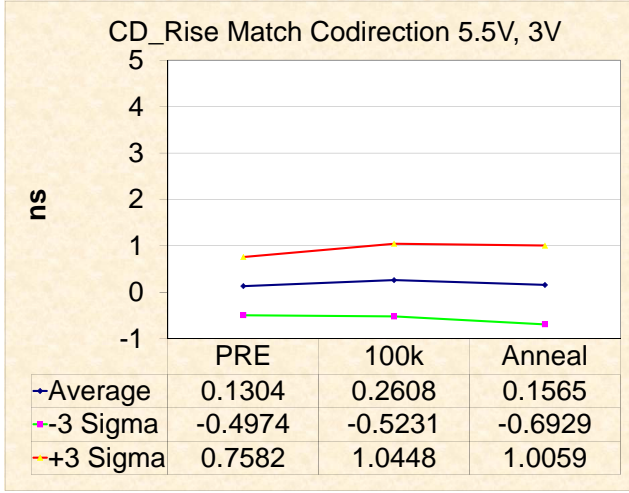
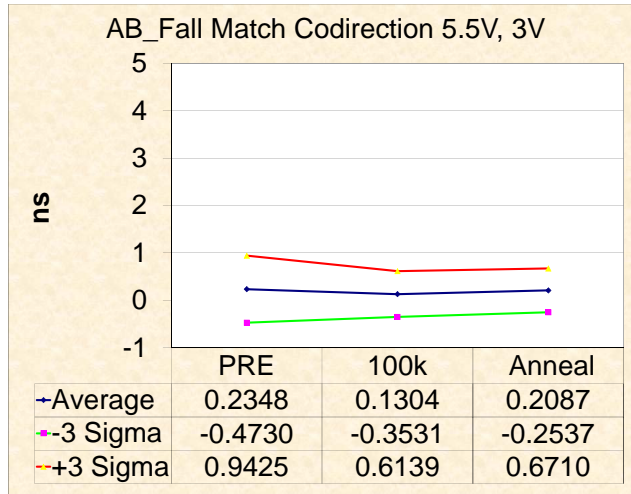
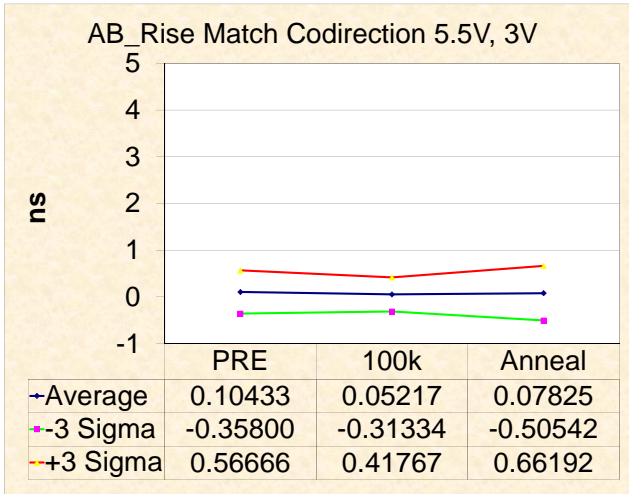
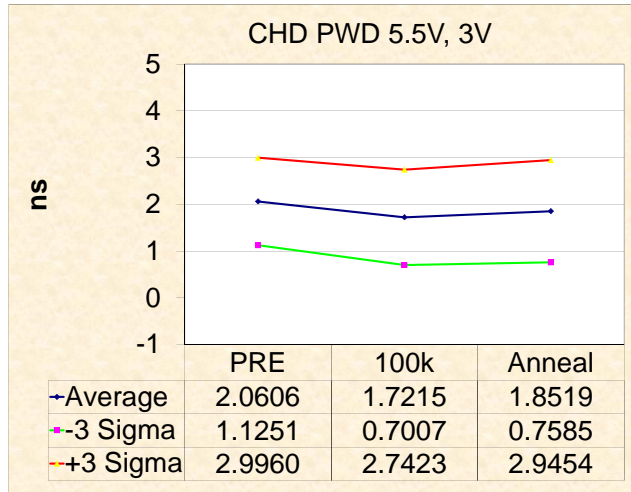
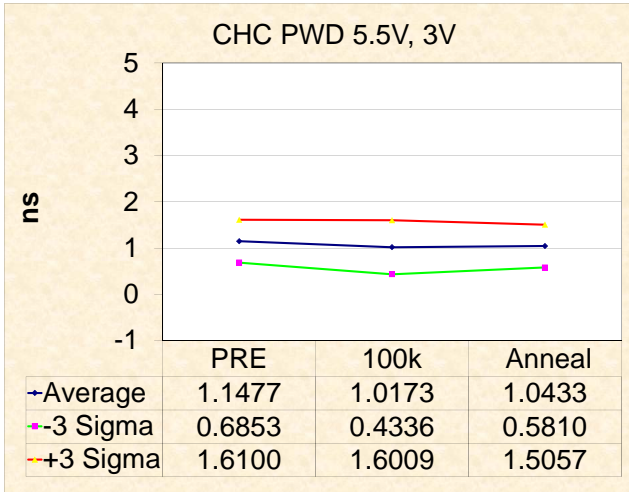
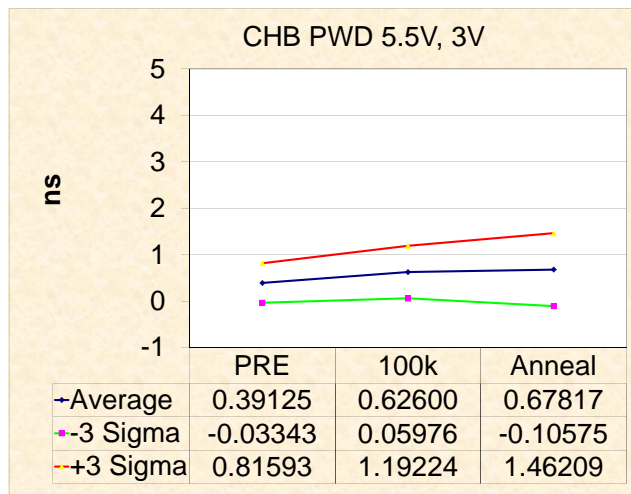
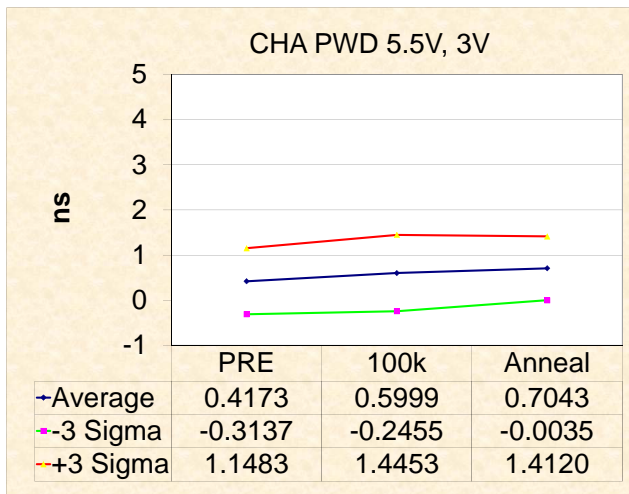




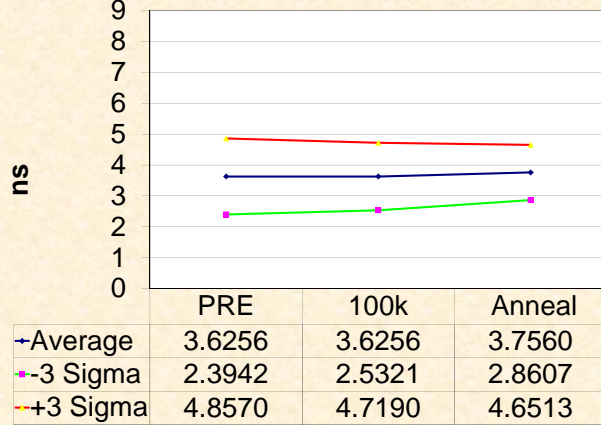




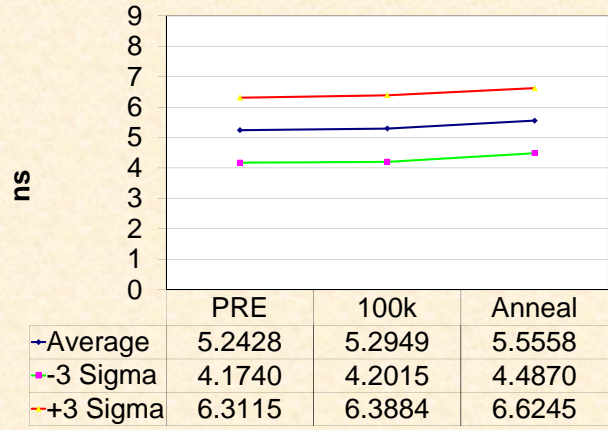




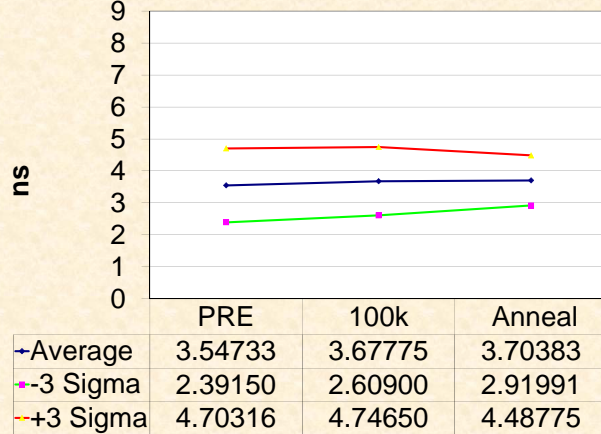
AB_C Rise Match Opposing 5.5V, 3V



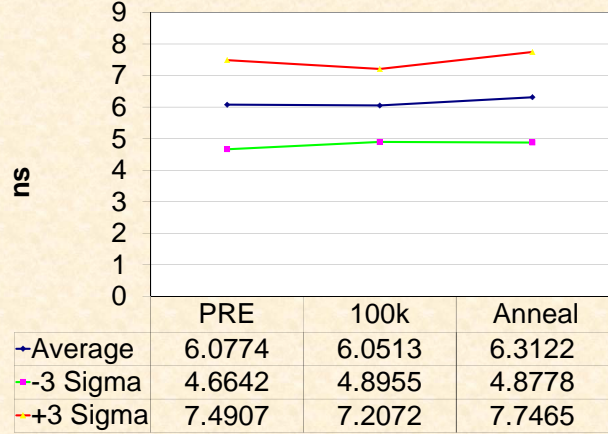
AB_C Fall Match Opposing 5.5V, 3V



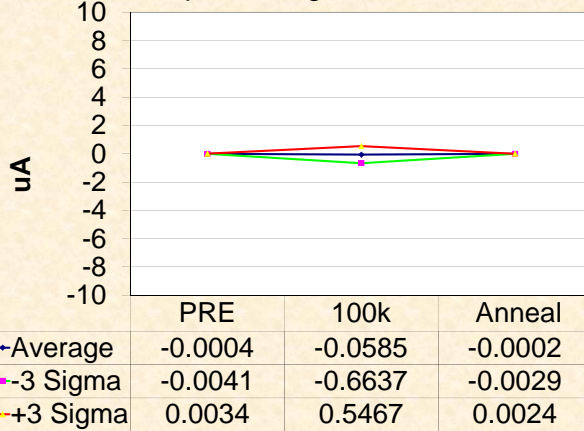
AB_D Rise Match Opposing 5.5V, 3V



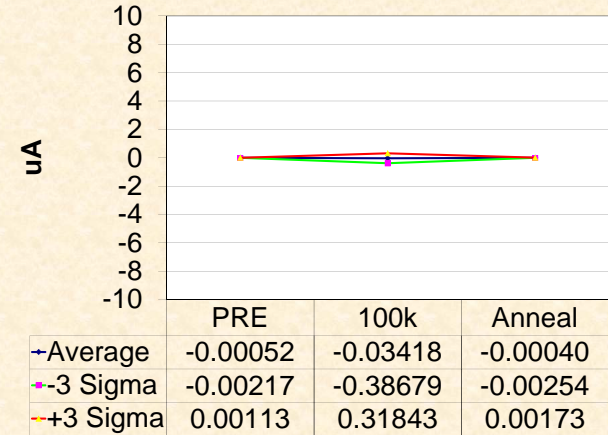
AB_D Fall Match Opposing 5.5V, 3V



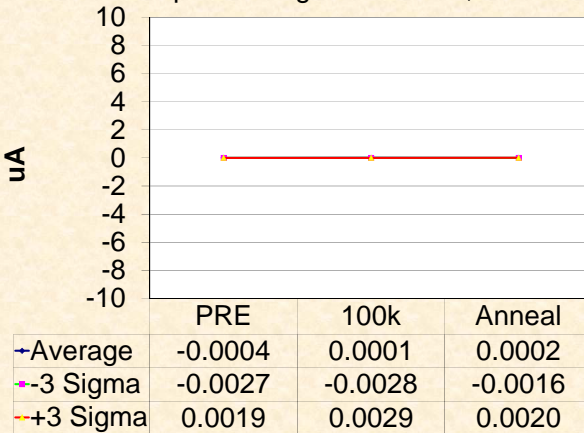
ChA Input Leakage @ 0V 5.5V, 3V



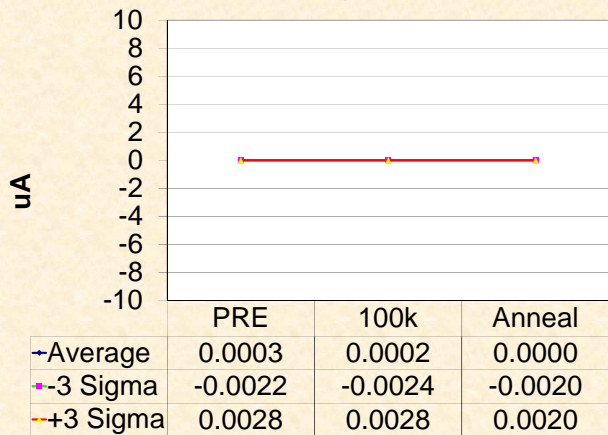
ChB Input Leakage @ 0V 5.5V, 3V

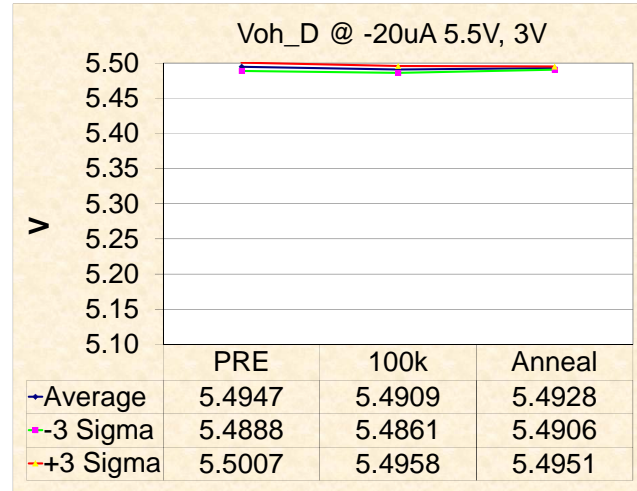
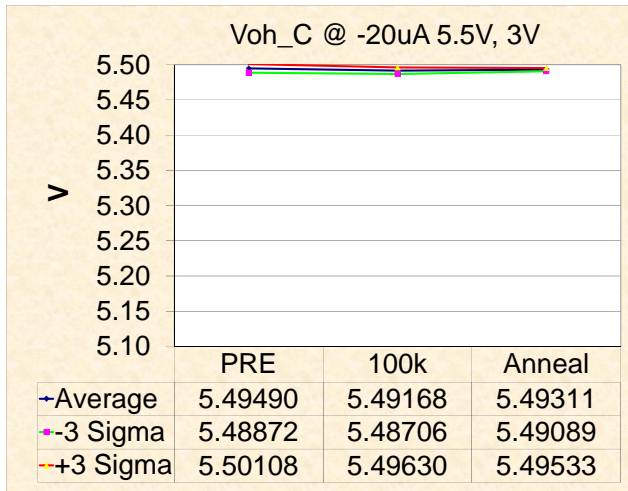
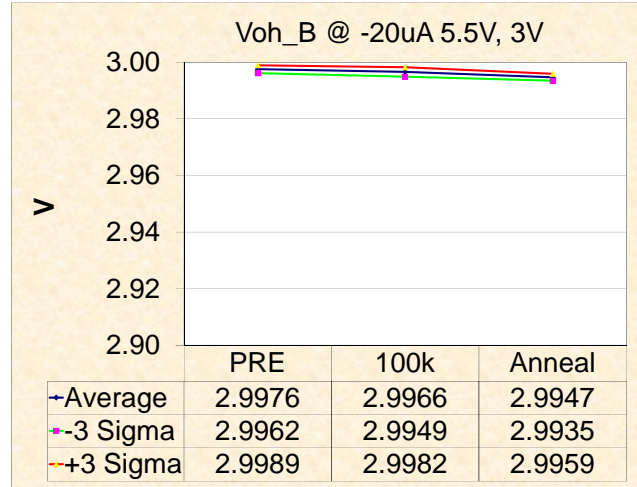
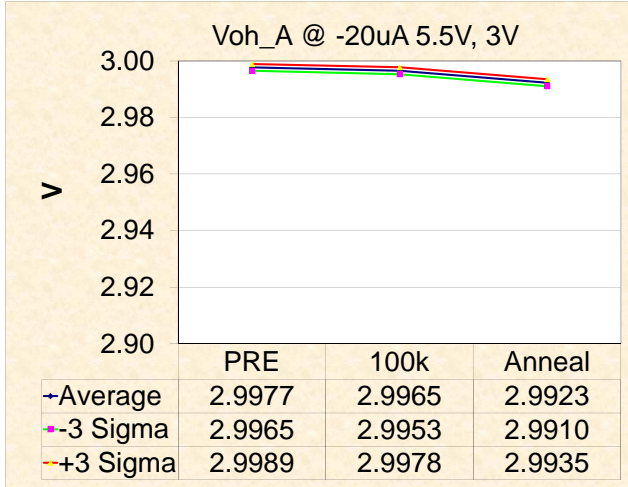
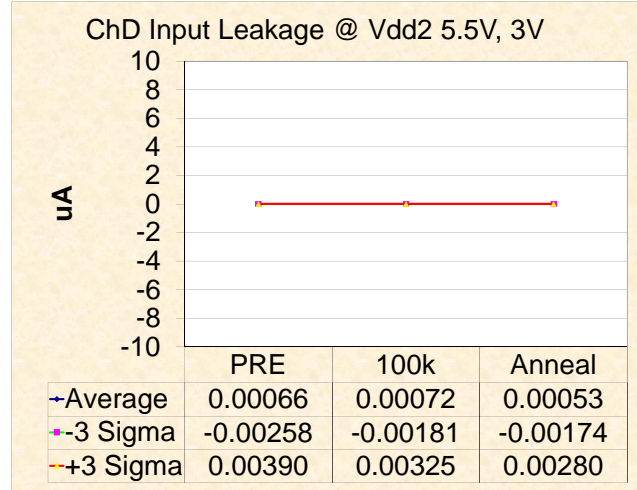
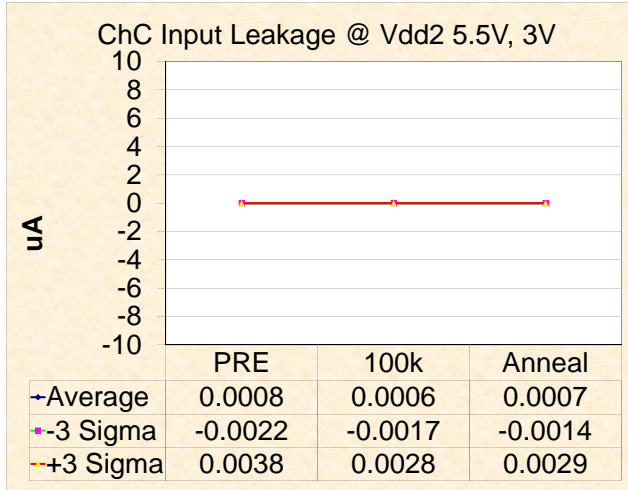
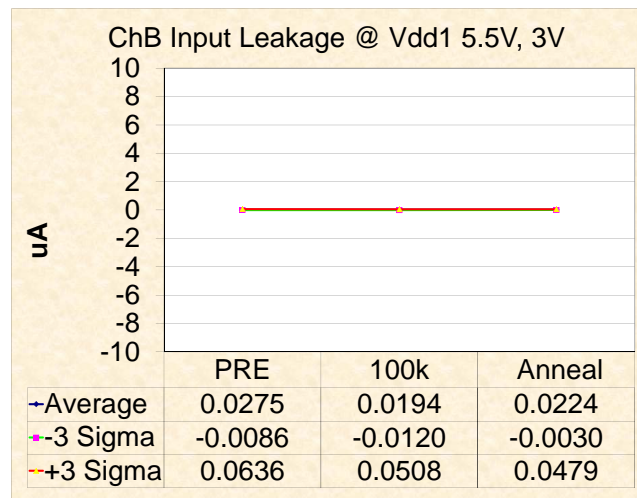
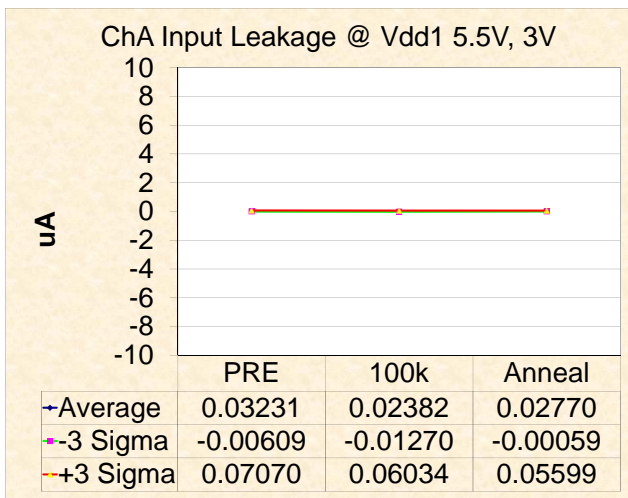


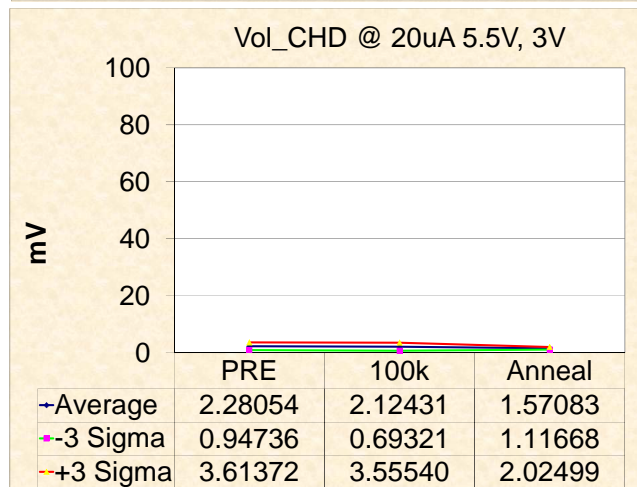
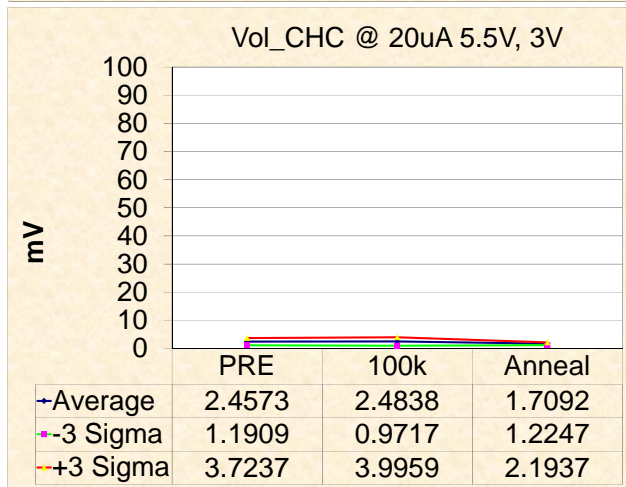
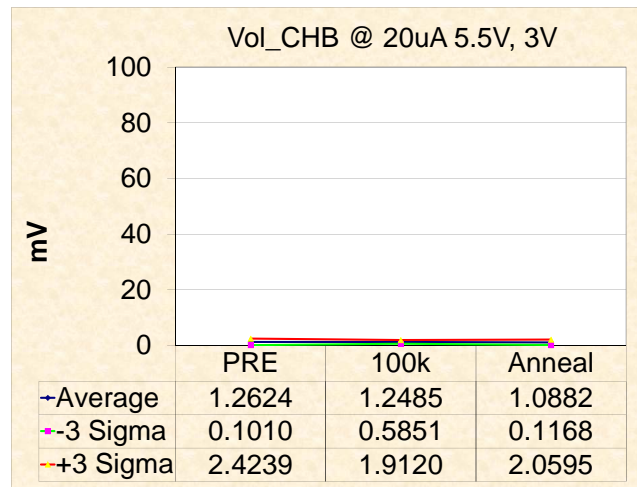
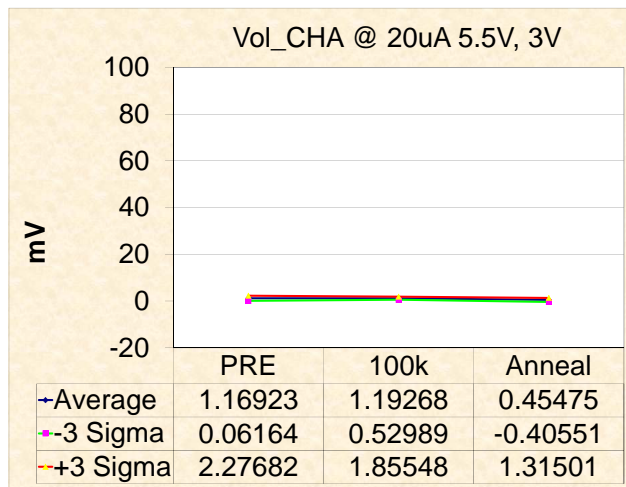
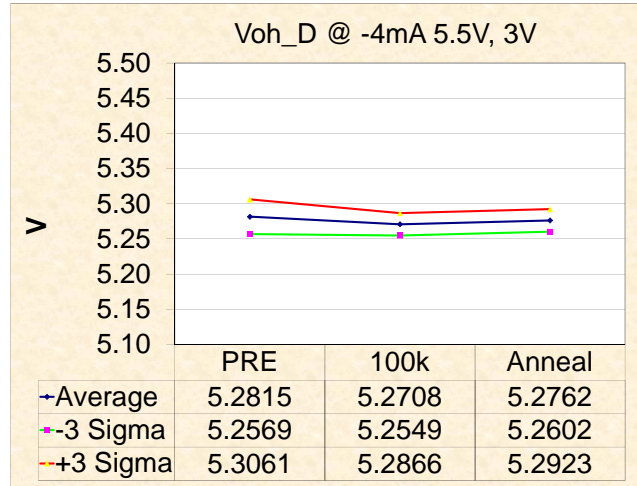
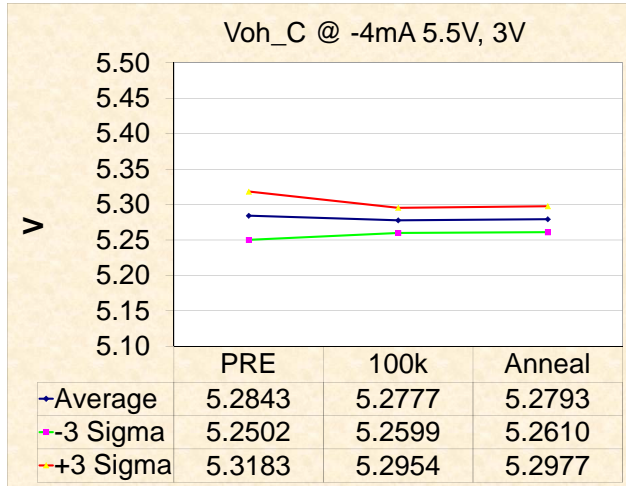
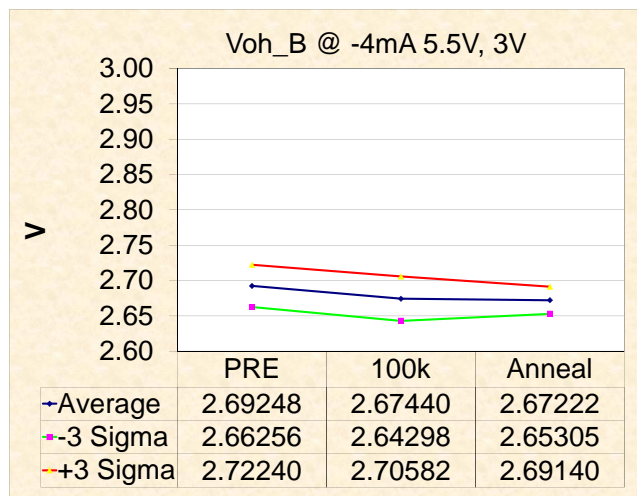
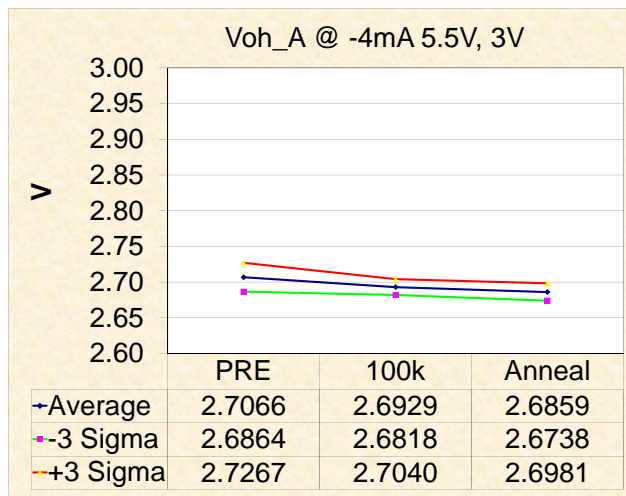
ChC Input Leakage @ 0V 5.5V, 3V

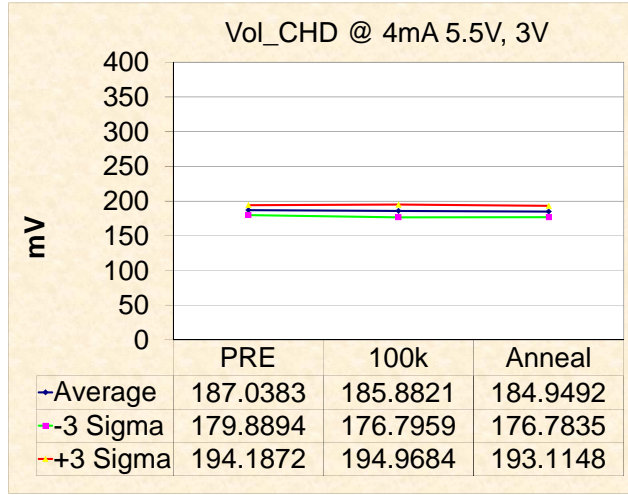
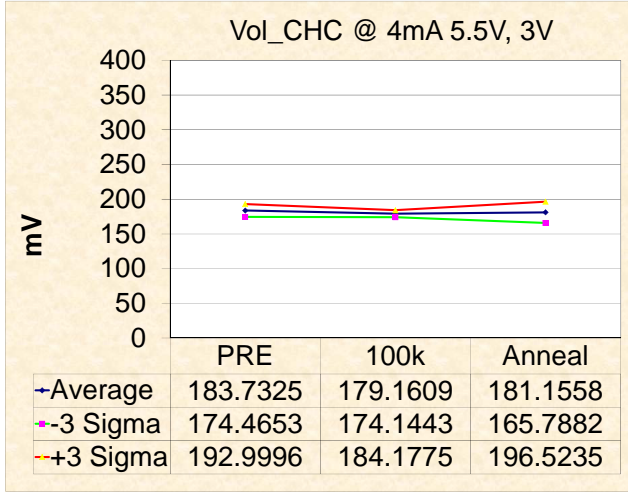
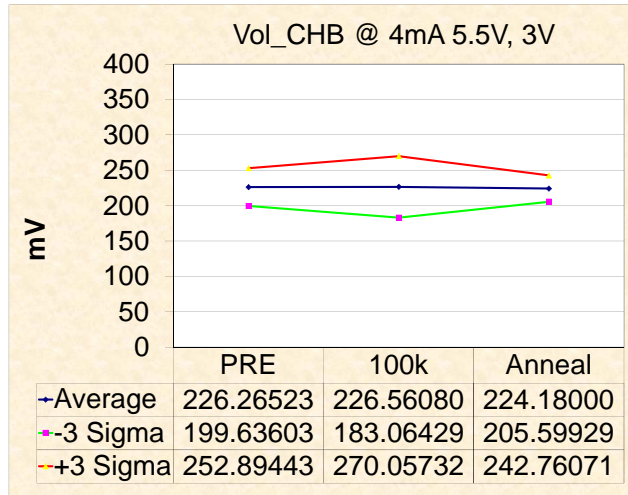
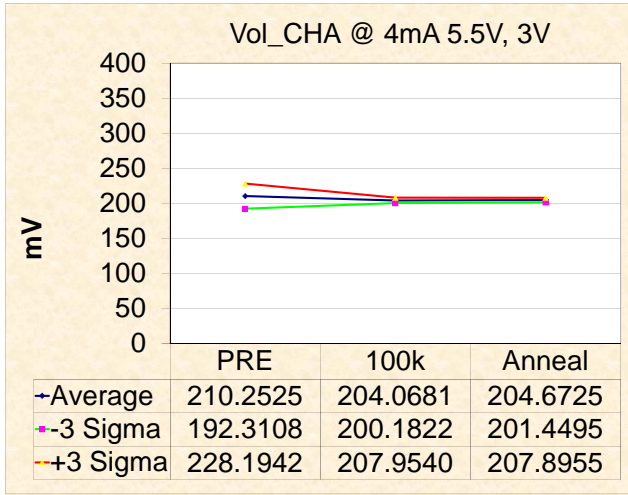
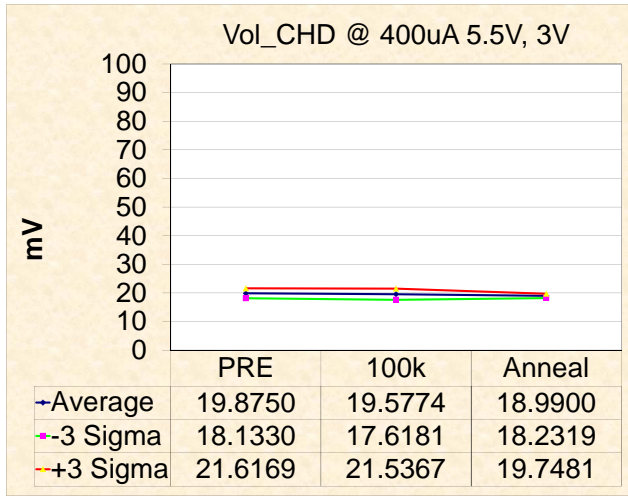
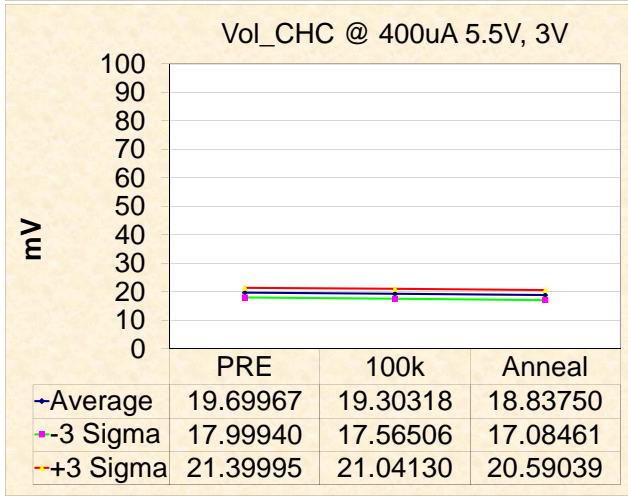
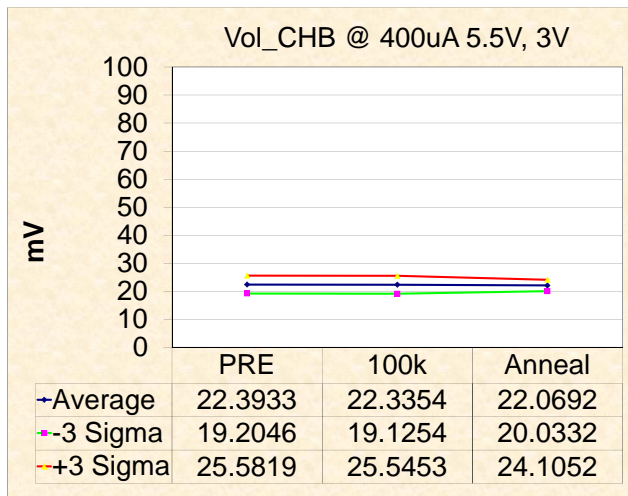
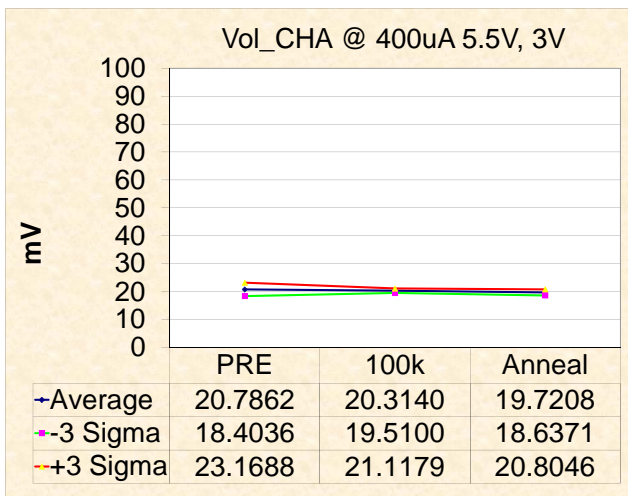


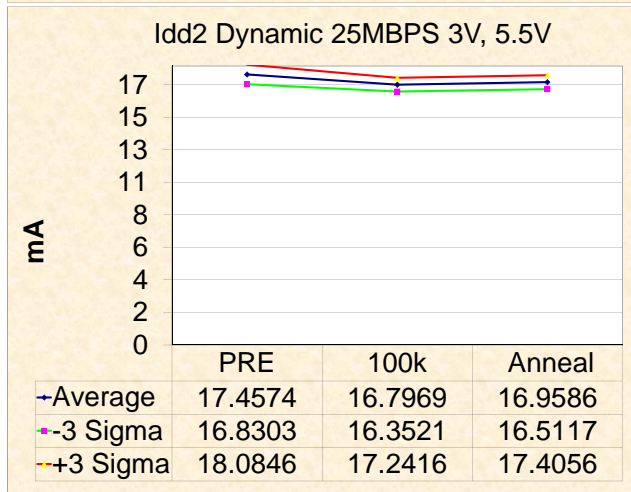
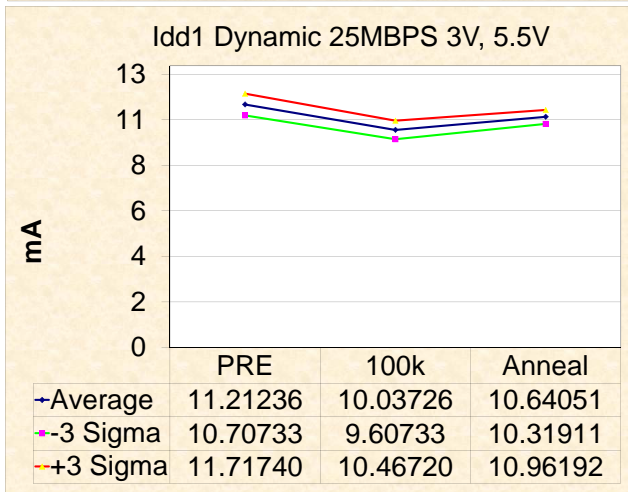
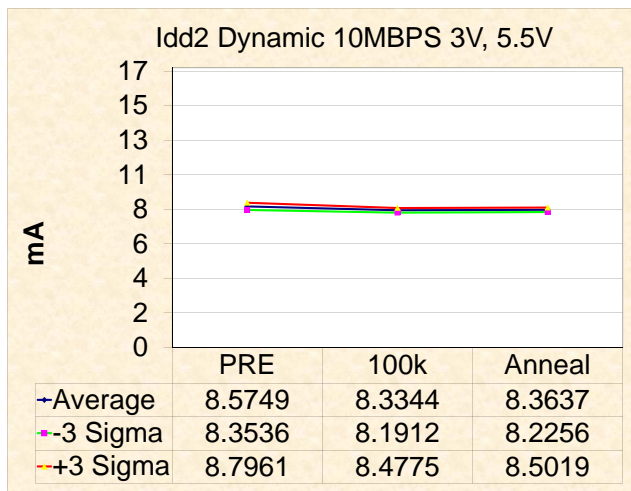
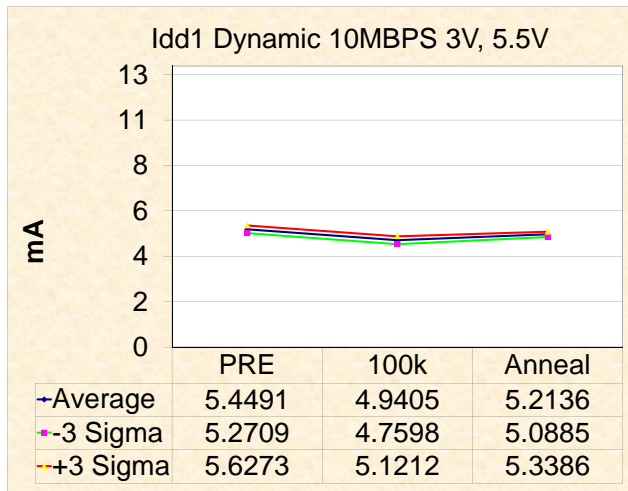
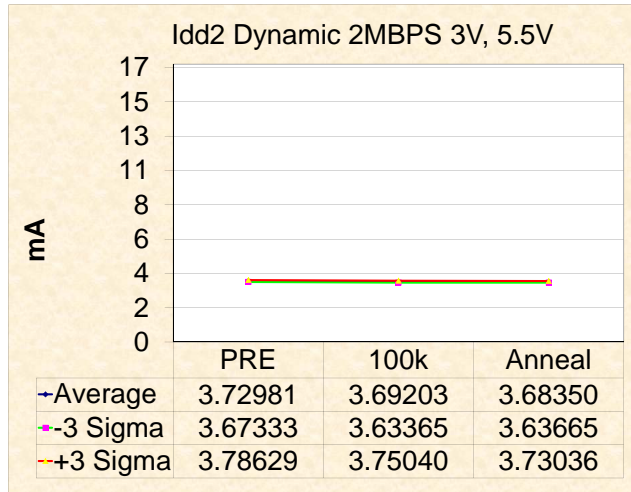
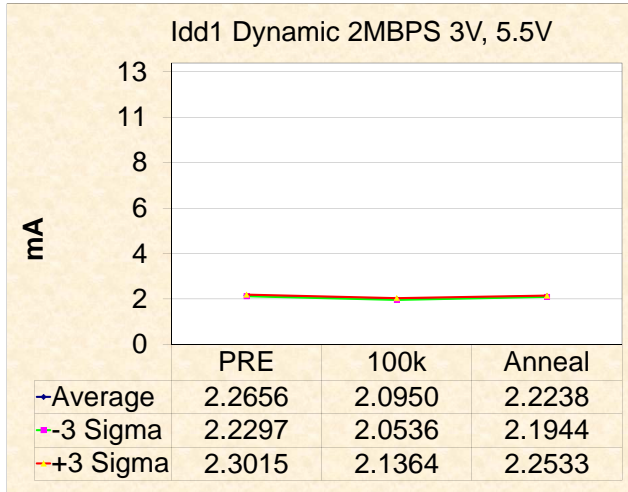
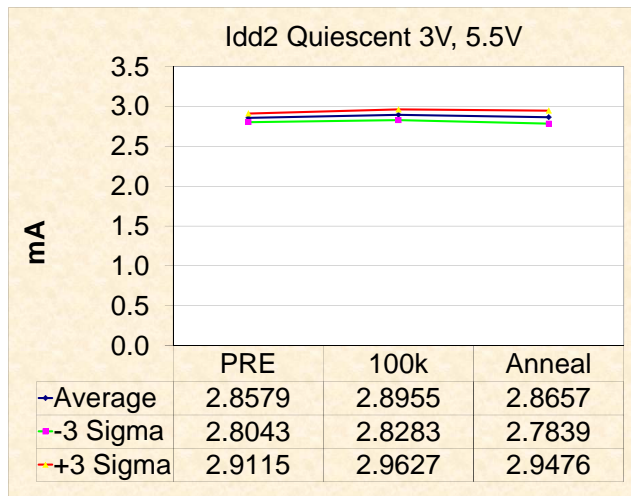
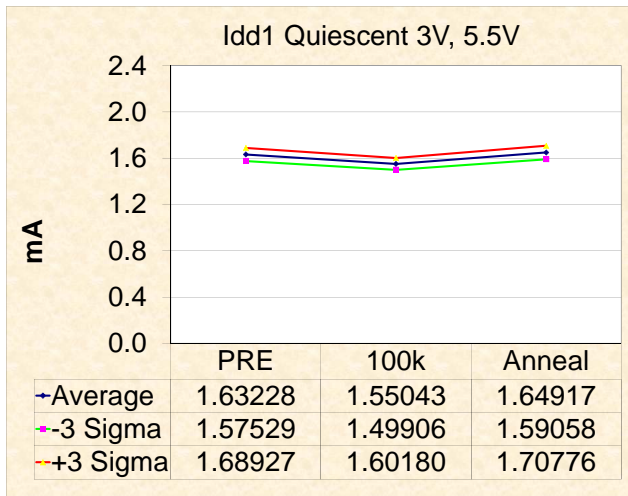
ChD Input Leakage @ 0V 5.5V, 3V

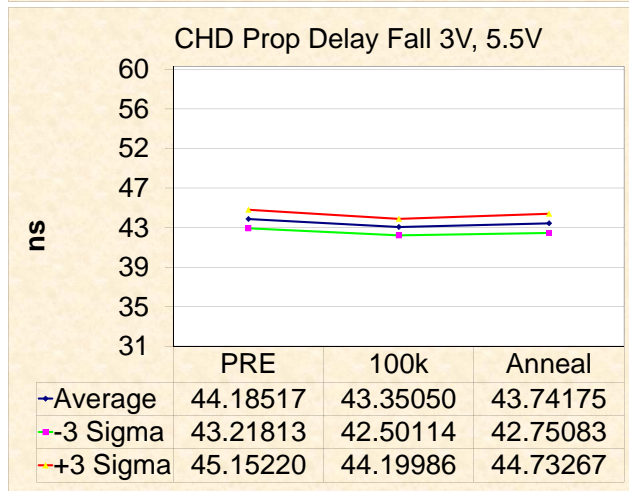
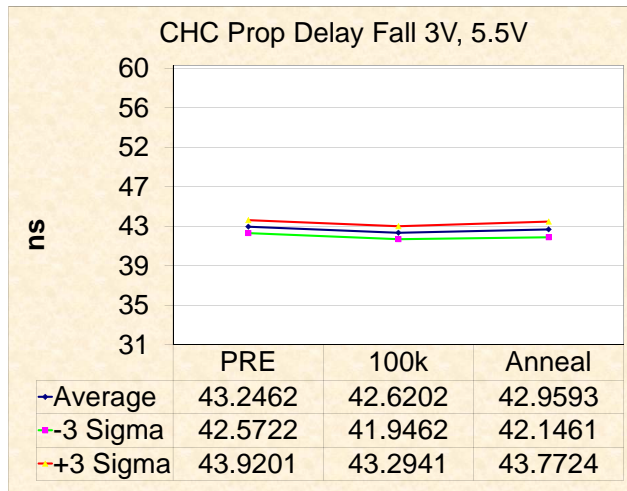
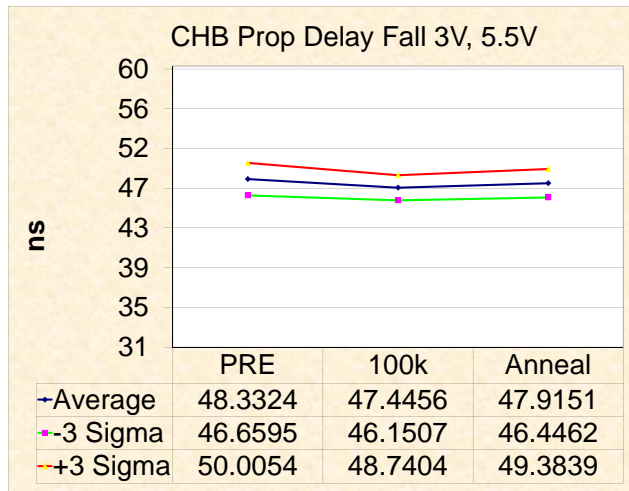
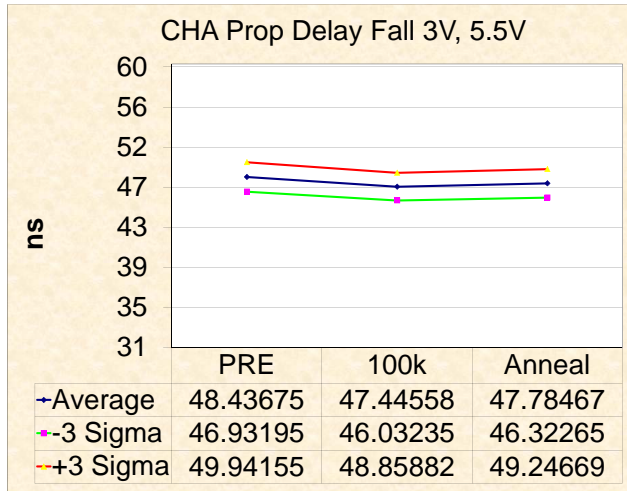
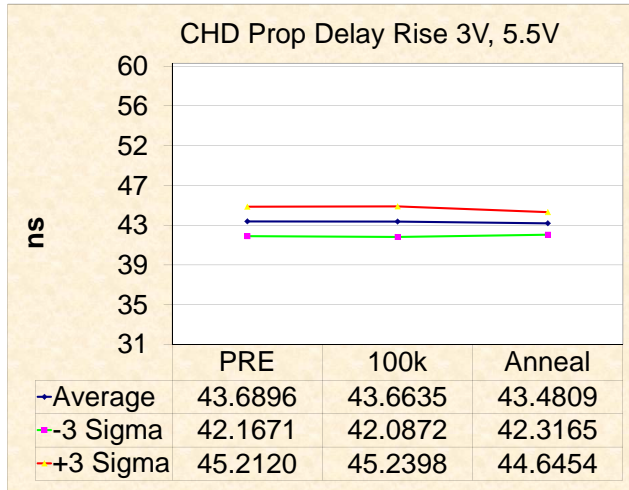
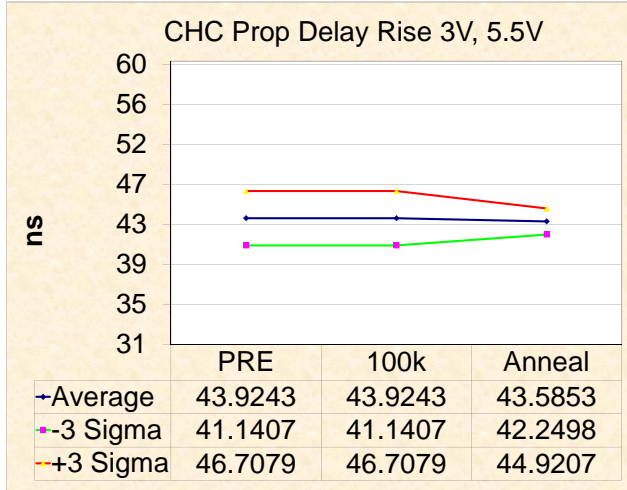
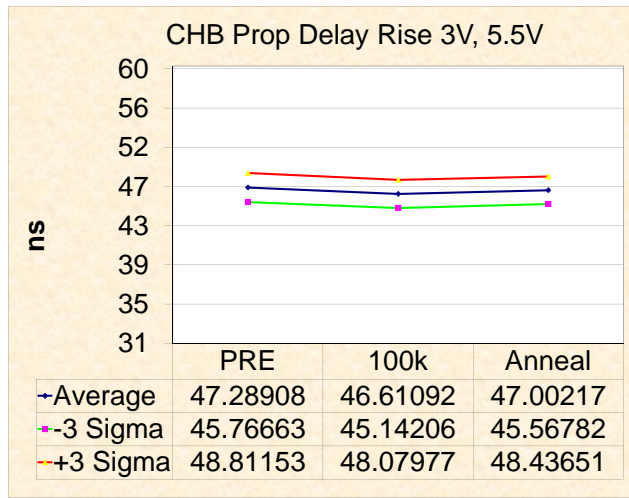
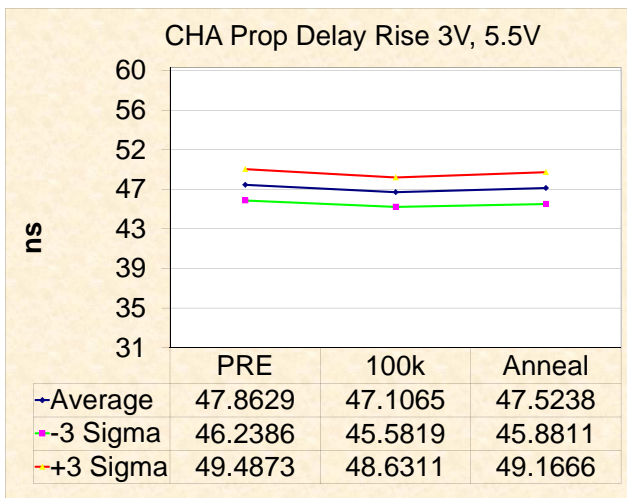


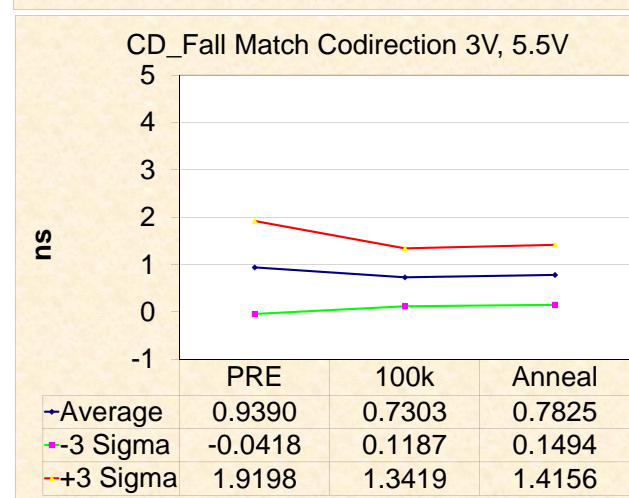
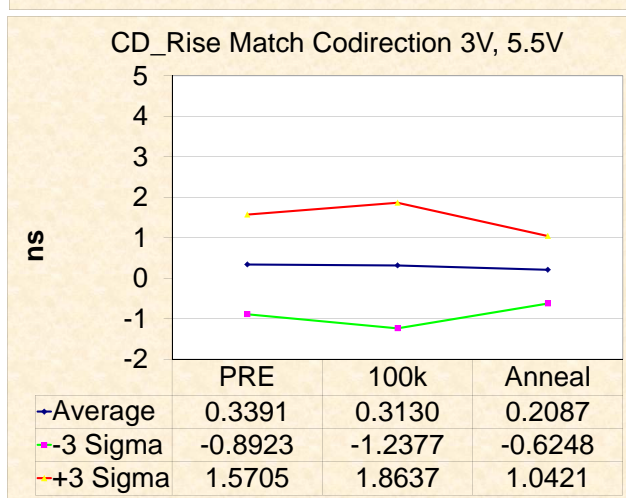
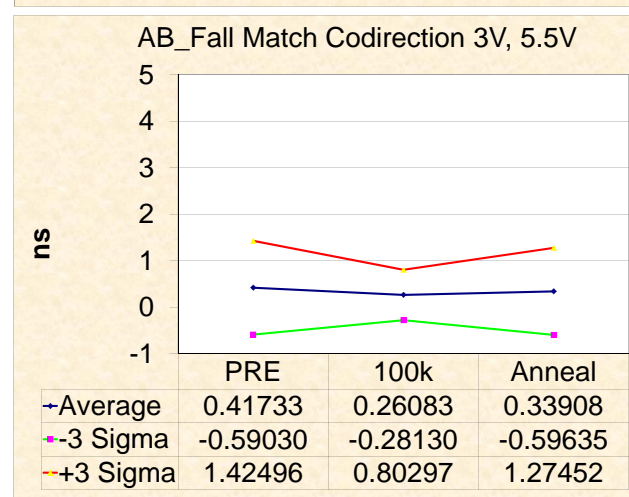
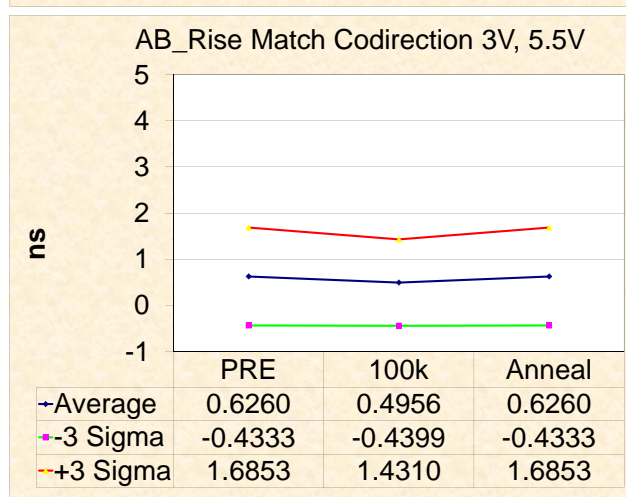
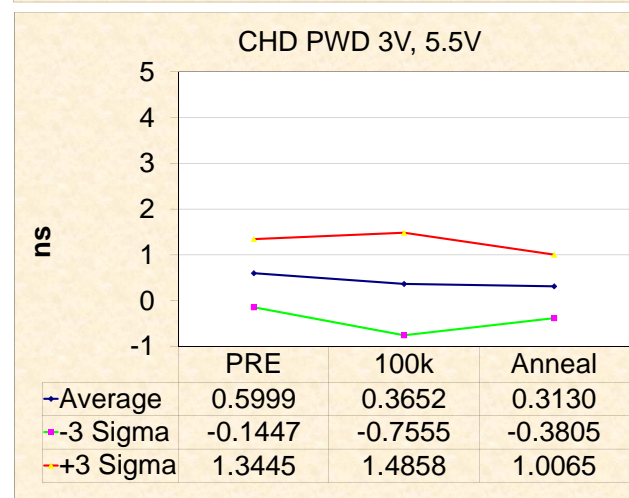
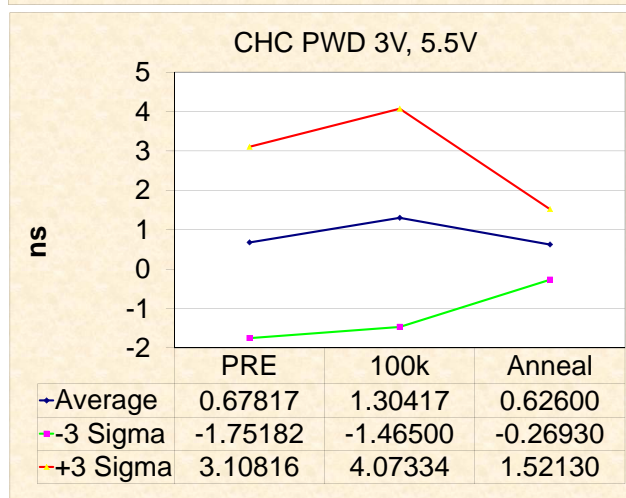
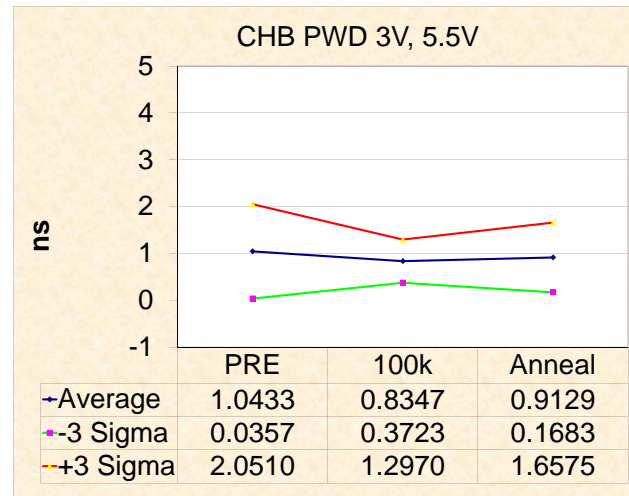
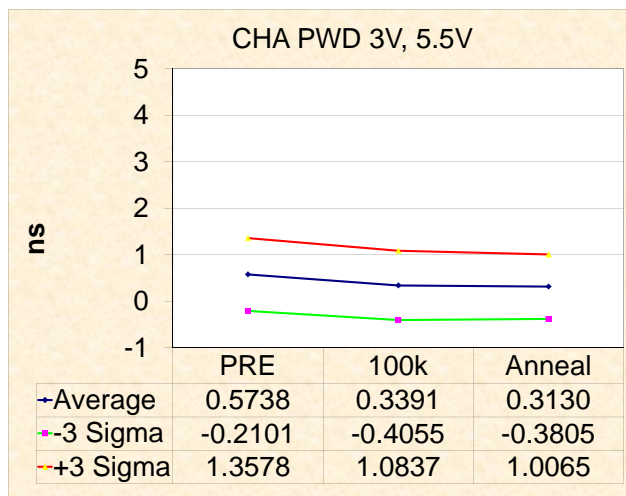




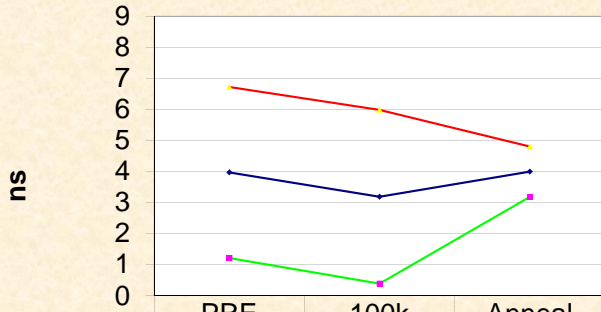






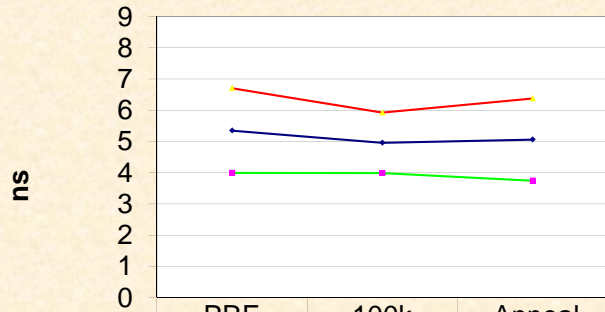


AB_C Rise Match Opposing 3V, 5.5V



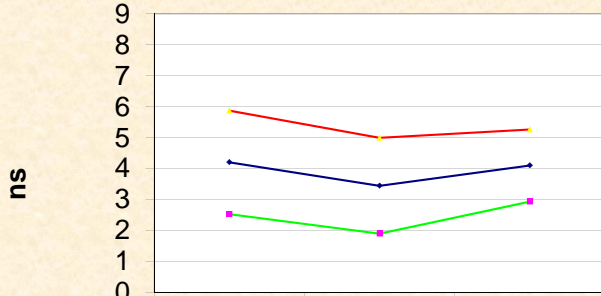
	PRE	100k	Anneal
→Average	3.96467	3.18217	3.99075
←-3 Sigma	1.21001	0.38420	3.17755
→+3 Sigma	6.71933	5.98013	4.80395

AB_C Fall Match Opposing 3V, 5.5V



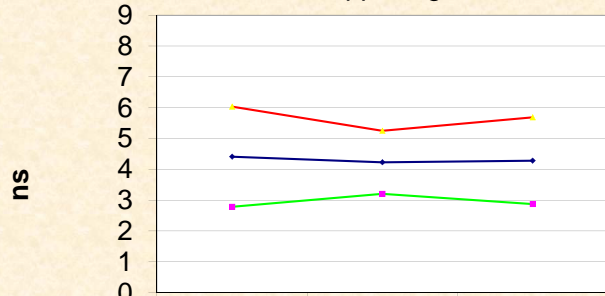
	PRE	100k	Anneal
→Average	5.3471	4.9558	5.0602
←-3 Sigma	3.9918	3.9888	3.7423
→+3 Sigma	6.7024	5.9229	6.3780

AB_D Rise Match Opposing 3V, 5.5V



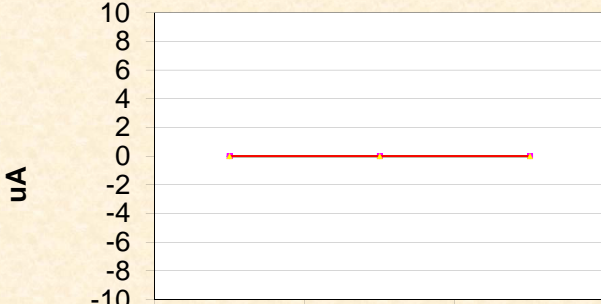
	PRE	100k	Anneal
→Average	4.1994	3.4430	4.0951
←-3 Sigma	2.5265	1.8923	2.9306
→+3 Sigma	5.8724	4.9937	5.2595

AB_D Fall Match Opposing 3V, 5.5V



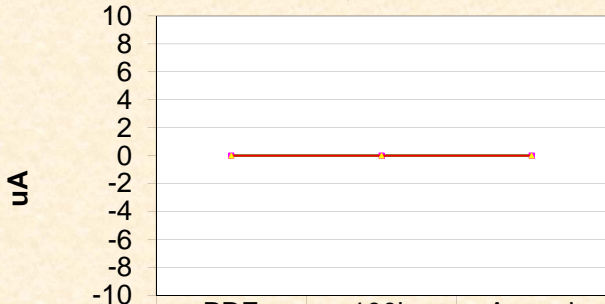
	PRE	100k	Anneal
→Average	4.40808	4.22550	4.27767
←-3 Sigma	2.78374	3.20470	2.87154
→+3 Sigma	6.03242	5.24630	5.68379

ChA Input Leakage @ 0V 3V, 5.5V



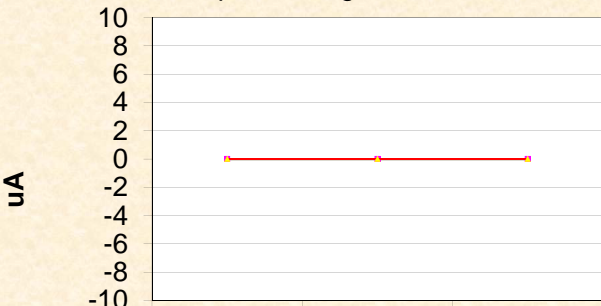
	PRE	100k	Anneal
→Average	-0.0006	-0.0002	0.0001
←-3 Sigma	-0.0031	-0.0027	-0.0017
→+3 Sigma	0.0018	0.0022	0.0020

ChB Input Leakage @ 0V 3V, 5.5V



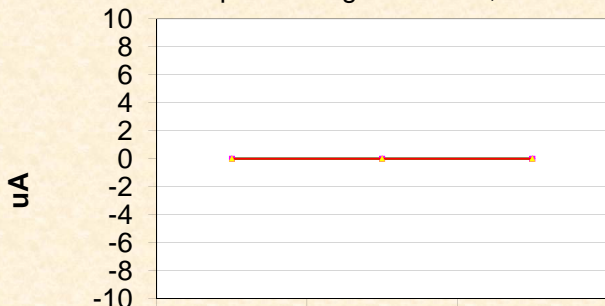
	PRE	100k	Anneal
→Average	-0.0003	-0.0002	0.0001
←-3 Sigma	-0.0029	-0.0017	-0.0022
→+3 Sigma	0.0024	0.0012	0.0025

ChC Input Leakage @ 0V 3V, 5.5V

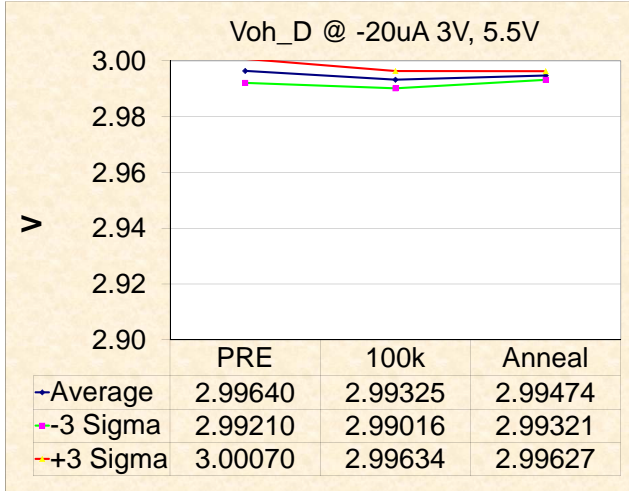
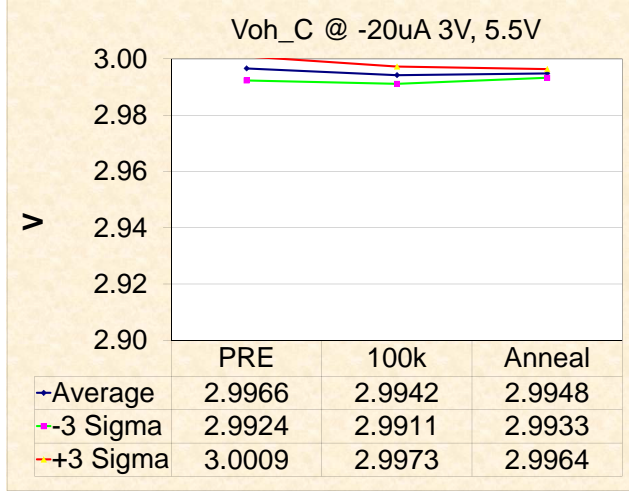
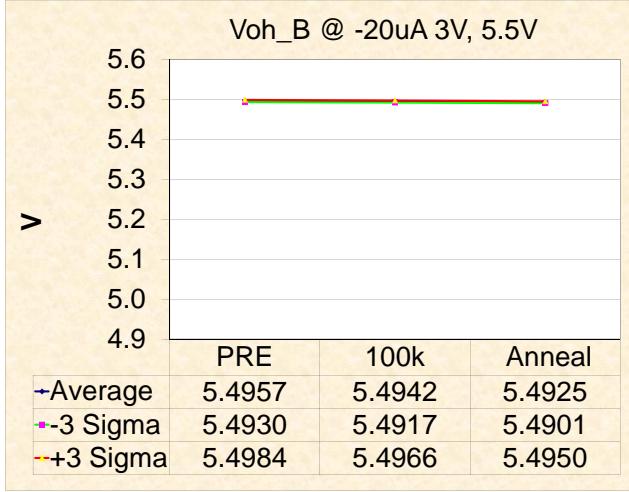
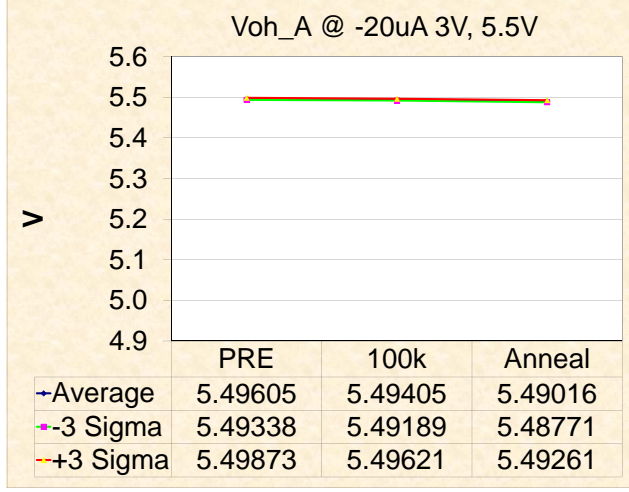
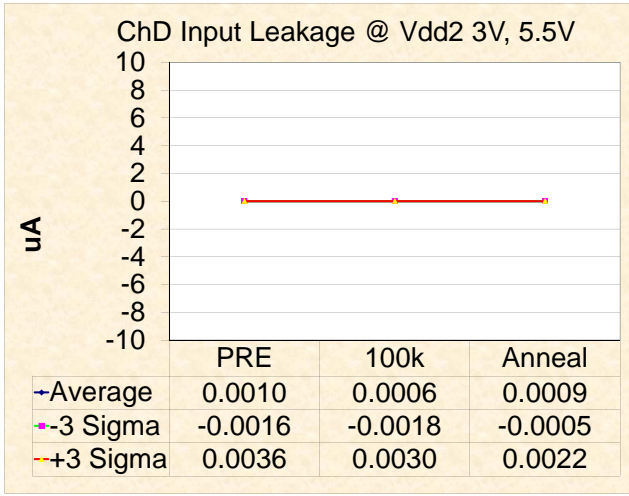
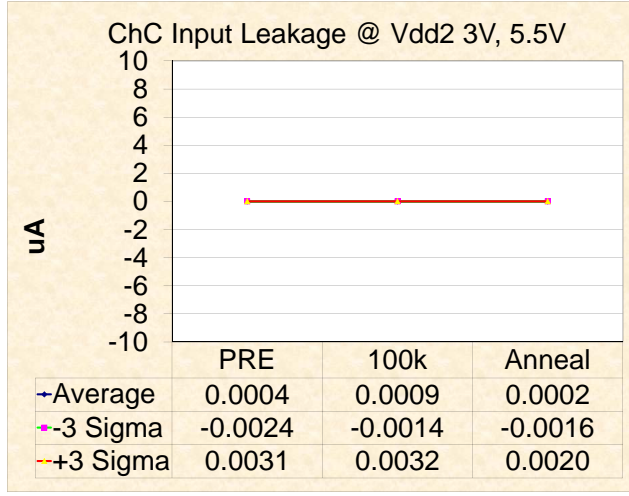
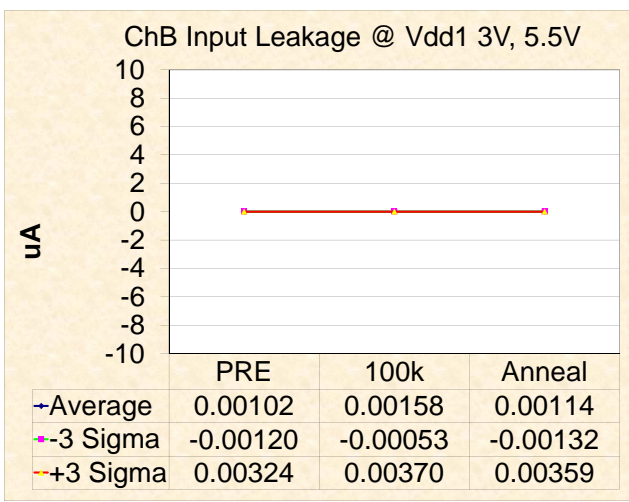
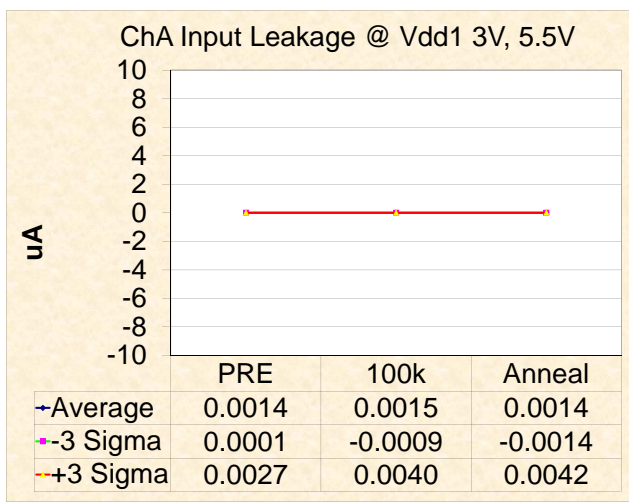


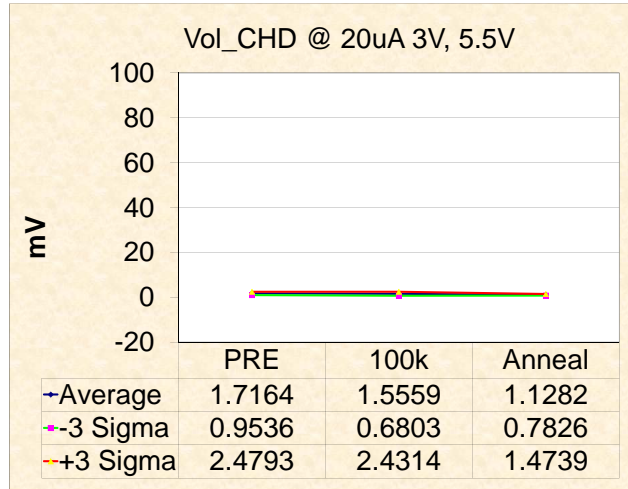
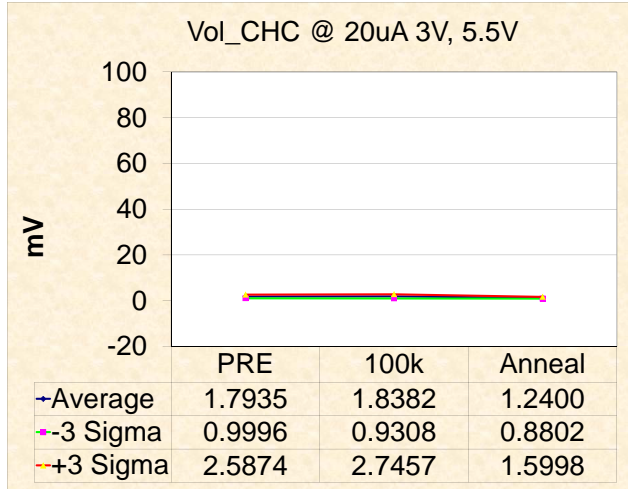
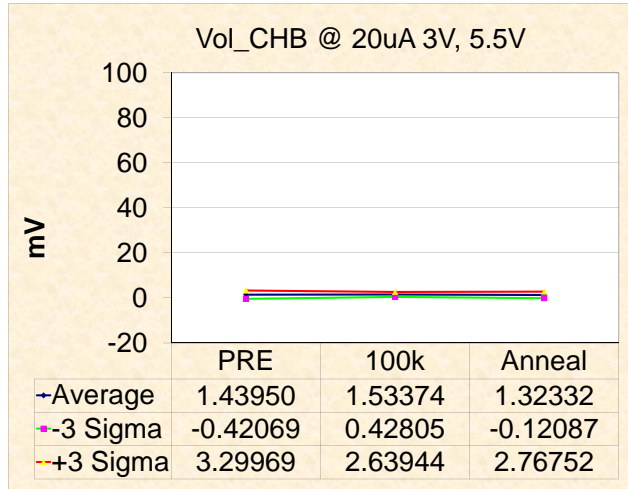
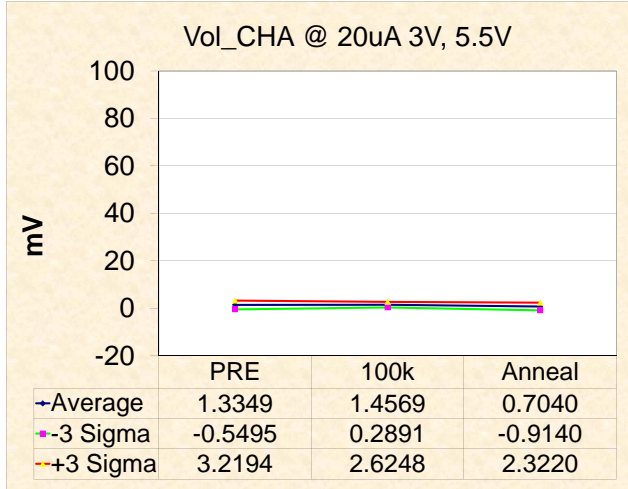
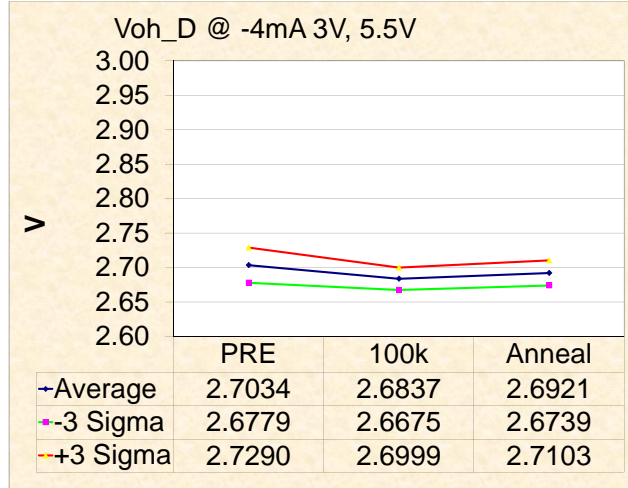
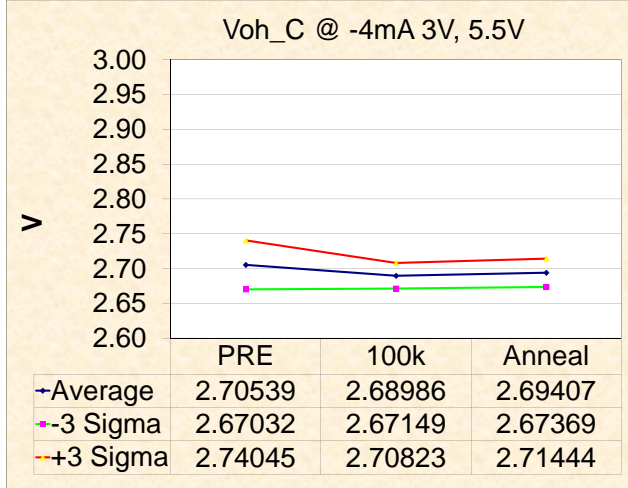
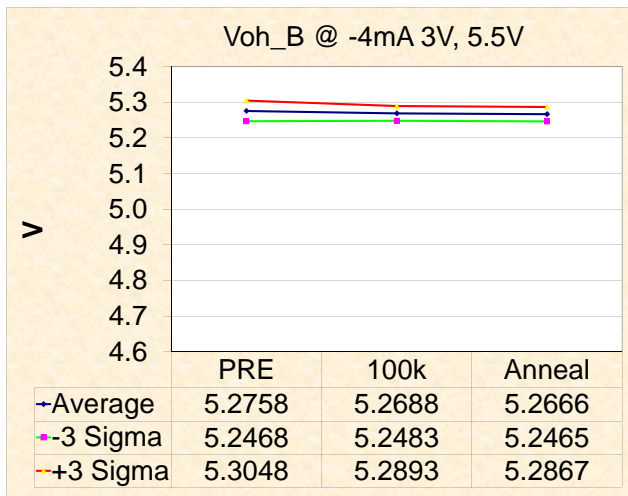
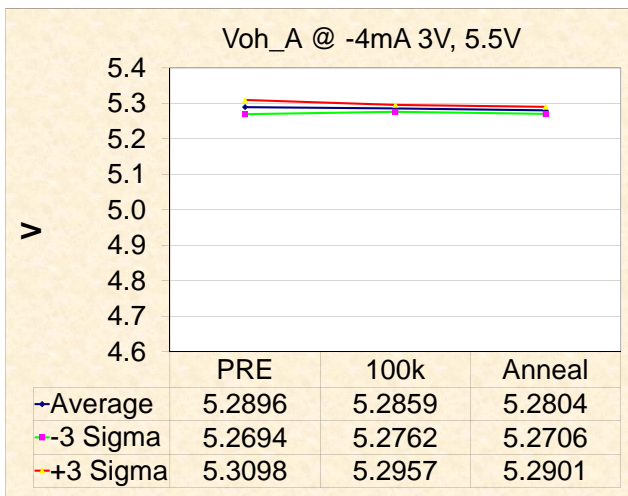
	PRE	100k	Anneal
→Average	-0.00056	-0.00038	-0.00086
←-3 Sigma	-0.00345	-0.00230	-0.00298
→+3 Sigma	0.00234	0.00154	0.00126

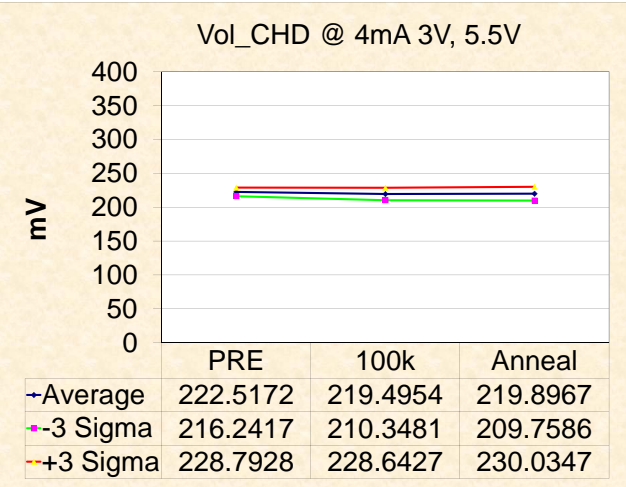
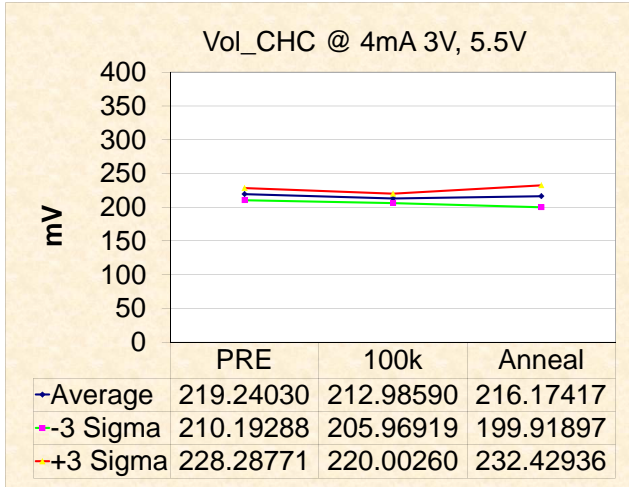
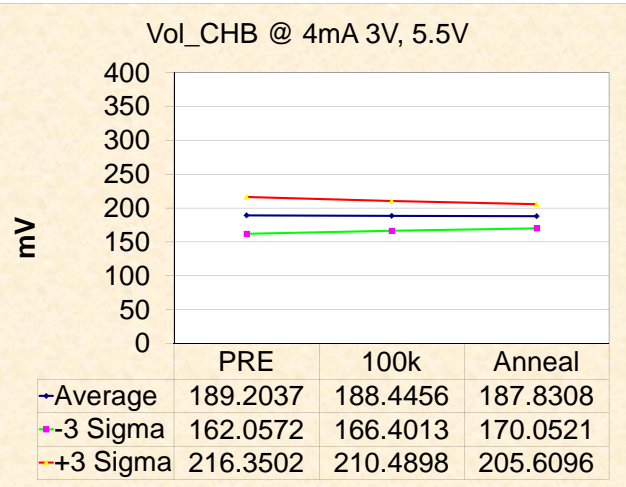
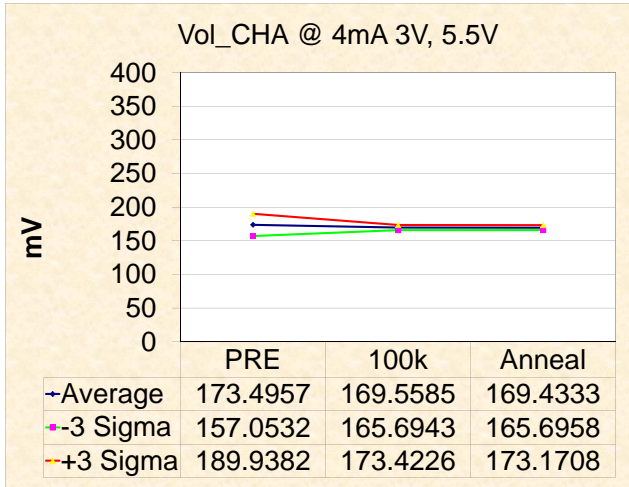
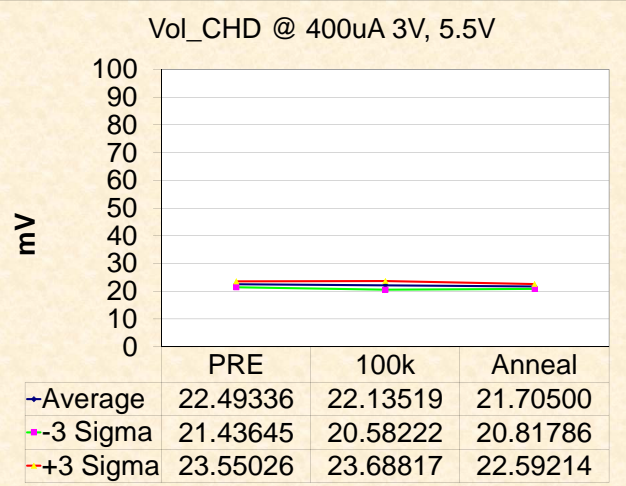
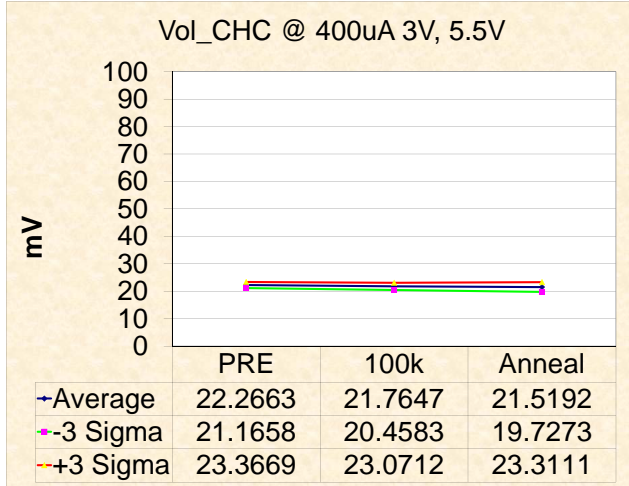
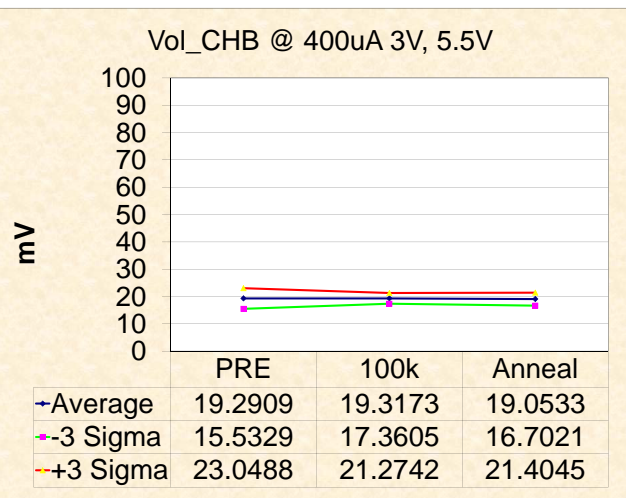
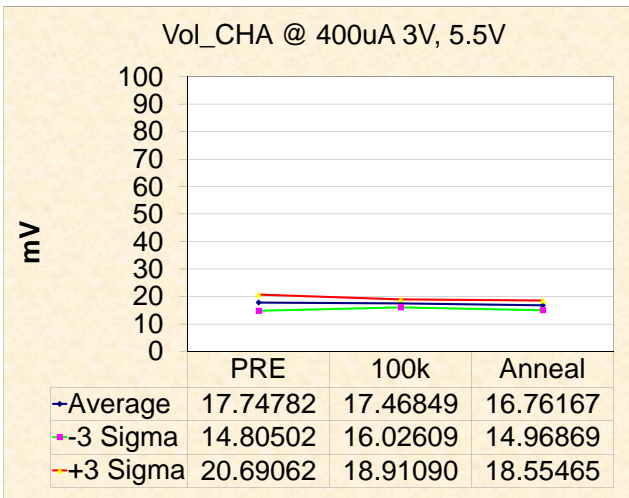
ChD Input Leakage @ 0V 3V, 5.5V

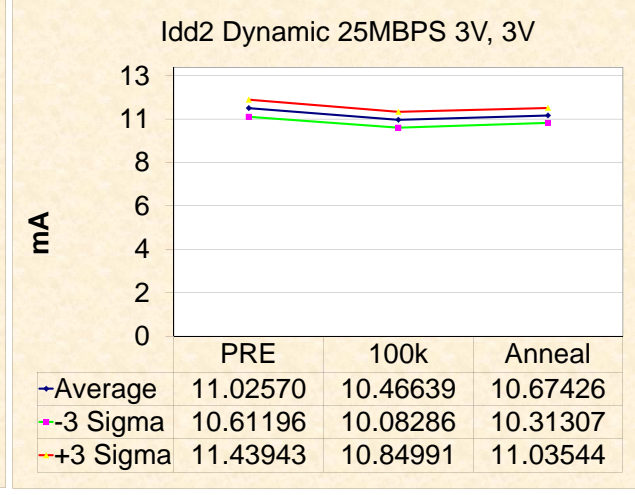
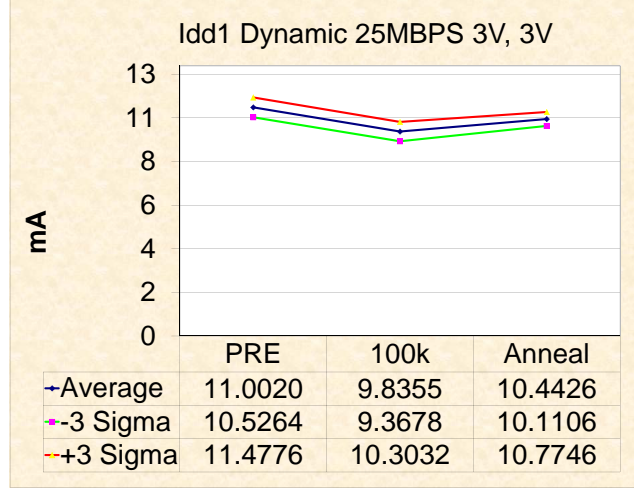
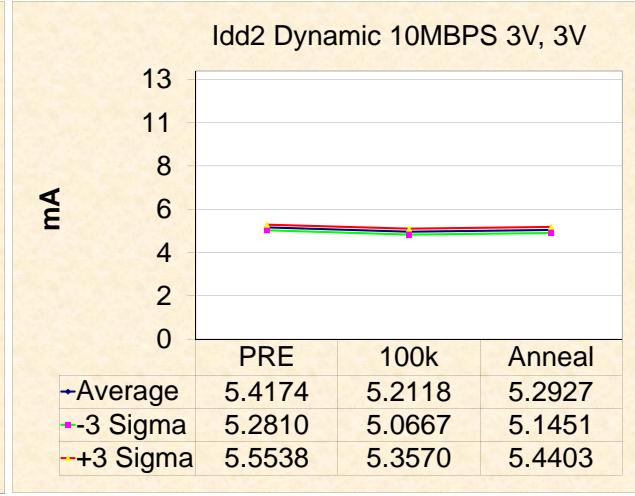
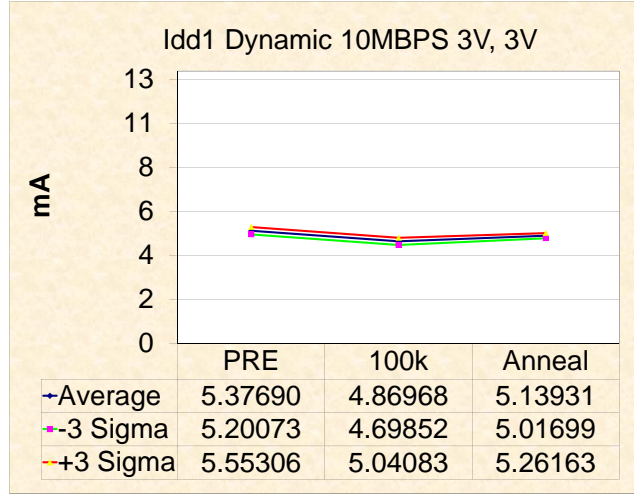
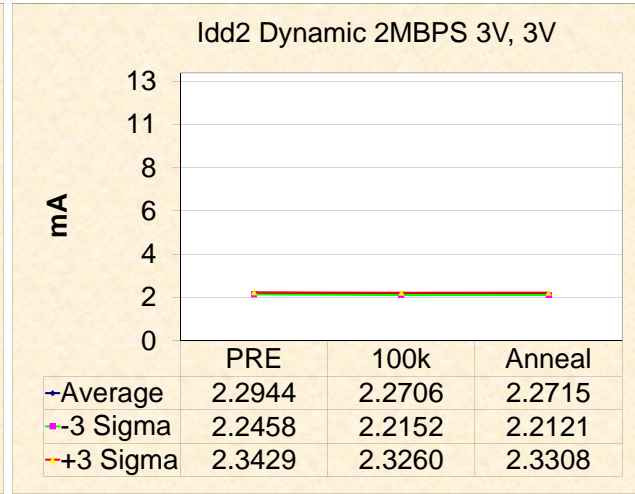
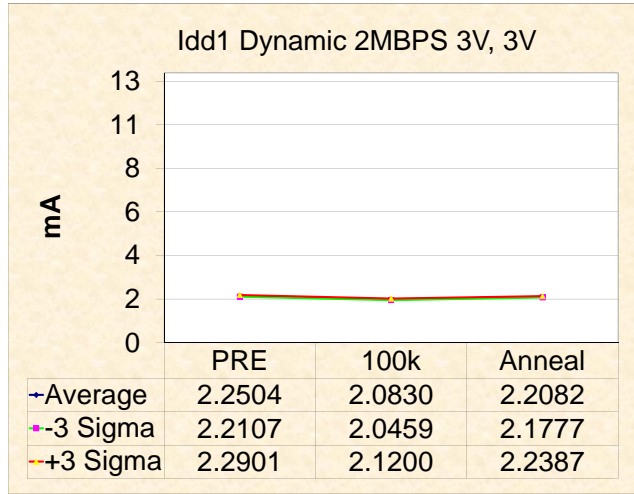
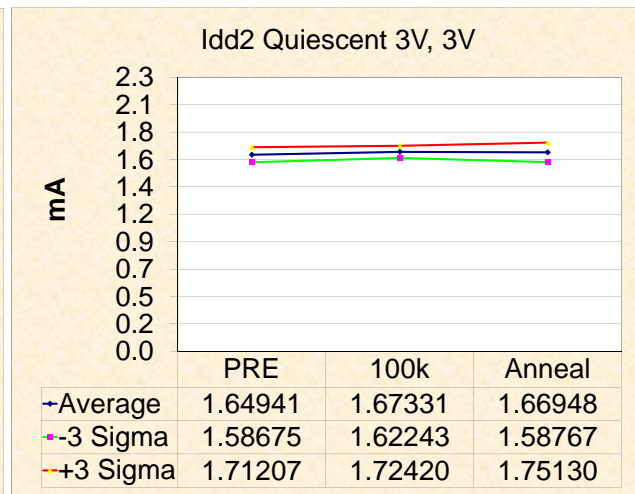
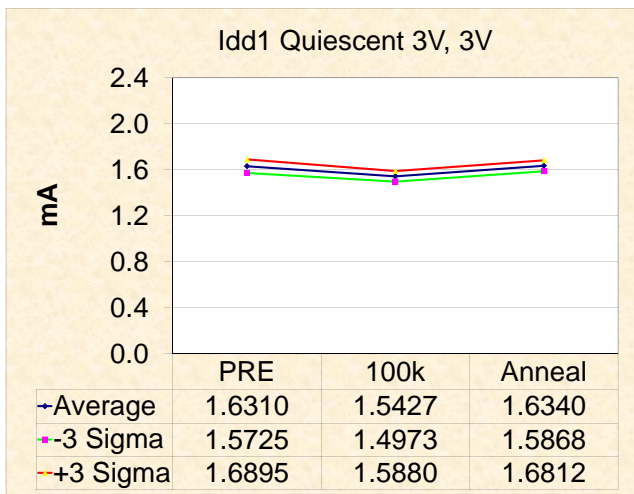


	PRE	100k	Anneal
→Average	-0.0006	-0.0006	-0.0004
←-3 Sigma	-0.0030	-0.0028	-0.0023
→+3 Sigma	0.0017	0.0016	0.0014

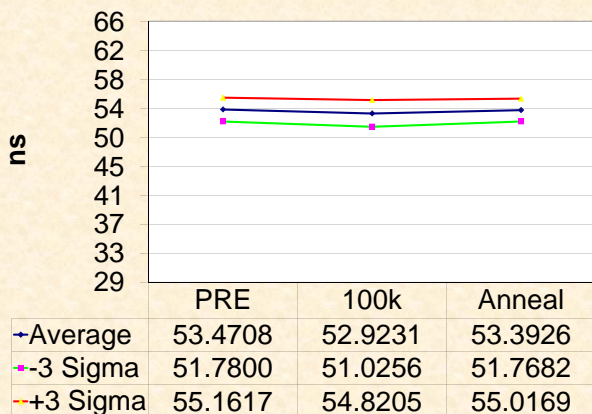




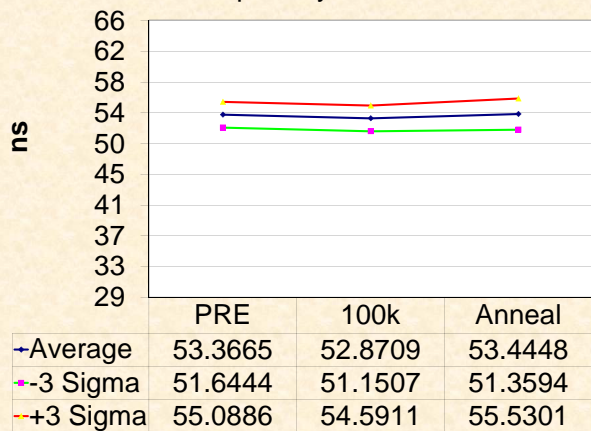




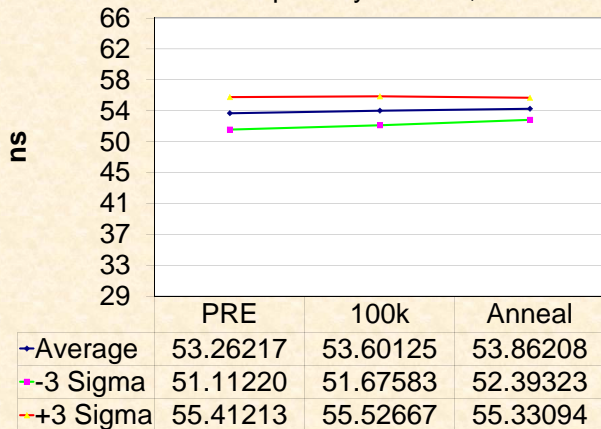
CHA Prop Delay Rise 3V, 3V



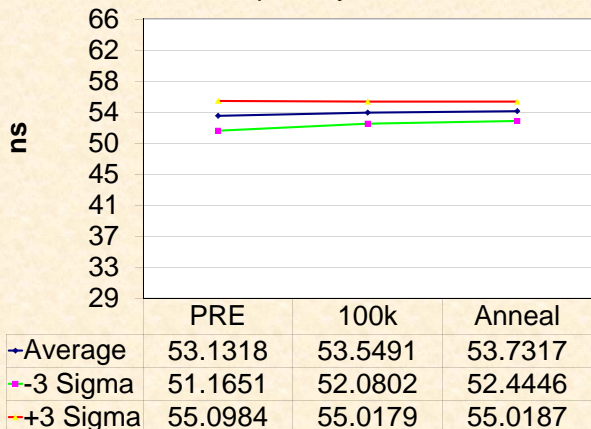
CHB Prop Delay Rise 3V, 3V



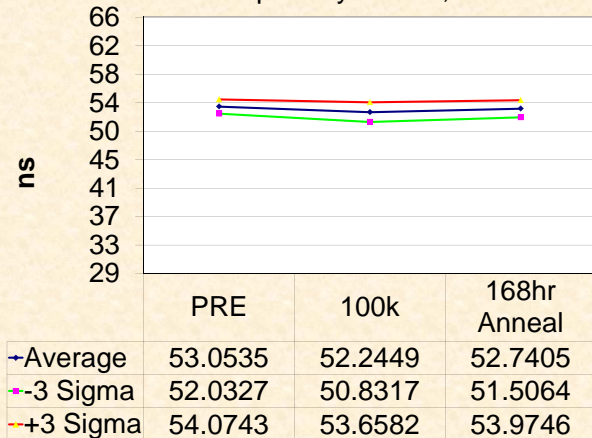
CHC Prop Delay Rise 3V, 3V



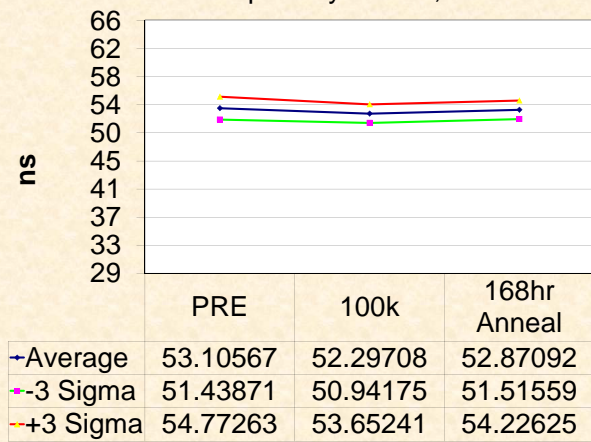
CHD Prop Delay Rise 3V, 3V



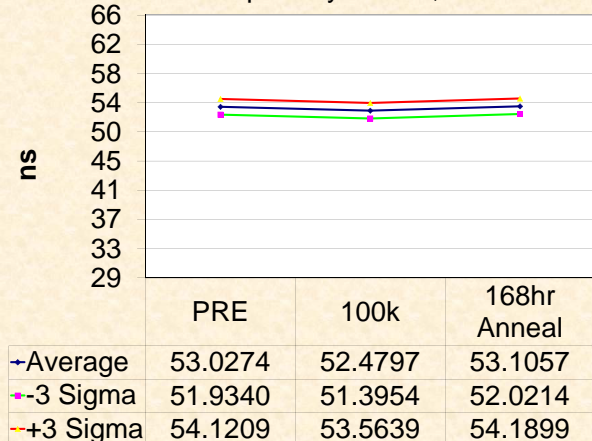
CHA Prop Delay Fall 3V, 3V



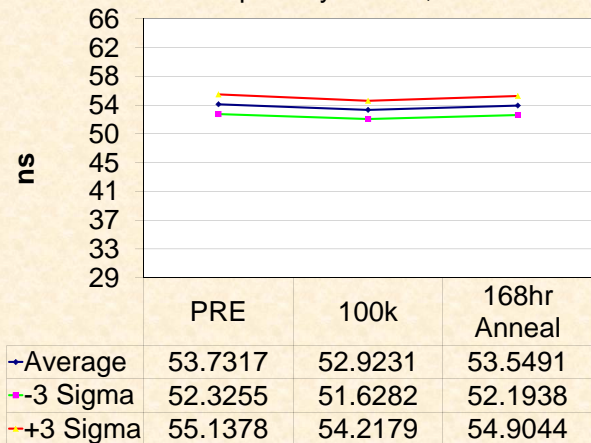
CHB Prop Delay Fall 3V, 3V

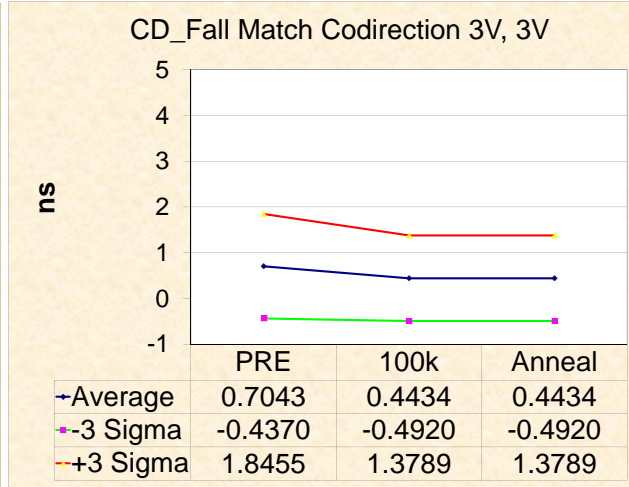
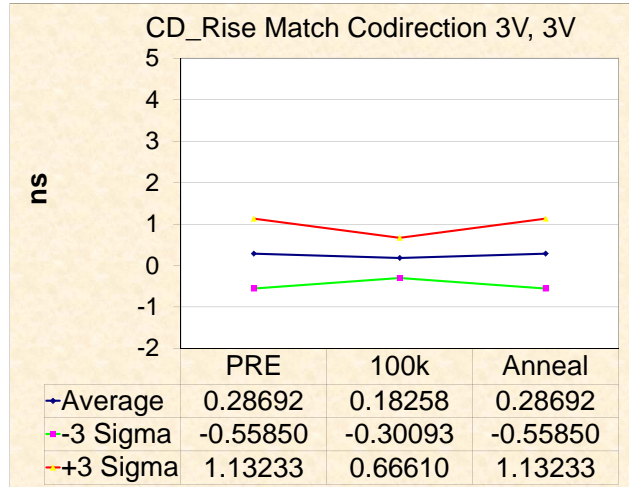
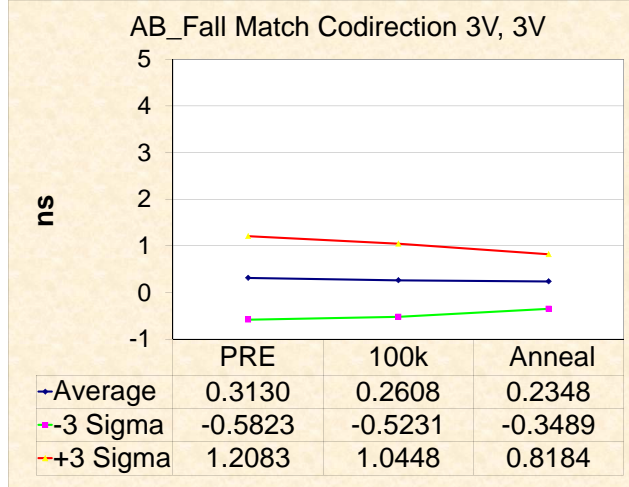
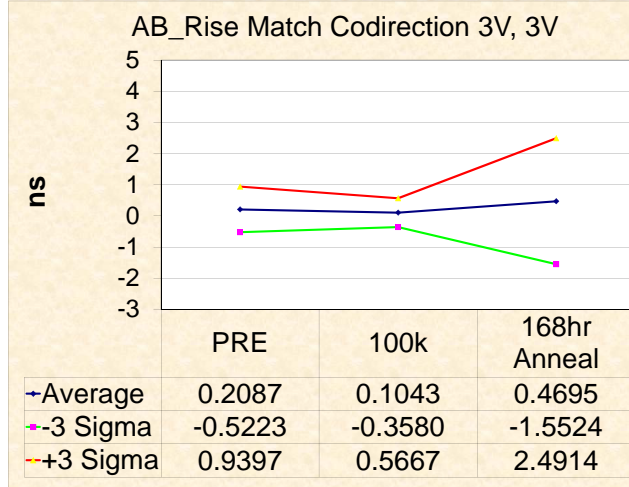
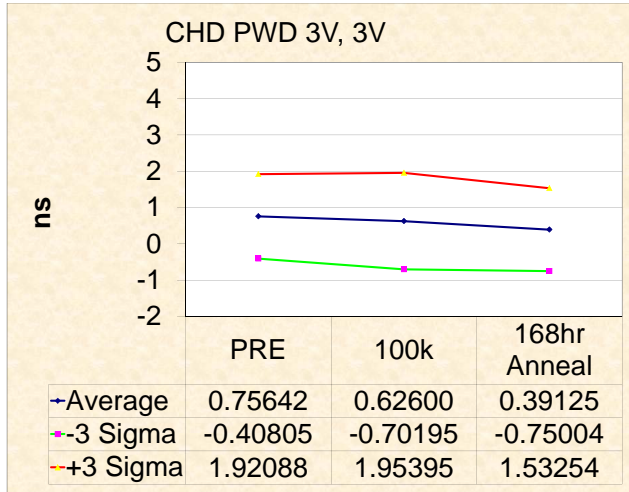
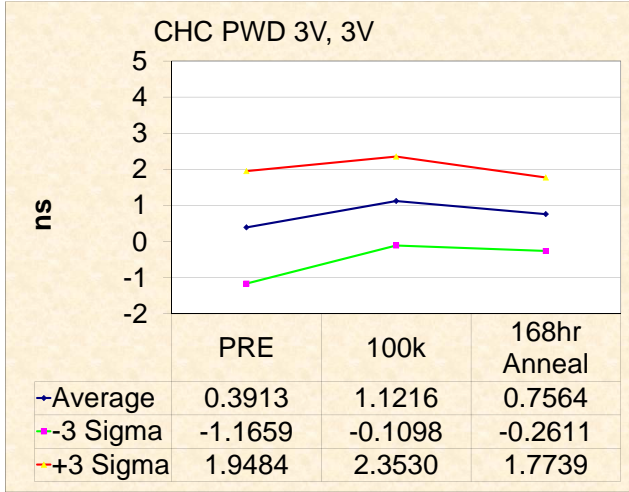
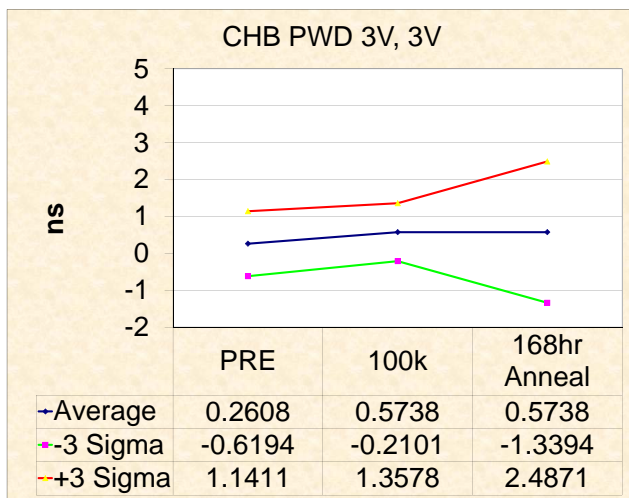
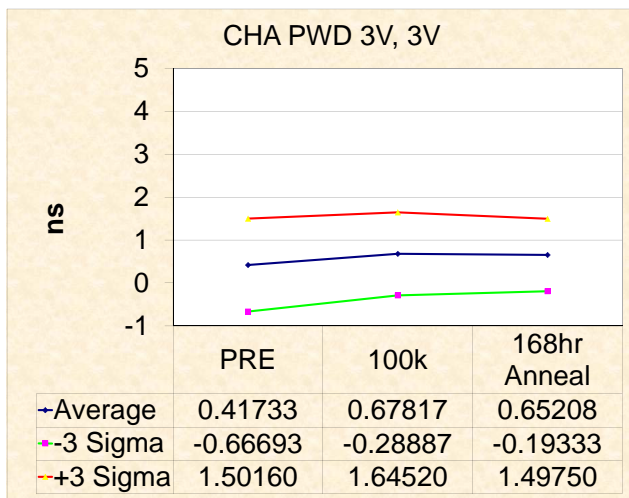


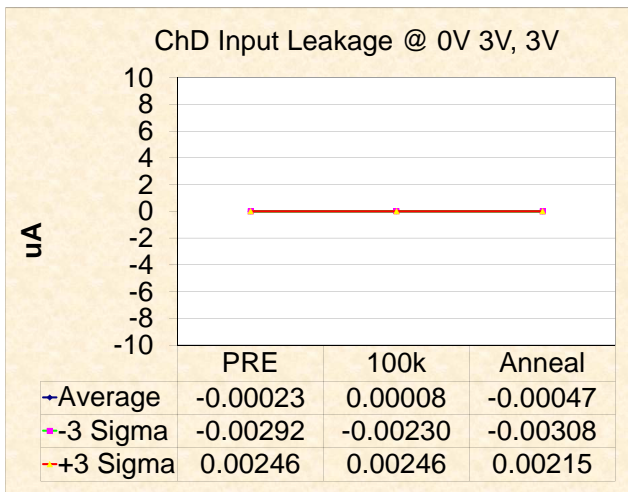
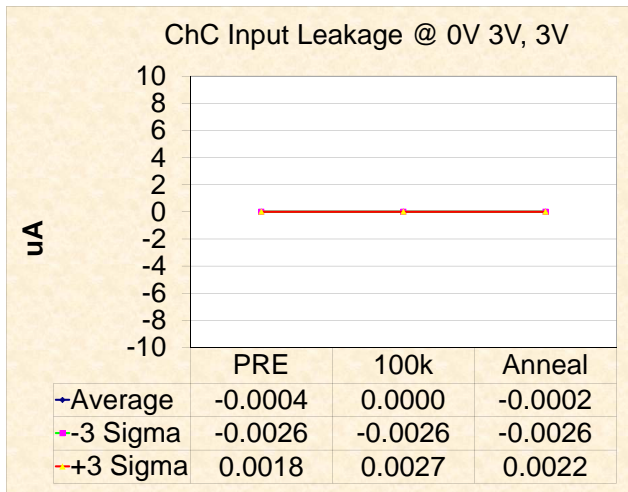
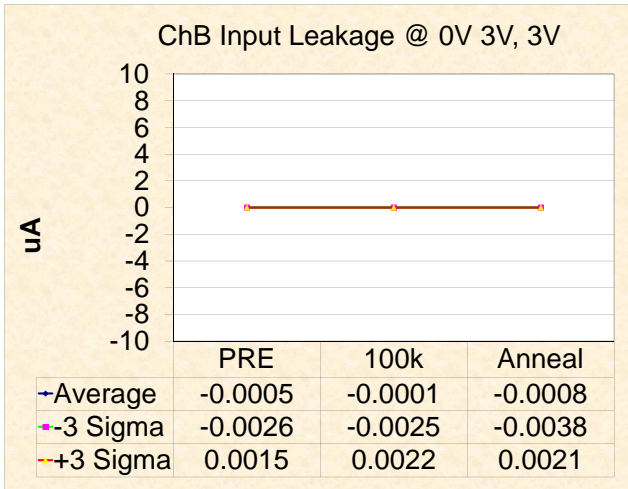
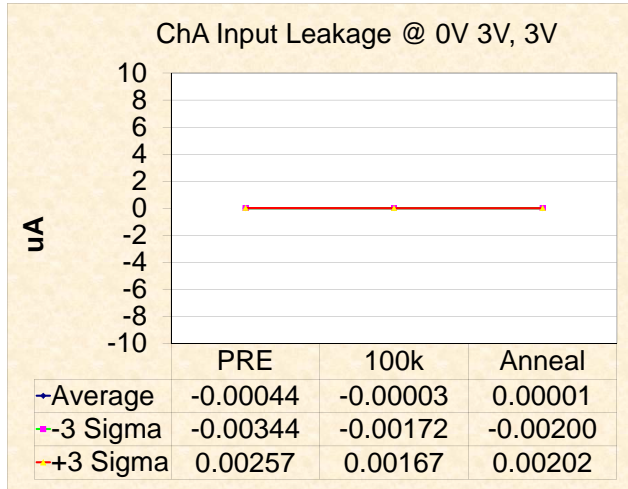
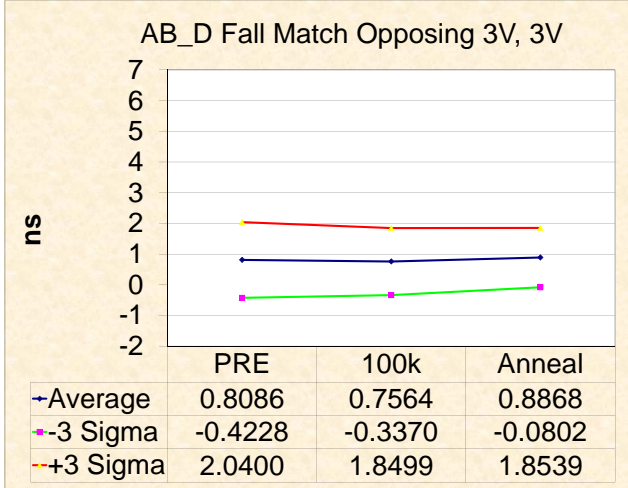
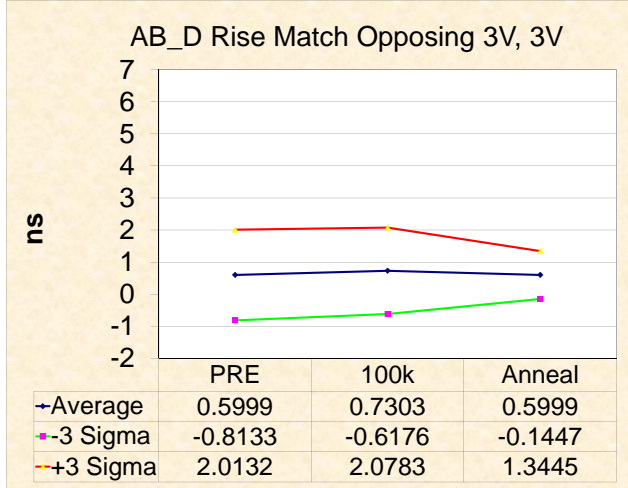
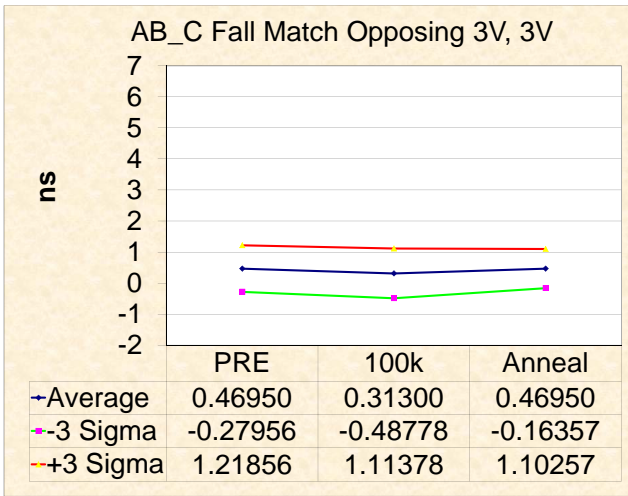
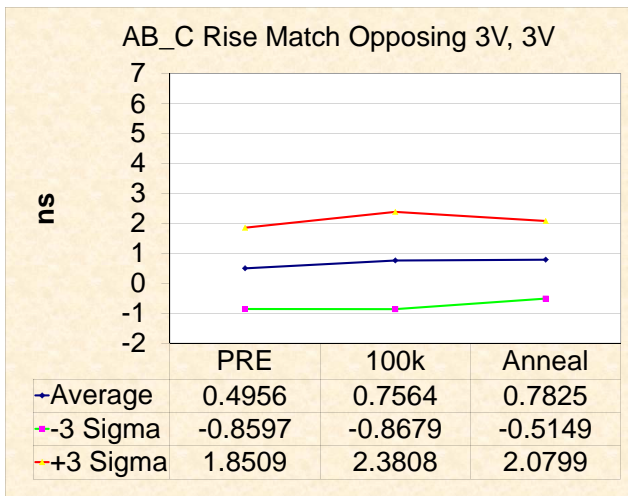
CHC Prop Delay Fall 3V, 3V

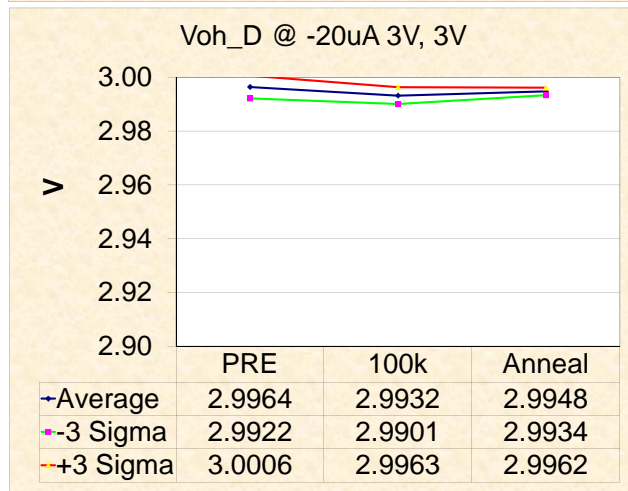
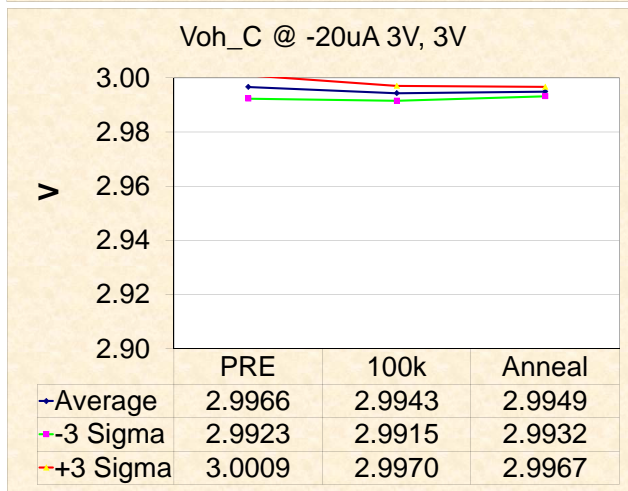
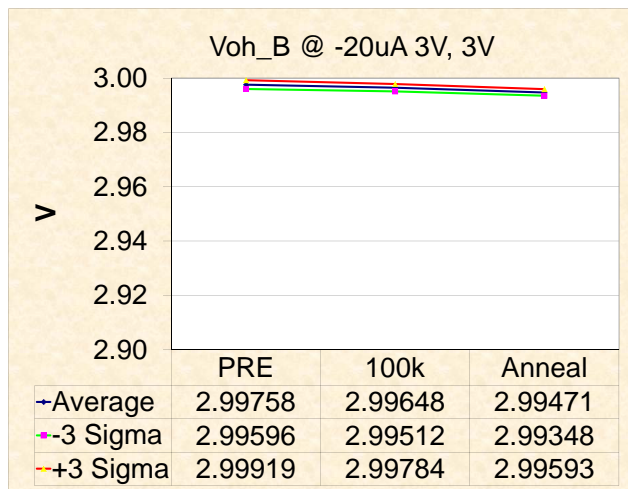
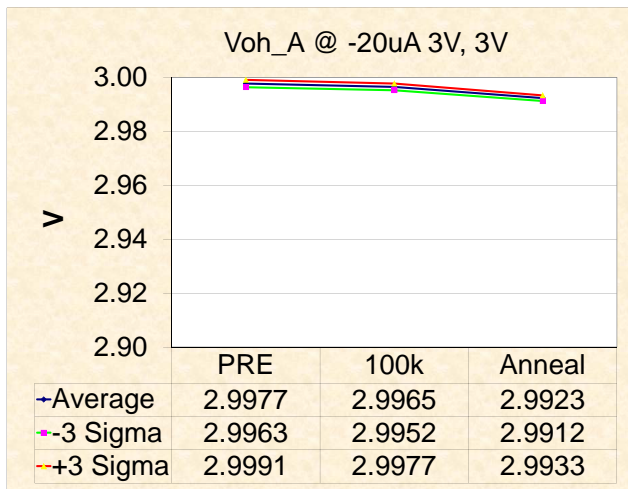
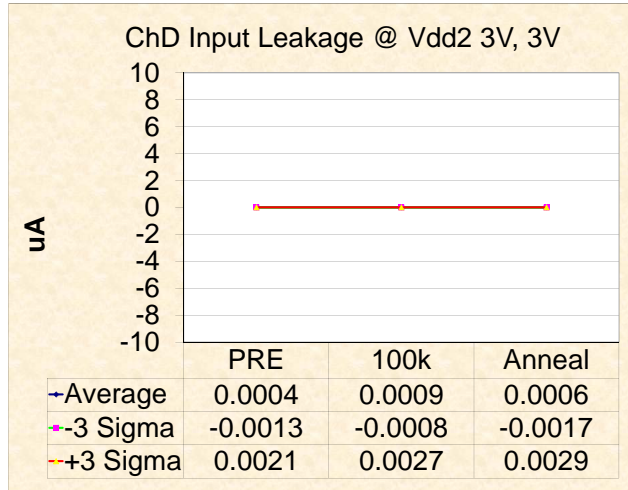
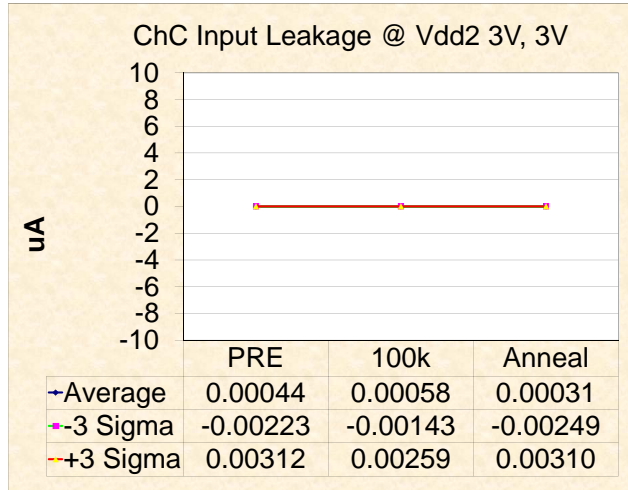
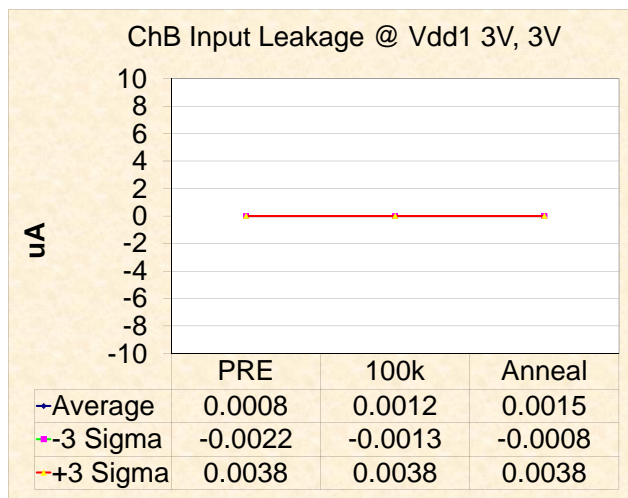
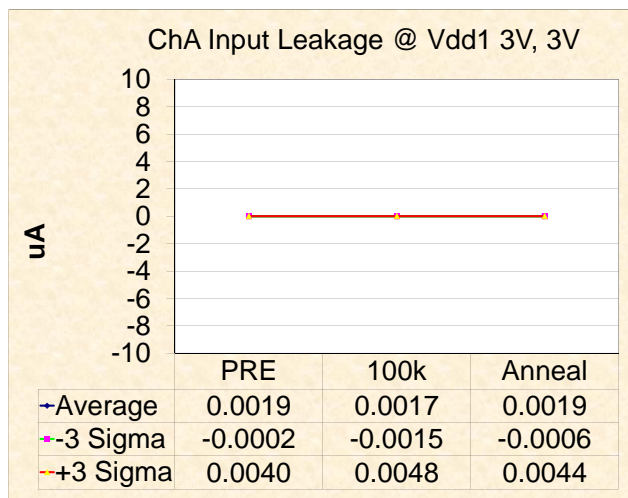


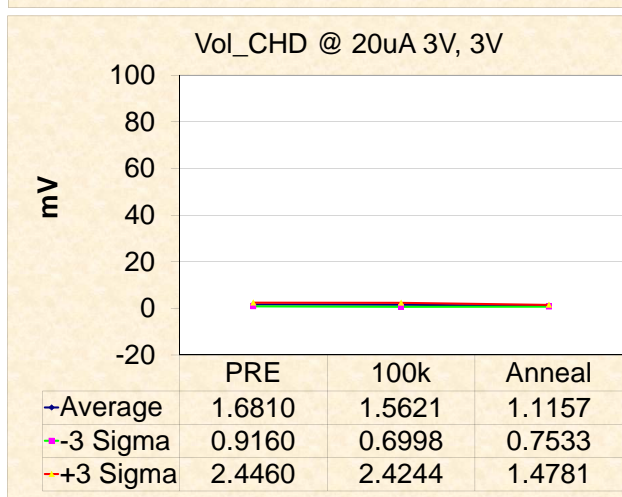
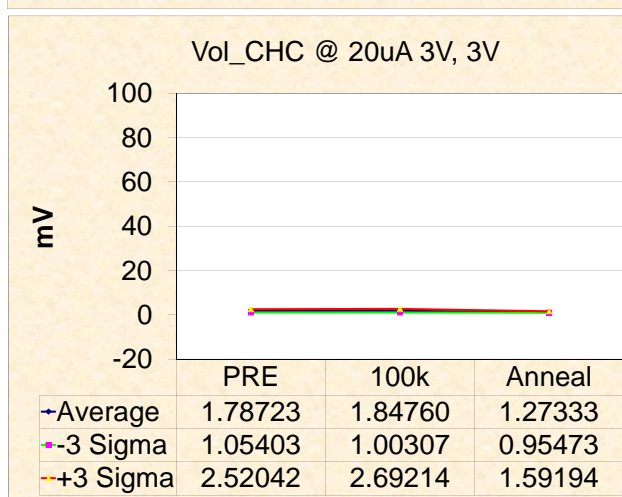
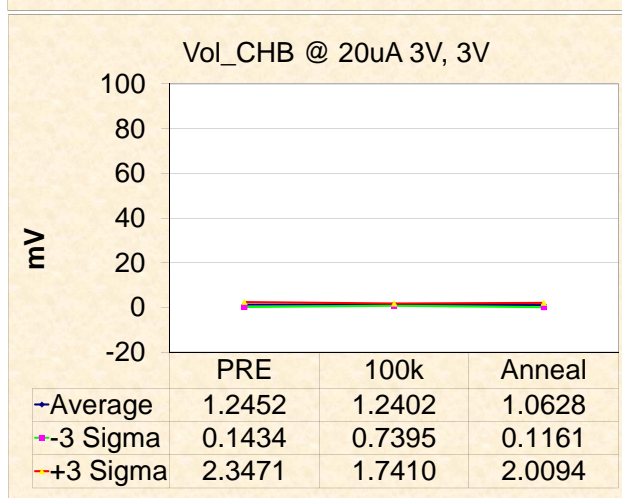
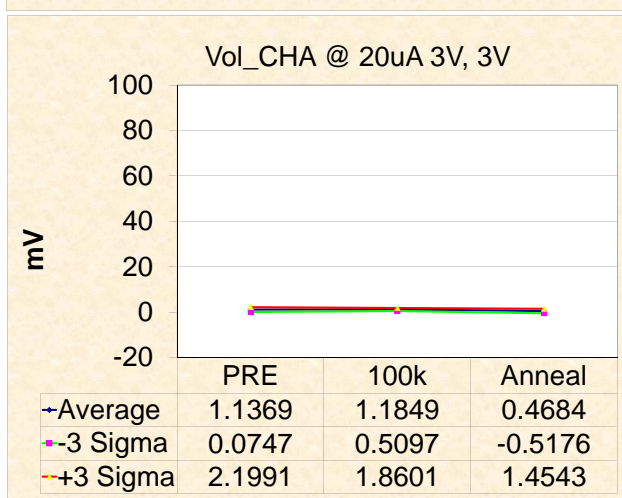
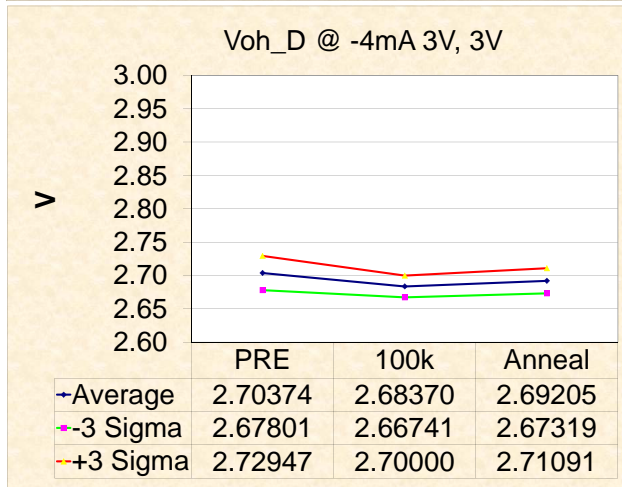
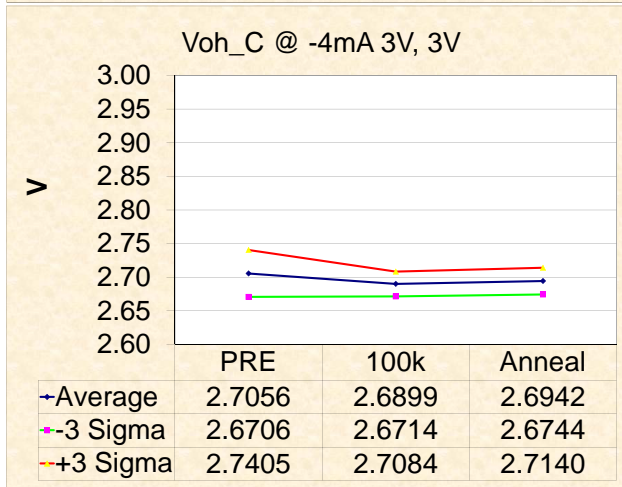
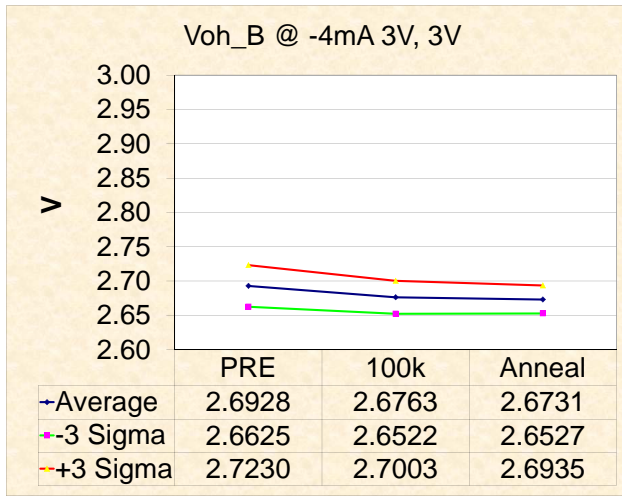
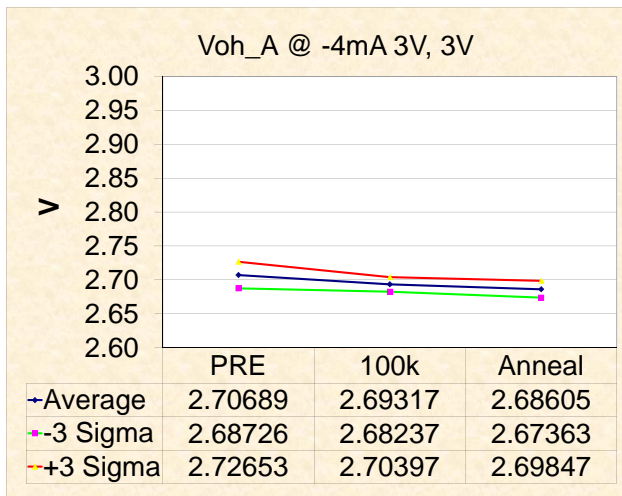
CHD Prop Delay Fall 3V, 3V

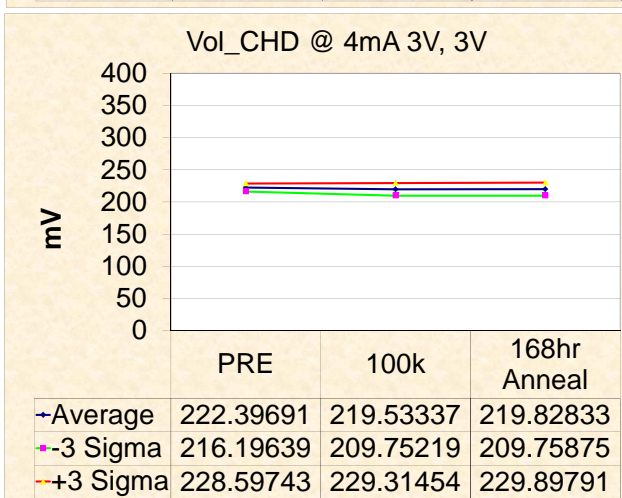
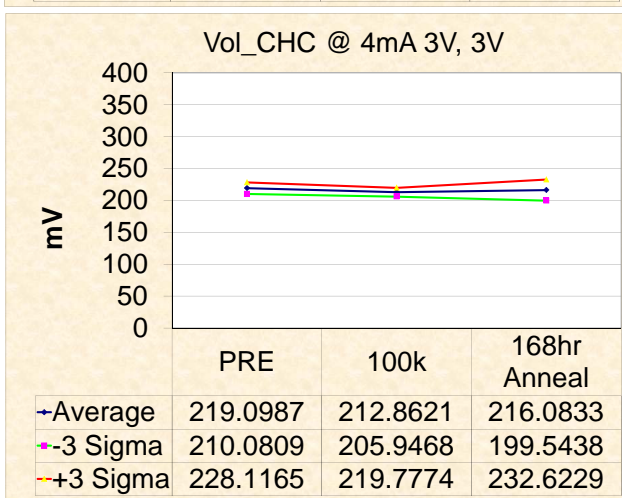
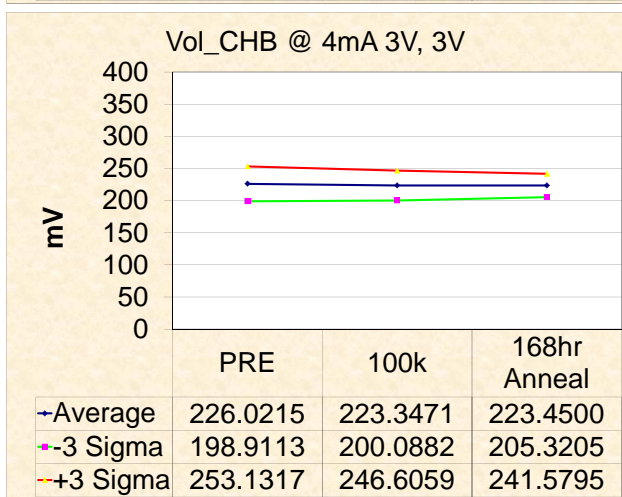
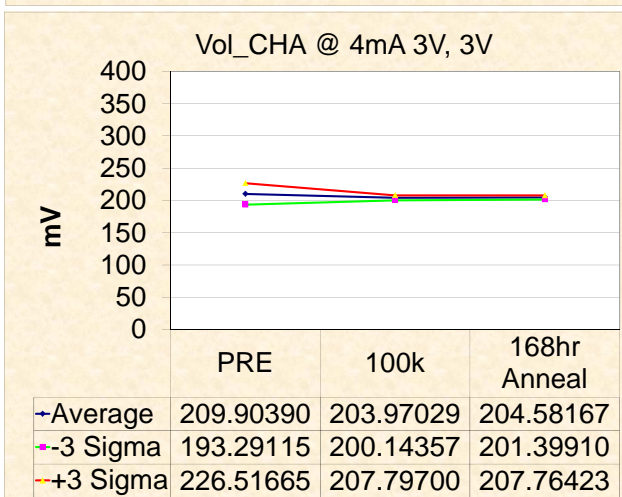
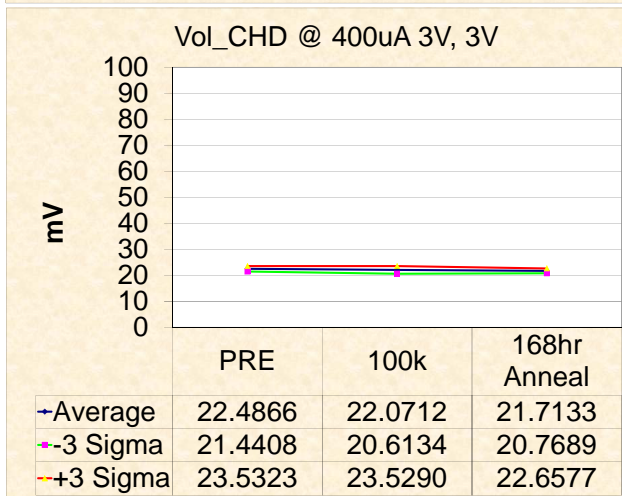
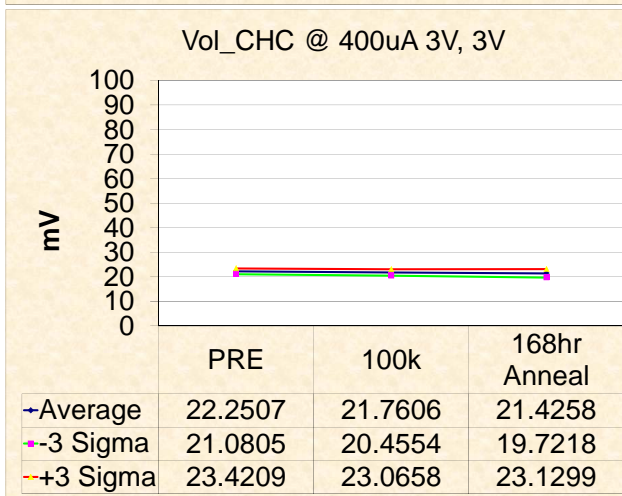
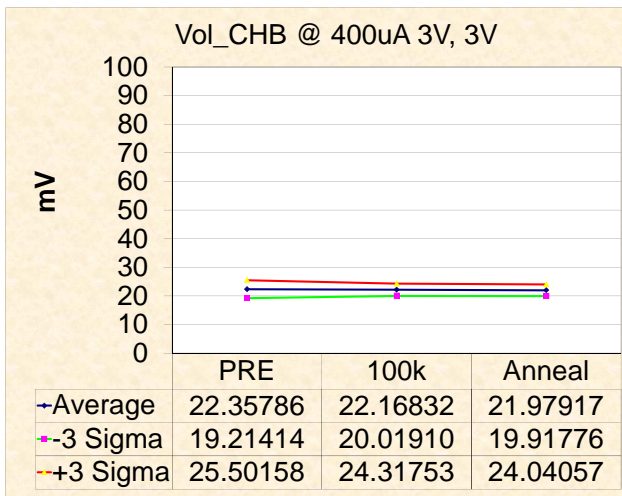
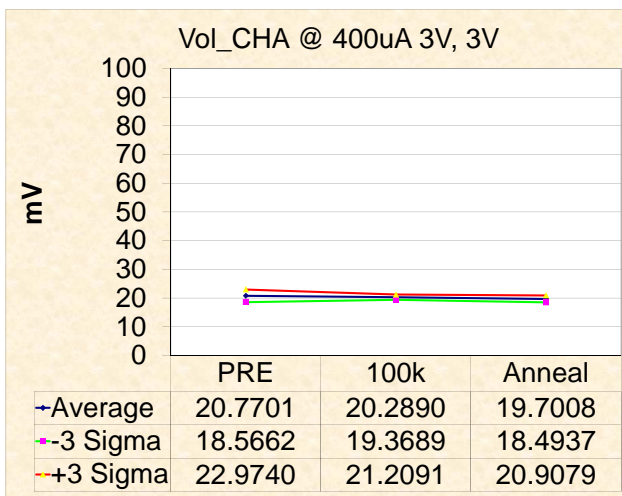


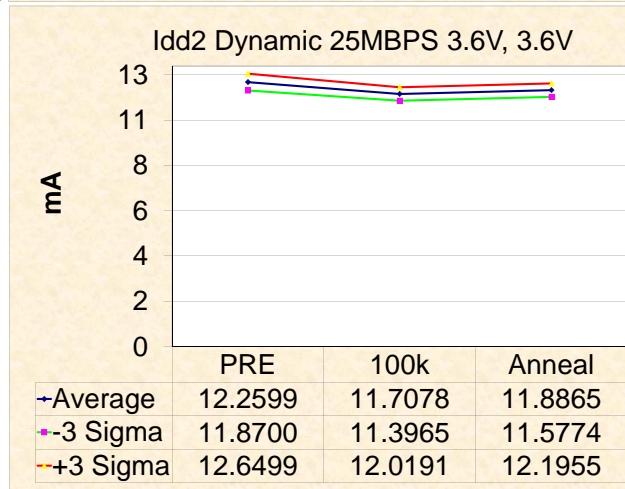
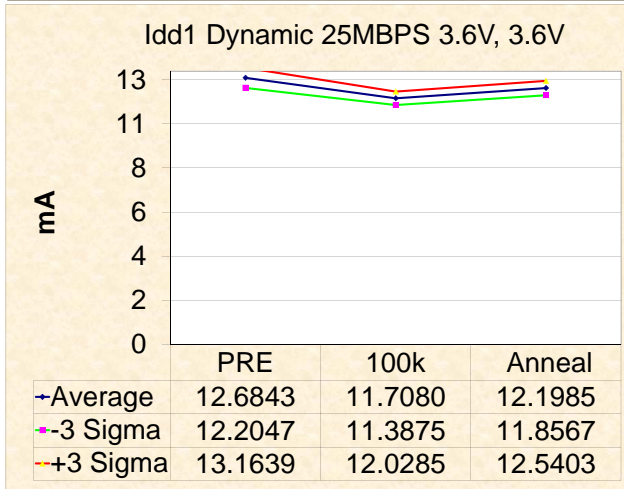
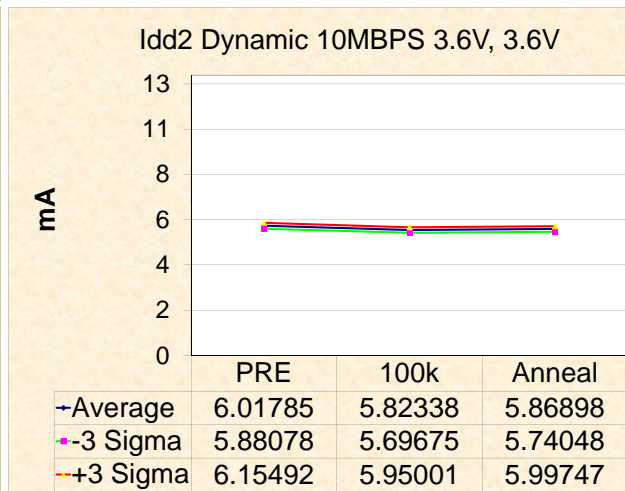
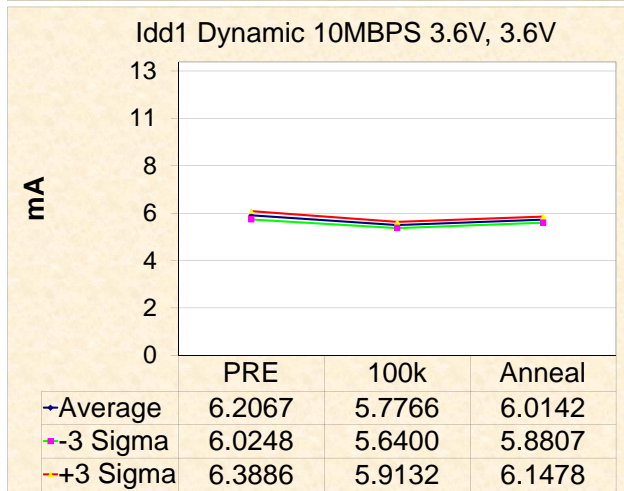
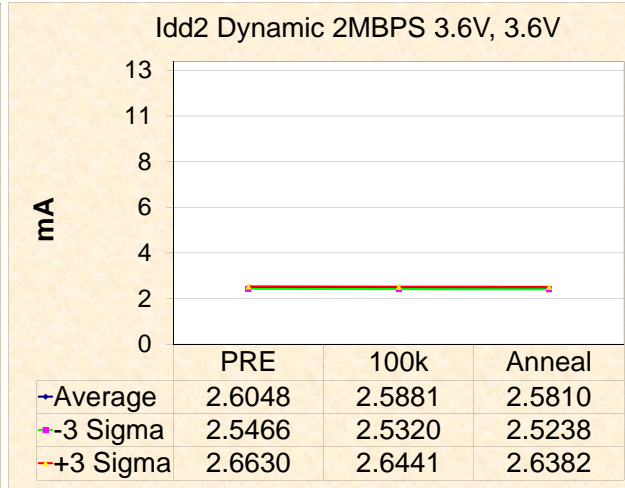
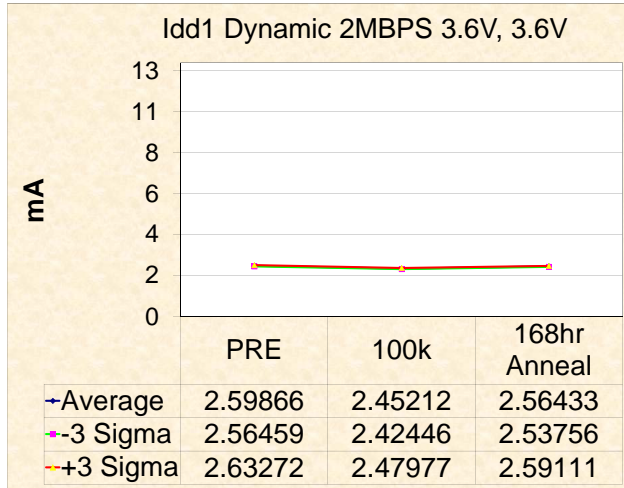
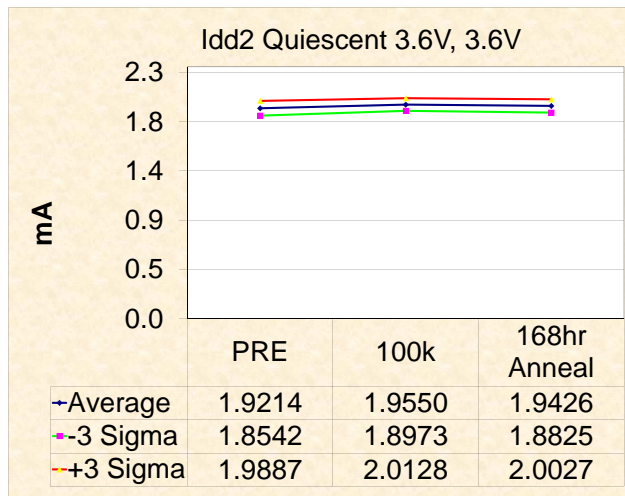
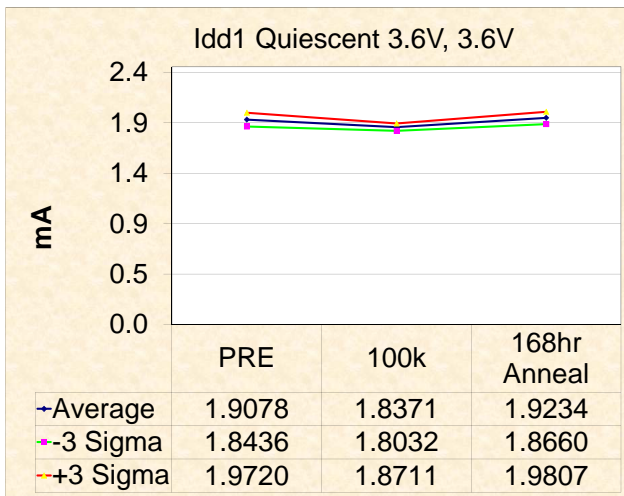


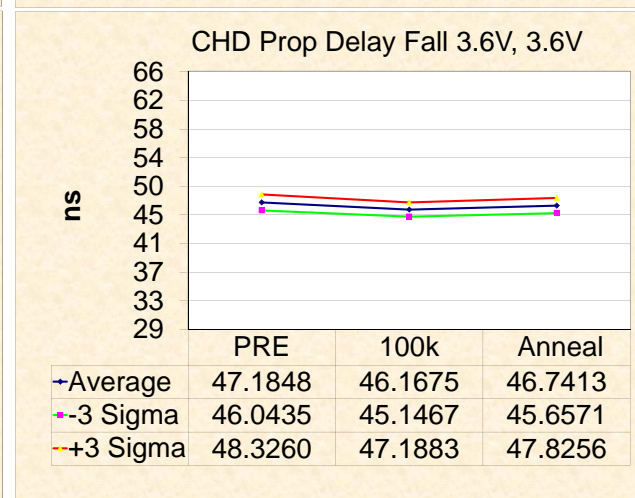
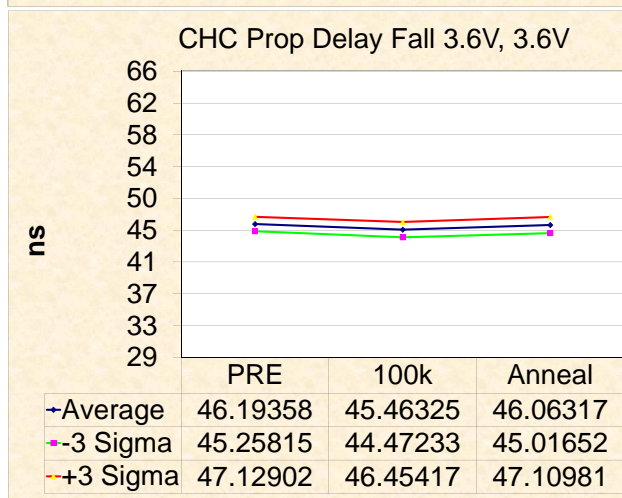
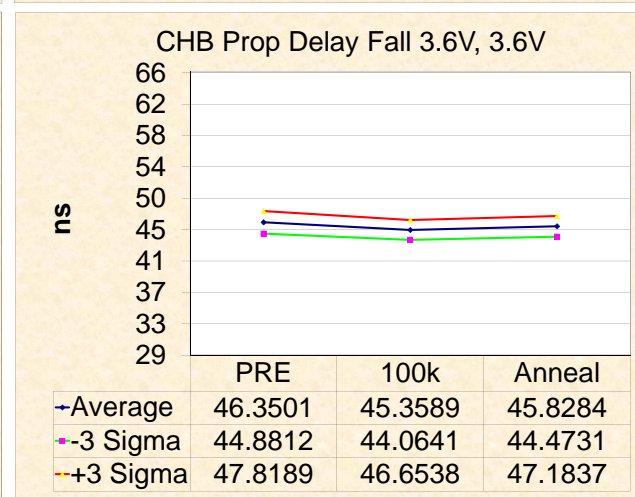
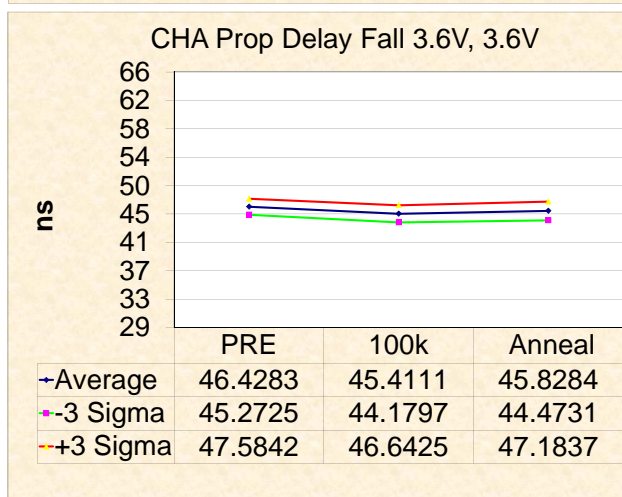
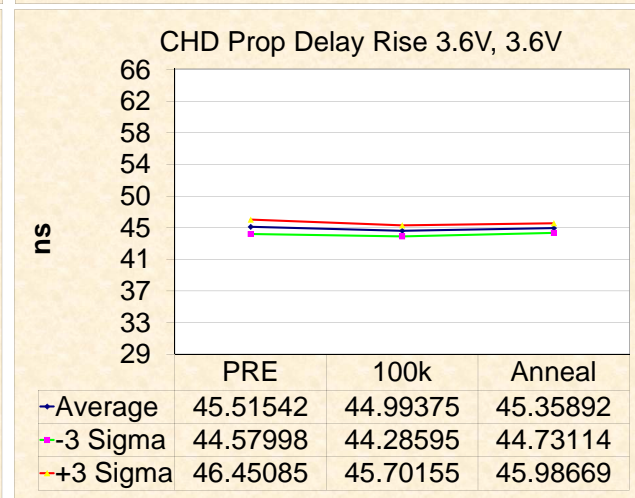
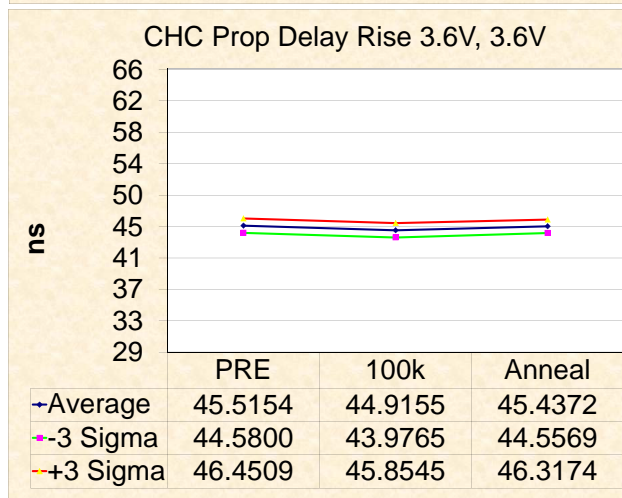
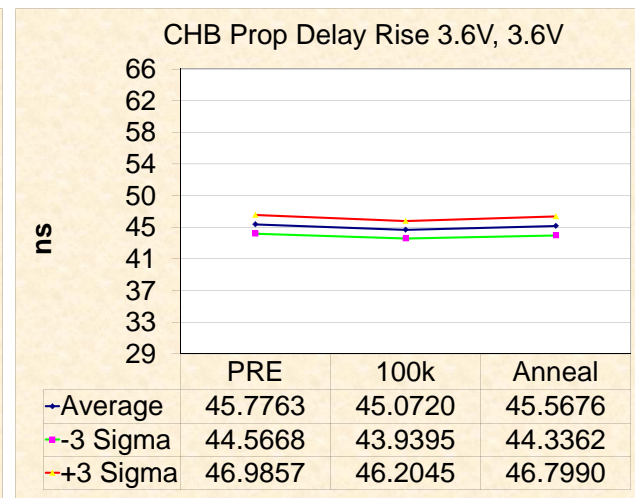
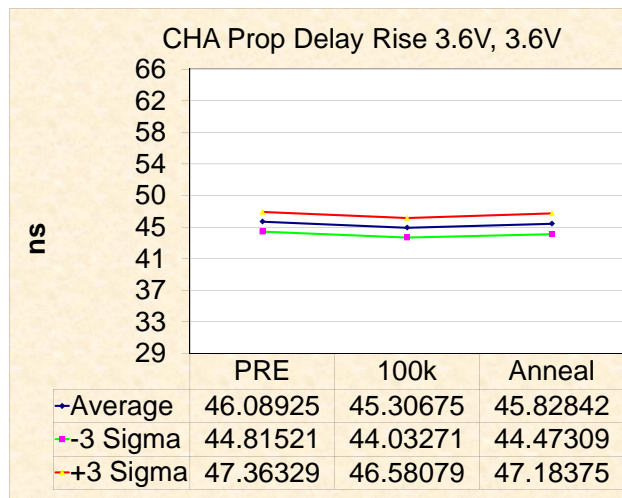


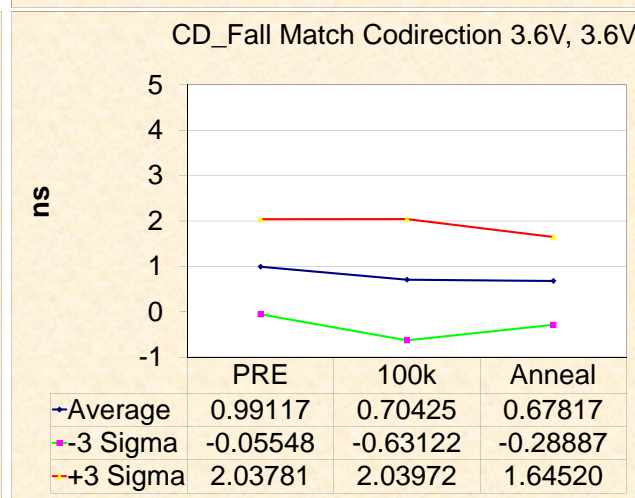
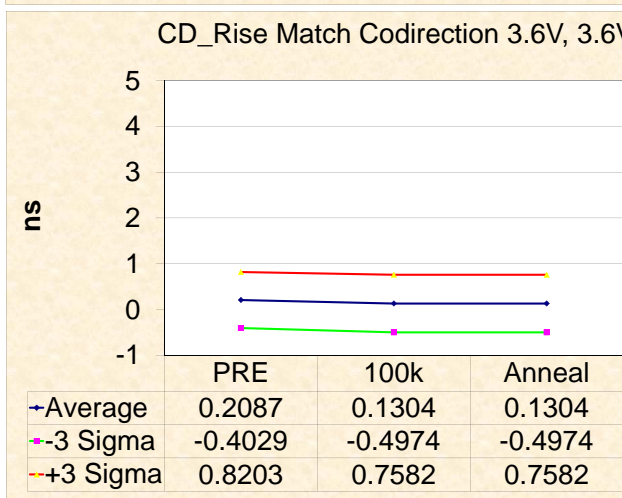
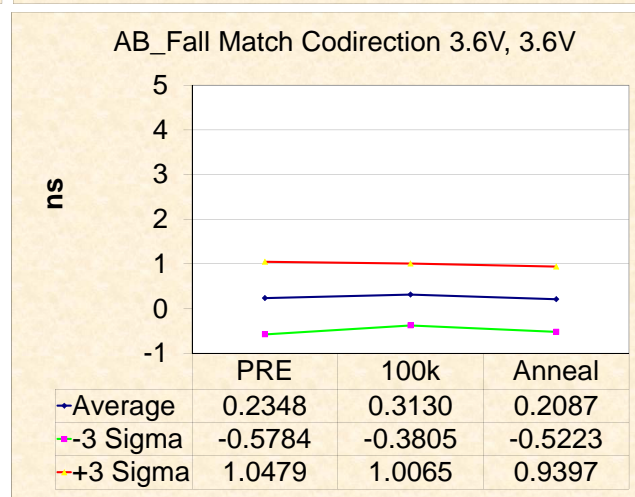
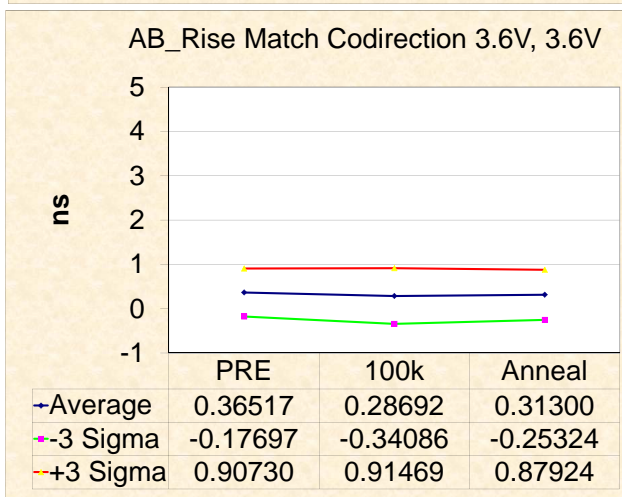
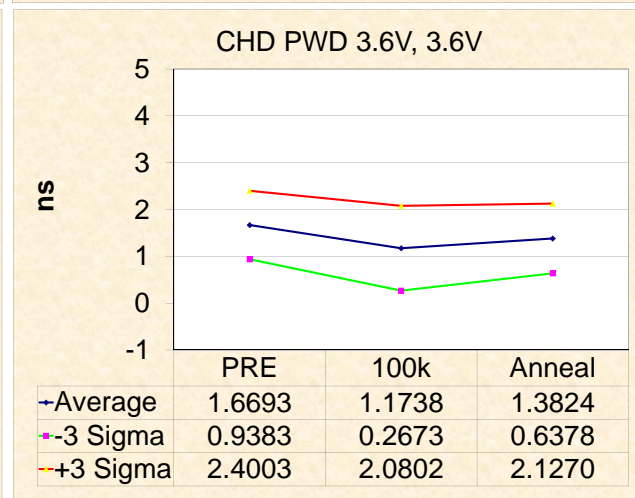
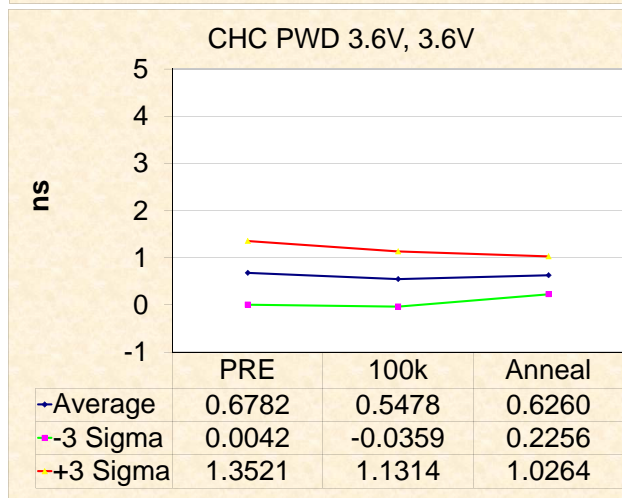
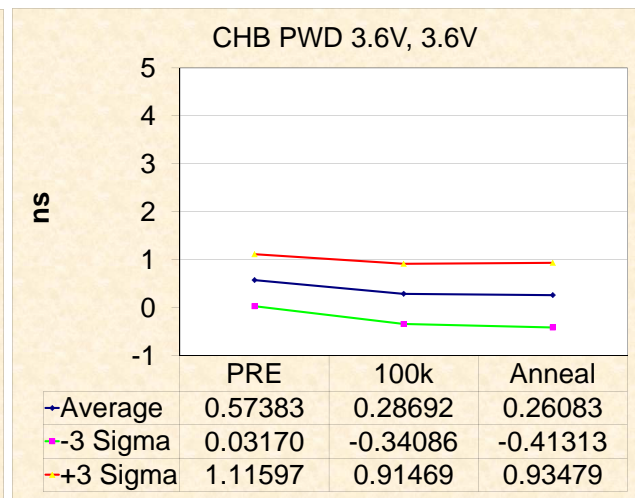
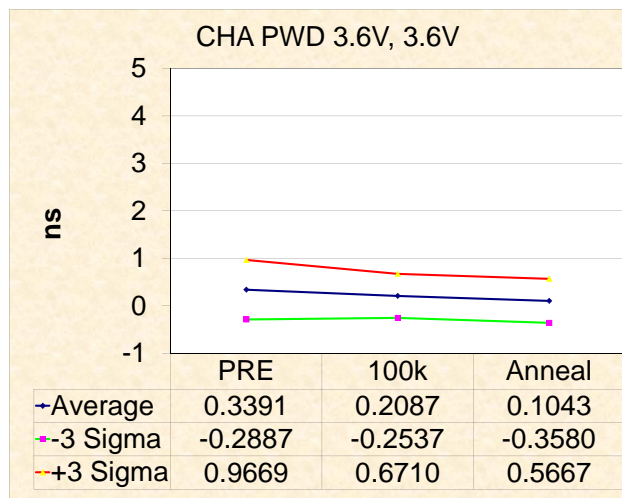




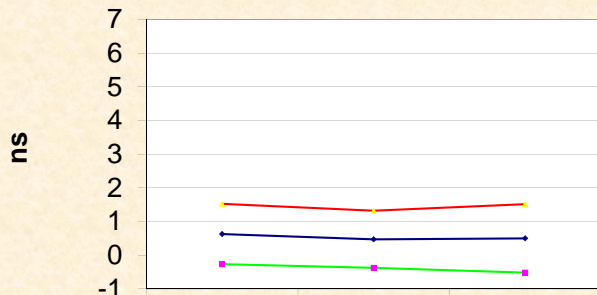






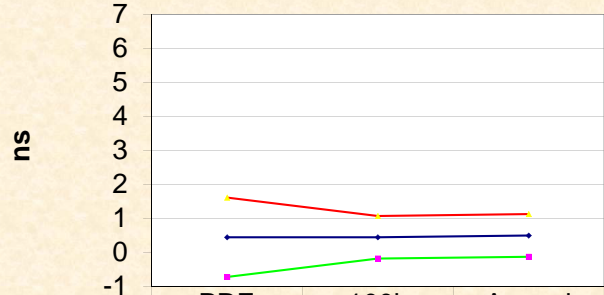


AB_C Rise Match Opposing 3.6V, 3.6V



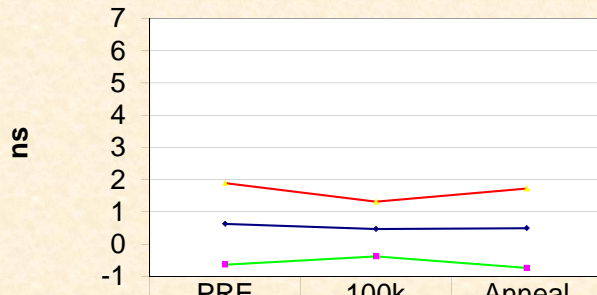
	PRE	100k	Anneal
→Average	0.6260	0.4695	0.4956
←-3 Sigma	-0.2693	-0.3799	-0.5219
→+3 Sigma	1.5213	1.3189	1.5131

AB_C Fall Match Opposing 3.6V, 3.6V



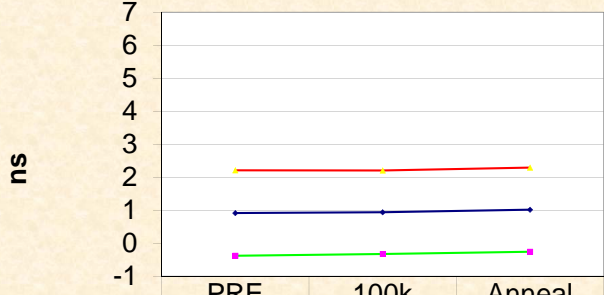
	PRE	100k	Anneal
→Average	0.4434	0.4434	0.4956
←-3 Sigma	-0.7210	-0.1844	-0.1322
→+3 Sigma	1.6079	1.0712	1.1234

AB_D Rise Match Opposing 3.6V, 3.6V



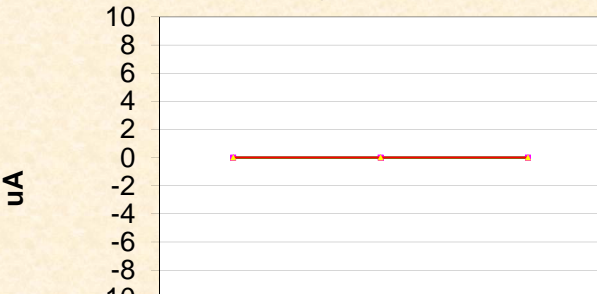
	PRE	100k	Anneal
→Average	0.62600	0.46950	0.49558
←-3 Sigma	-0.64015	-0.37986	-0.73580
→+3 Sigma	1.89215	1.31886	1.72696

AB_D Fall Match Opposing 3.6V, 3.6V



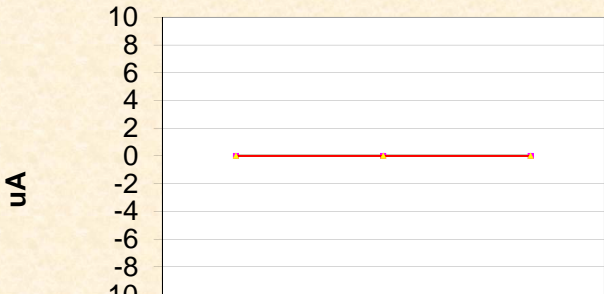
	PRE	100k	Anneal
→Average	0.9129	0.9390	1.0173
←-3 Sigma	-0.3819	-0.3271	-0.2568
→+3 Sigma	2.2078	2.2051	2.2913

ChA Input Leakage @ 0V 3.6V, 3.6V



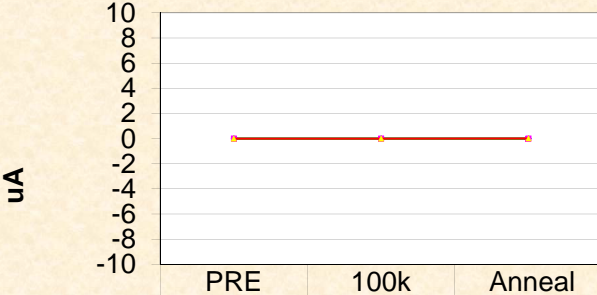
	PRE	100k	Anneal
→Average	-0.0007	-0.0004	-0.0001
←-3 Sigma	-0.0033	-0.0024	-0.0025
→+3 Sigma	0.0019	0.0017	0.0023

ChB Input Leakage @ 0V 3.6V, 3.6V



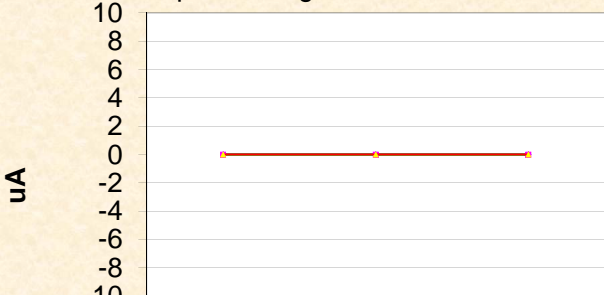
	PRE	100k	Anneal
→Average	-0.00031	-0.00057	-0.00001
←-3 Sigma	-0.00260	-0.00308	-0.00223
→+3 Sigma	0.00198	0.00194	0.00221

ChC Input Leakage @ 0V 3.6V, 3.6V



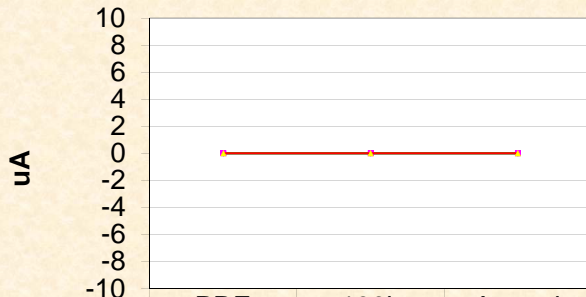
	PRE	100k	Anneal
→Average	-0.0001	-0.0002	-0.0002
←-3 Sigma	-0.0023	-0.0018	-0.0030
→+3 Sigma	0.0020	0.0015	0.0025

ChD Input Leakage @ 0V 3.6V, 3.6V



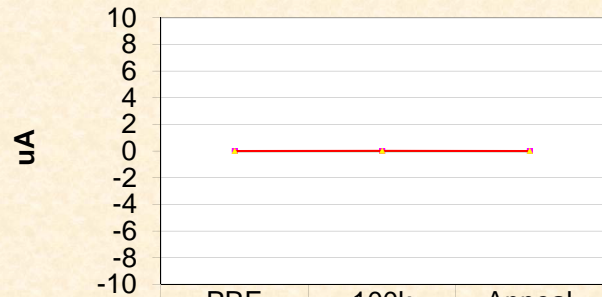
	PRE	100k	Anneal
→Average	-0.0005	-0.0001	-0.0002
←-3 Sigma	-0.0030	-0.0026	-0.0021
→+3 Sigma	0.0020	0.0024	0.0016

ChA Input Leakage @ Vdd1 3.6V, 3.6V



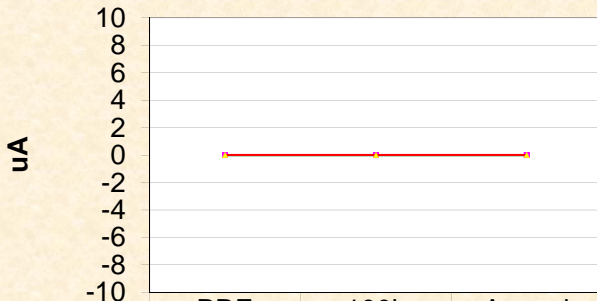
	PRE	100k	Anneal
+Average	0.00263	0.00239	0.00197
-3 Sigma	0.00051	0.00110	-0.00014
+3 Sigma	0.00475	0.00367	0.00408

ChB Input Leakage @ Vdd1 3.6V, 3.6V



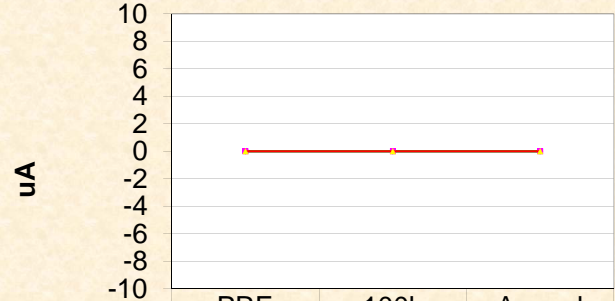
	PRE	100k	Anneal
+Average	0.0017	0.0016	0.0017
-3 Sigma	-0.0001	-0.0022	-0.0003
+3 Sigma	0.0035	0.0054	0.0037

ChC Input Leakage @ Vdd2 3.6V, 3.6V



	PRE	100k	Anneal
+Average	0.0006	0.0002	0.0006
-3 Sigma	-0.0014	-0.0025	-0.0014
+3 Sigma	0.0026	0.0029	0.0025

ChD Input Leakage @ Vdd2 3.6V, 3.6V



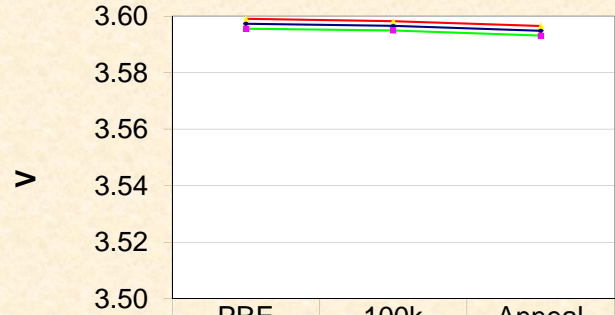
	PRE	100k	Anneal
+Average	0.00043	0.00078	0.00066
-3 Sigma	-0.00088	-0.00198	-0.00124
+3 Sigma	0.00175	0.00354	0.00255

Voh_A @ -20uA 3.6V, 3.6V



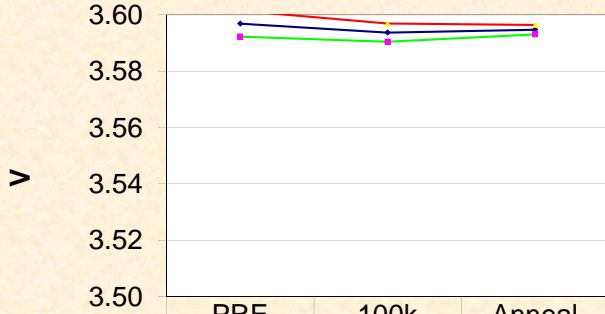
	PRE	100k	Anneal
+Average	3.5975	3.5965	3.5924
-3 Sigma	3.5959	3.5951	3.5908
+3 Sigma	3.5991	3.5979	3.5941

Voh_B @ -20uA 3.6V, 3.6V



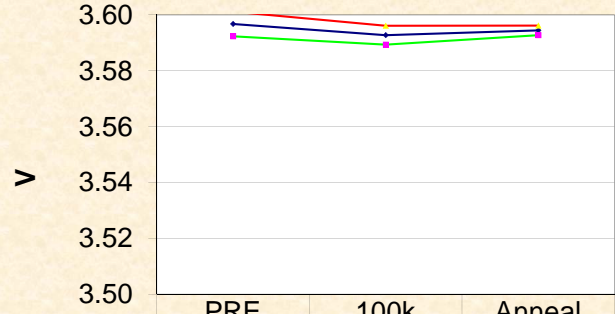
	PRE	100k	Anneal
+Average	3.5973	3.5965	3.5948
-3 Sigma	3.5955	3.5949	3.5931
+3 Sigma	3.5990	3.5982	3.5965

Voh_C @ -20uA 3.6V, 3.6V

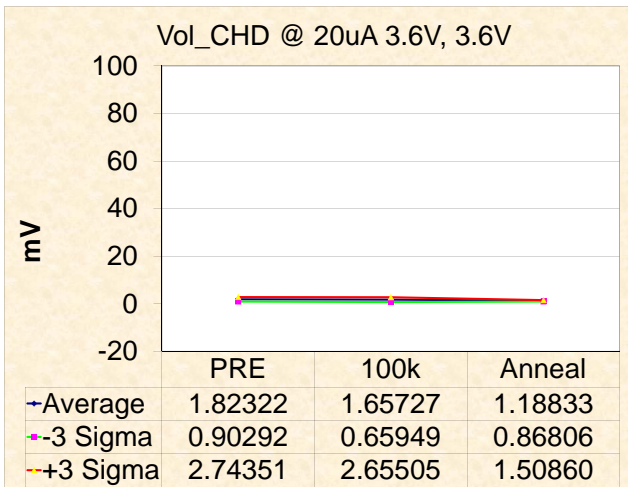
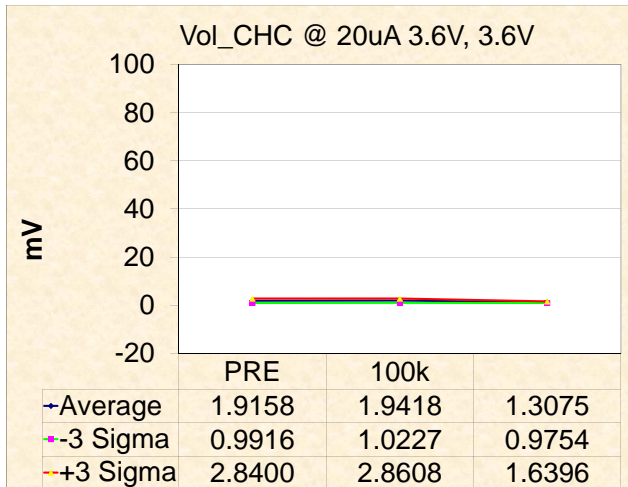
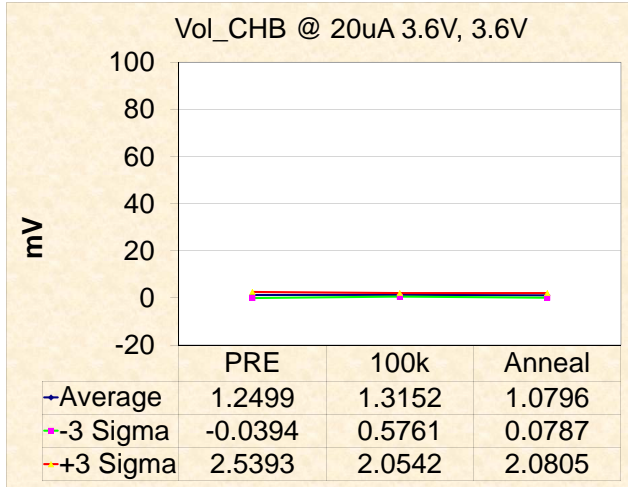
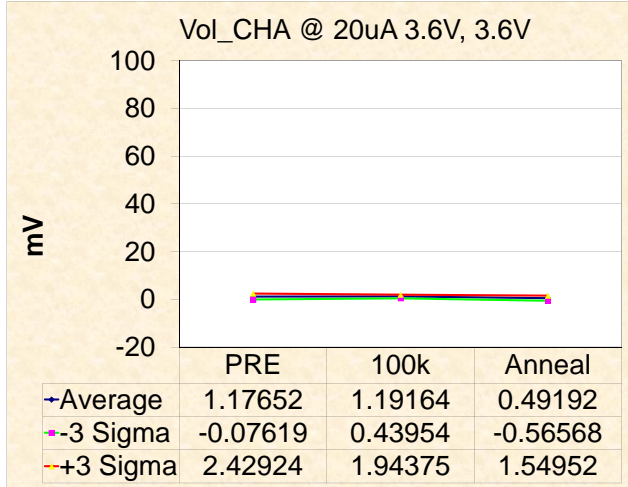
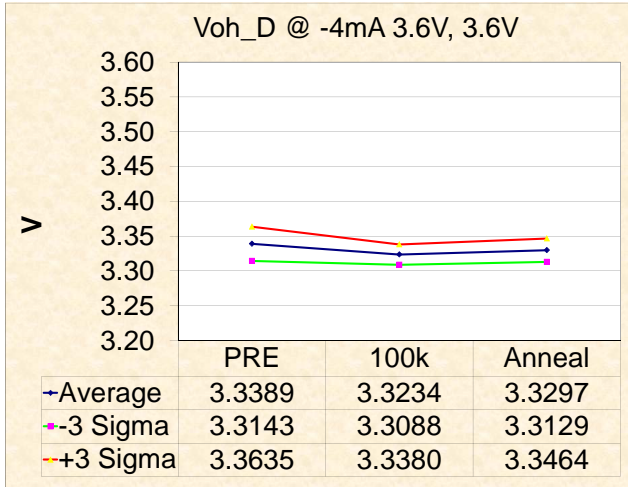
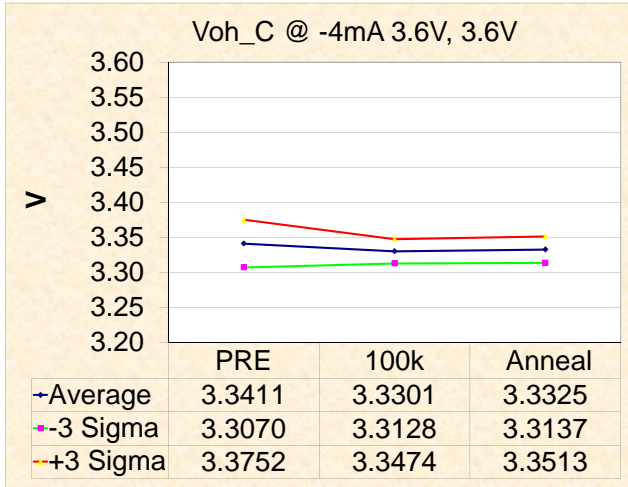
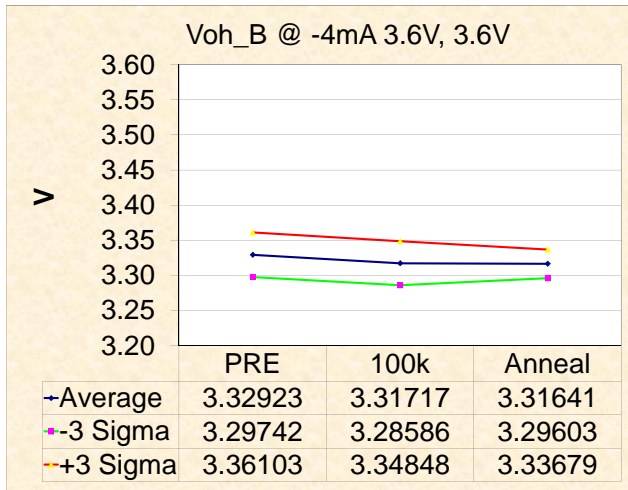
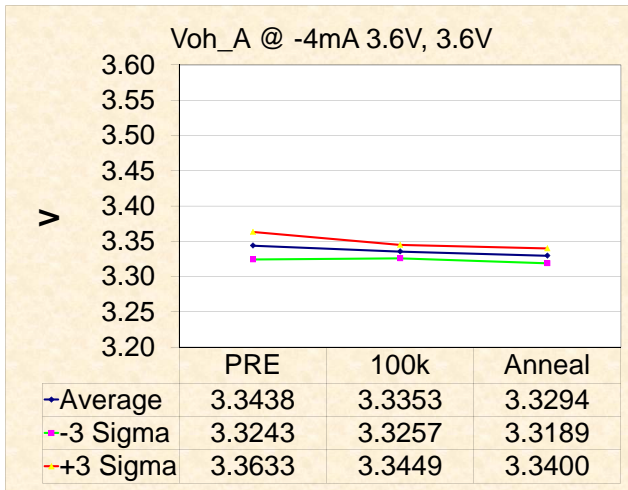


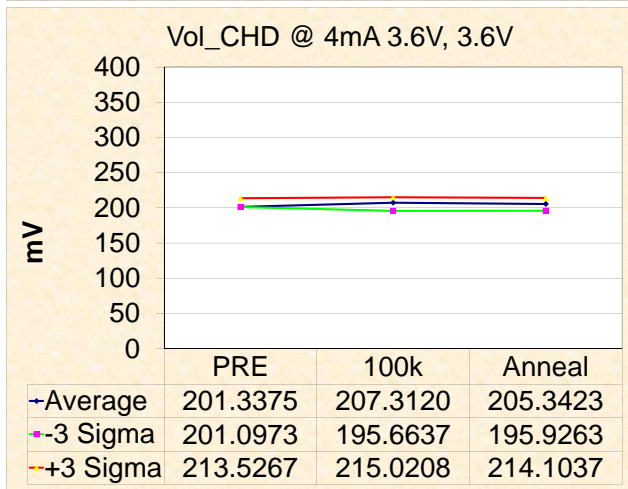
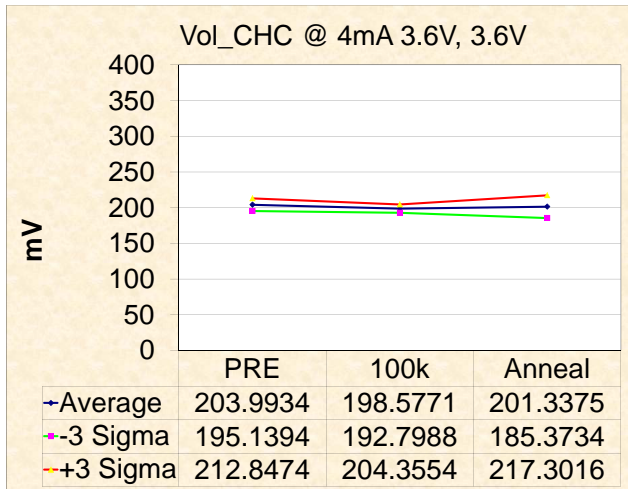
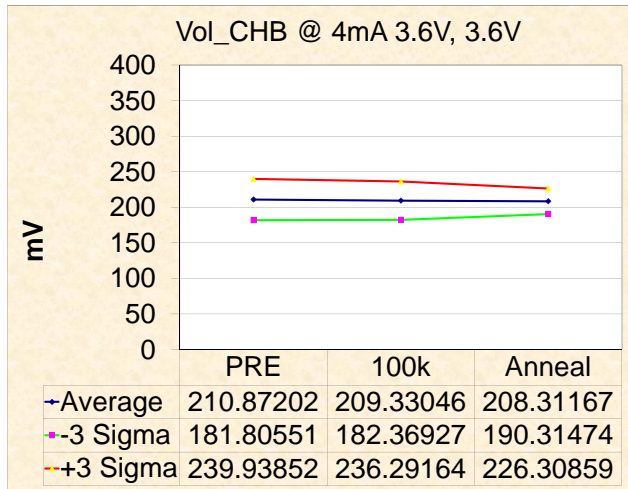
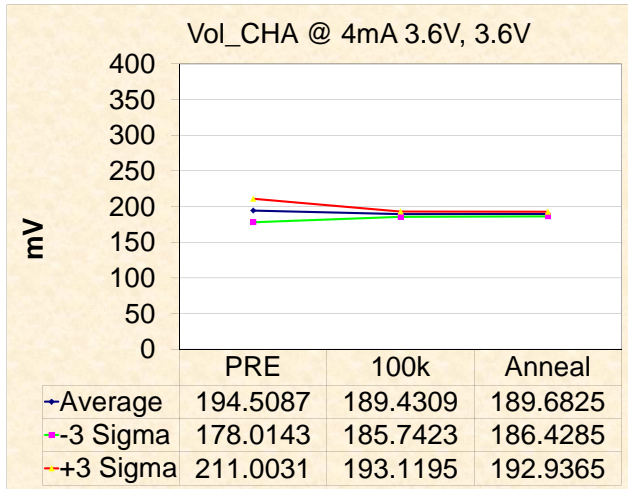
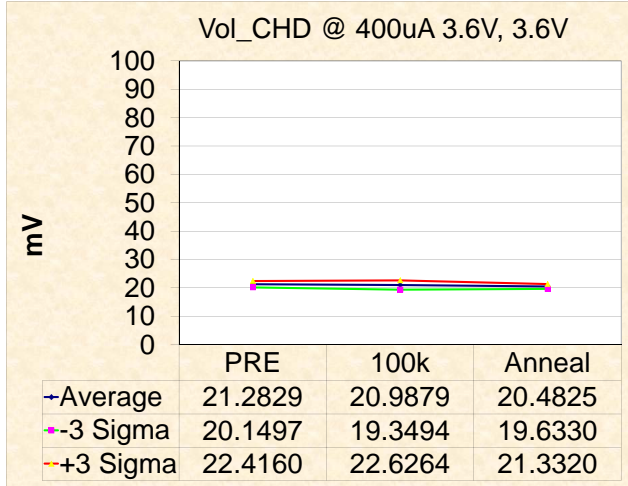
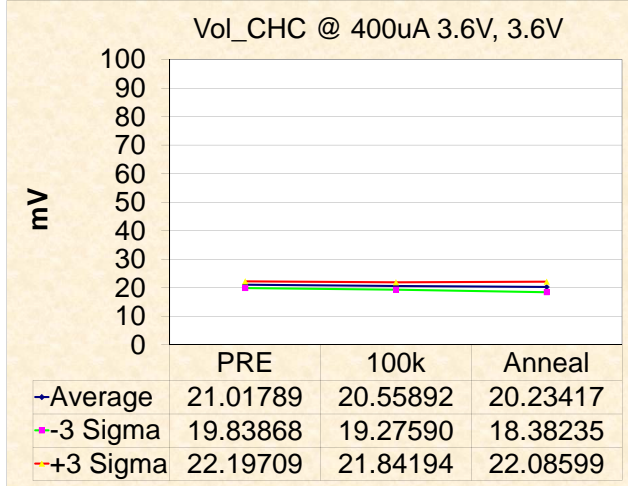
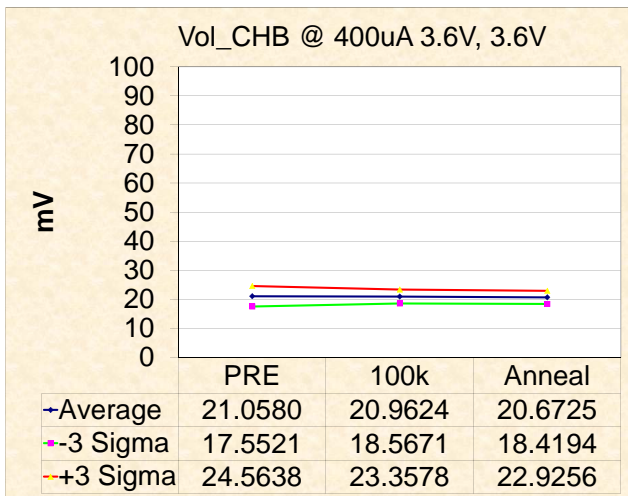
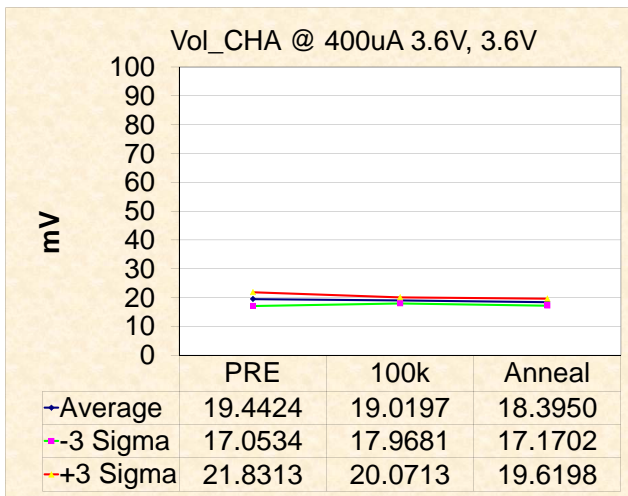
	PRE	100k	Anneal
+Average	3.59688	3.59370	3.59473
-3 Sigma	3.59229	3.59048	3.59305
+3 Sigma	3.60148	3.59691	3.59640

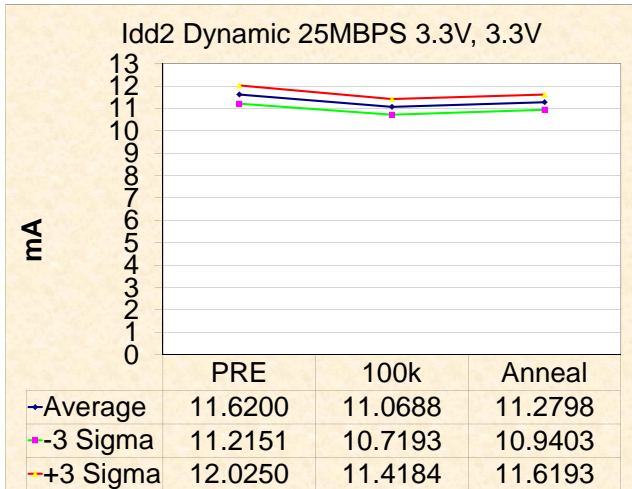
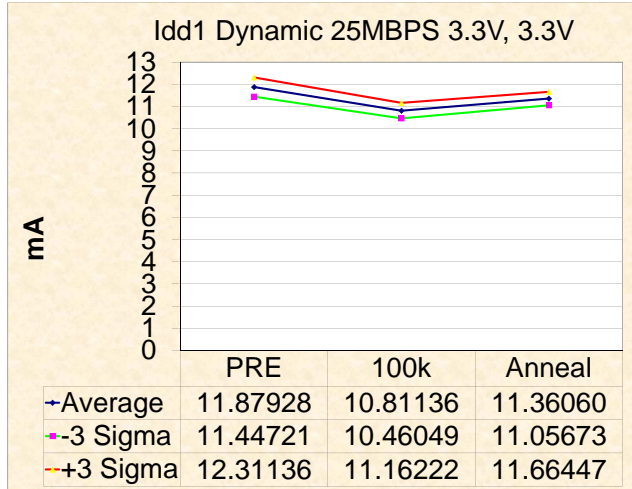
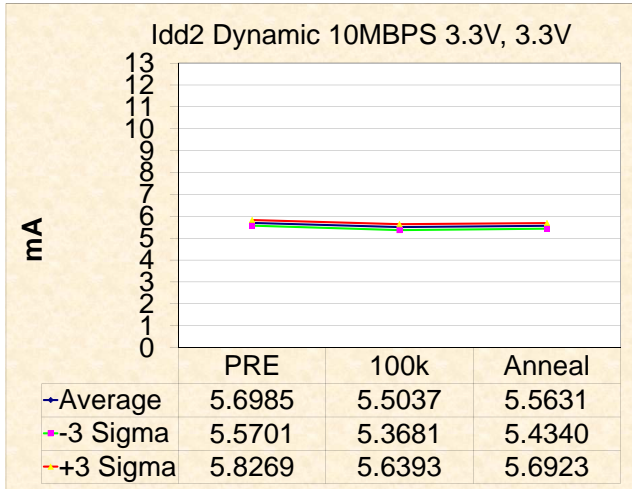
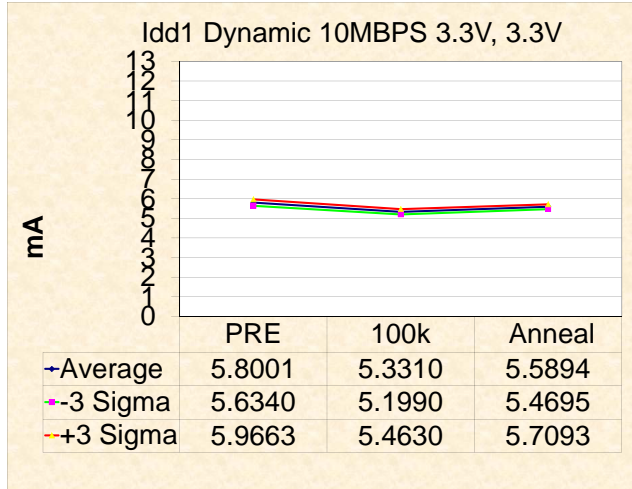
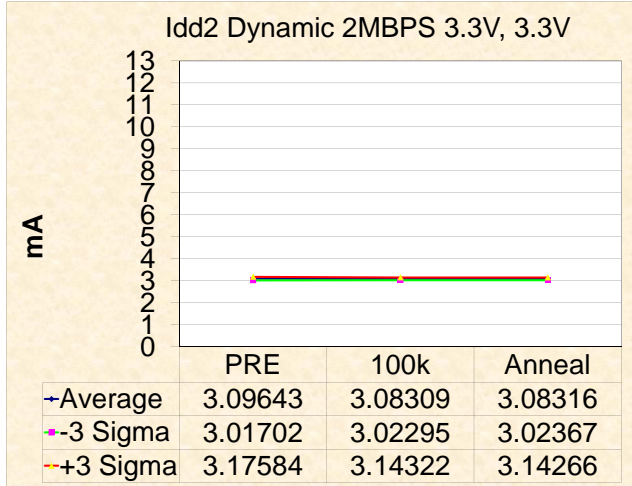
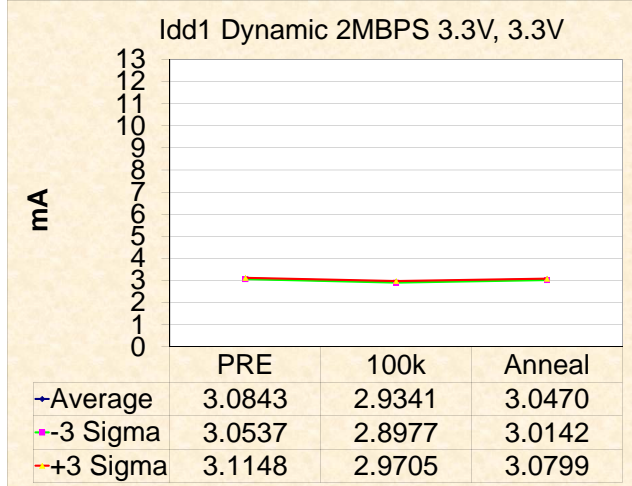
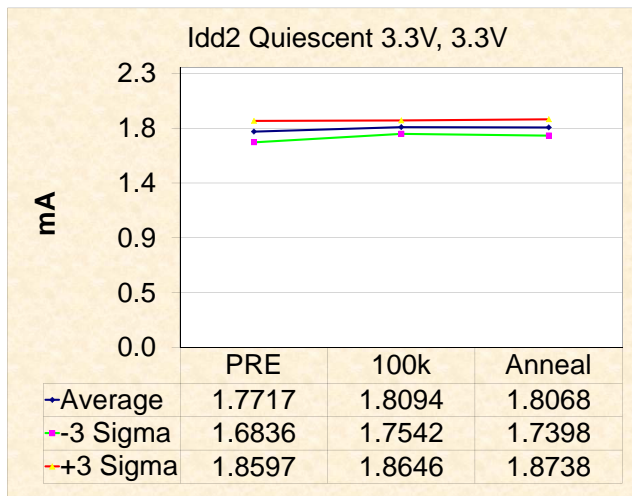
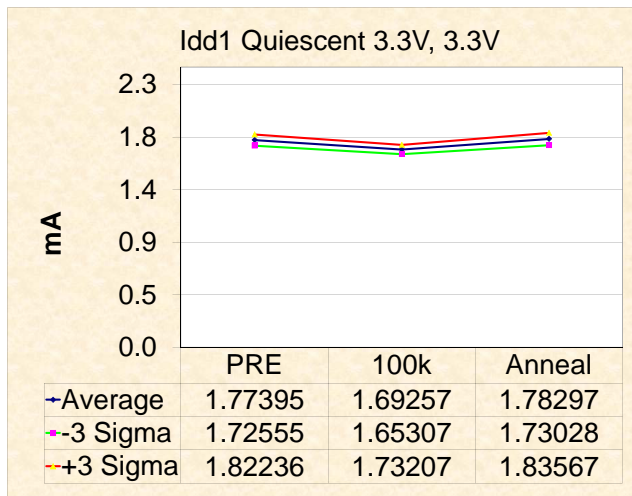
Voh_D @ -20uA 3.6V, 3.6V

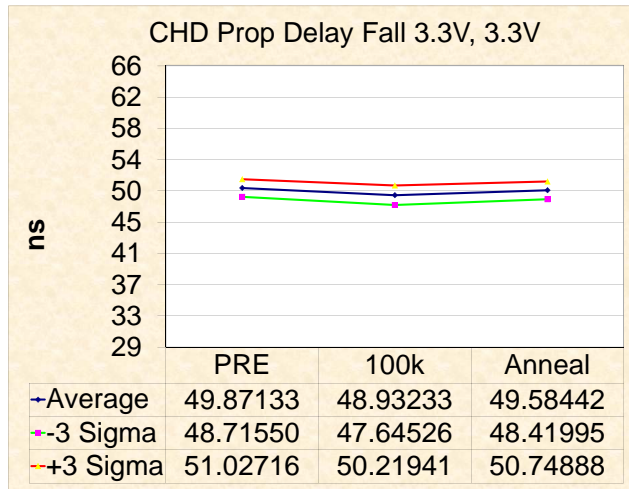
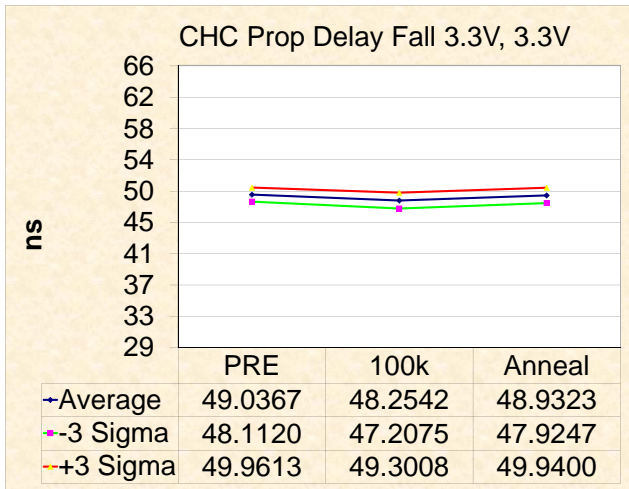
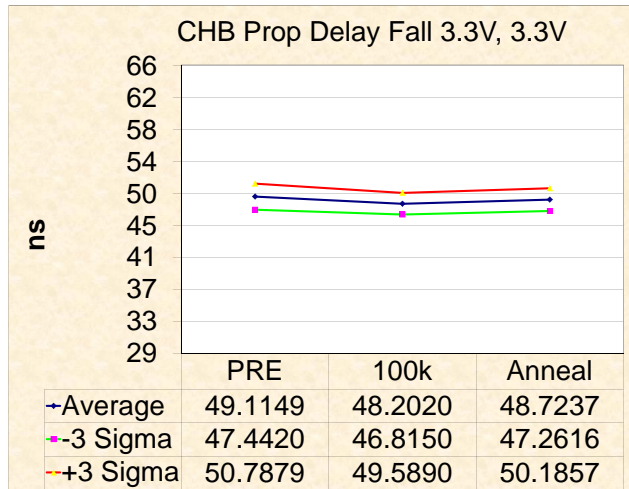
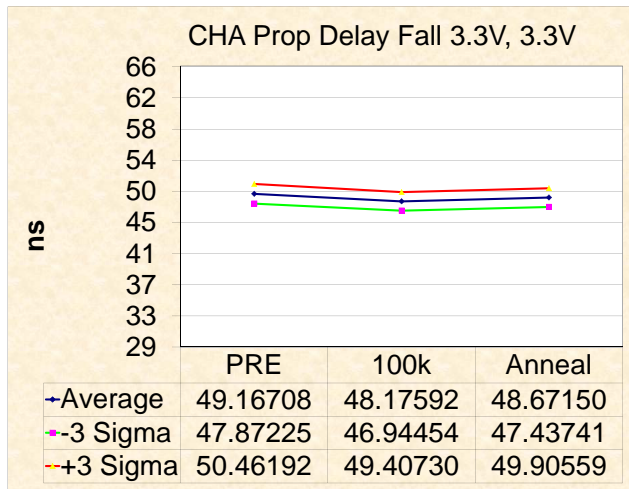
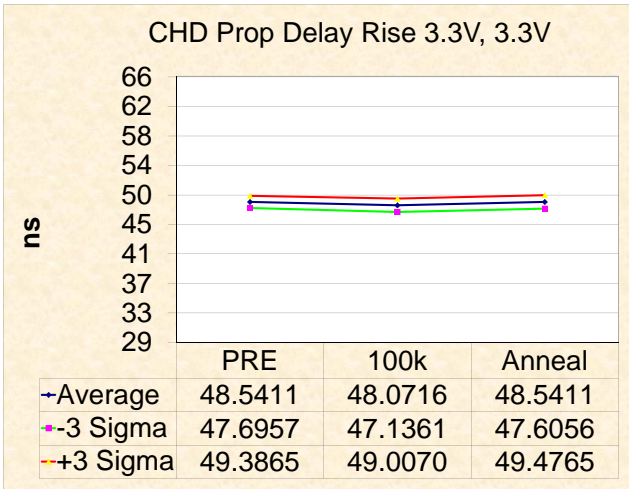
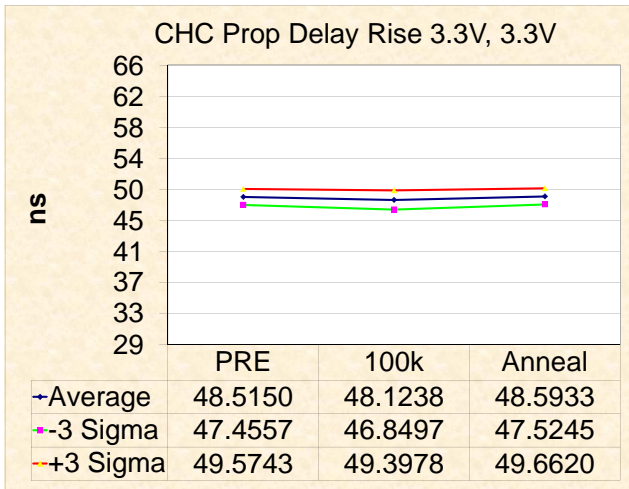
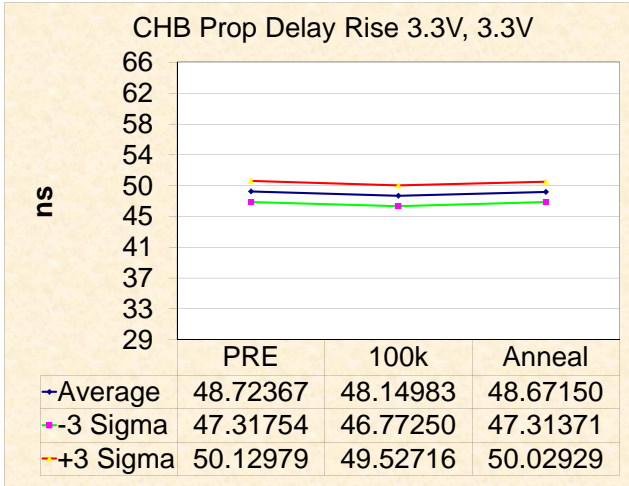
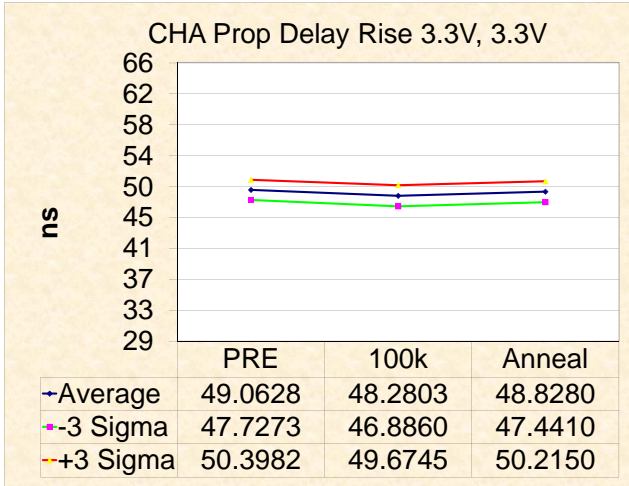


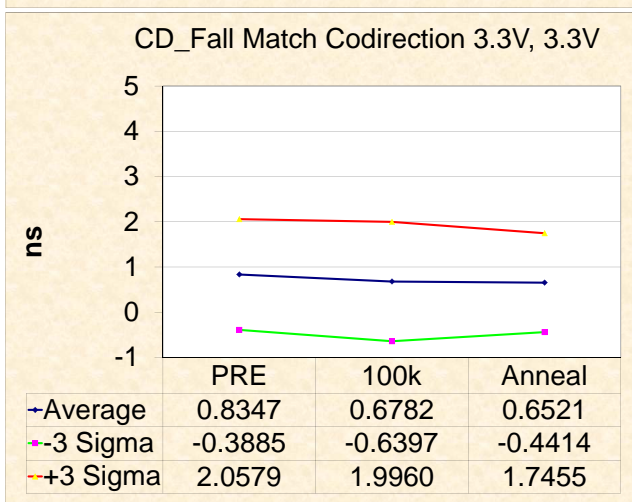
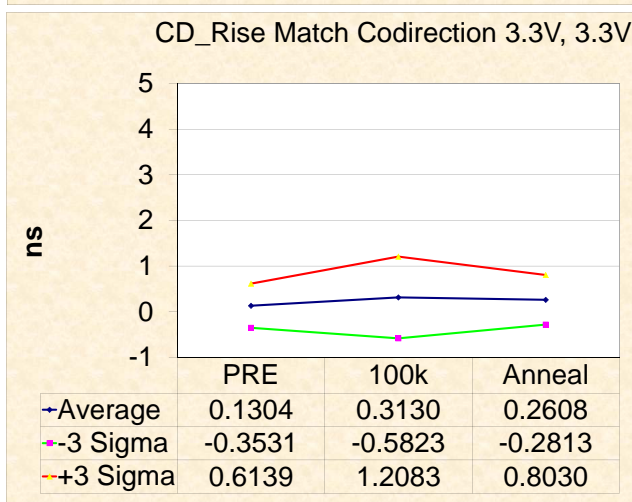
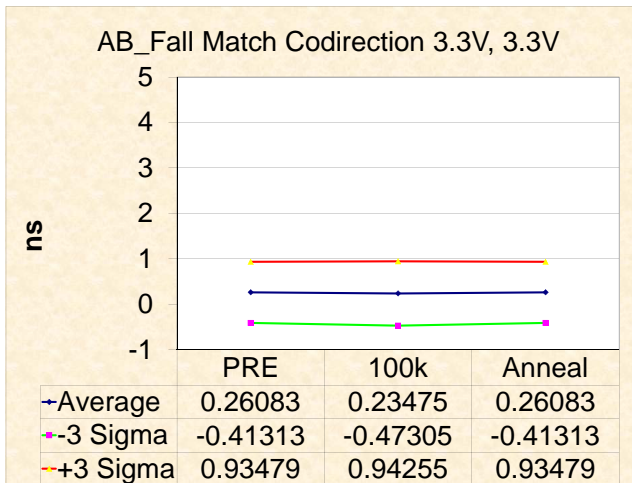
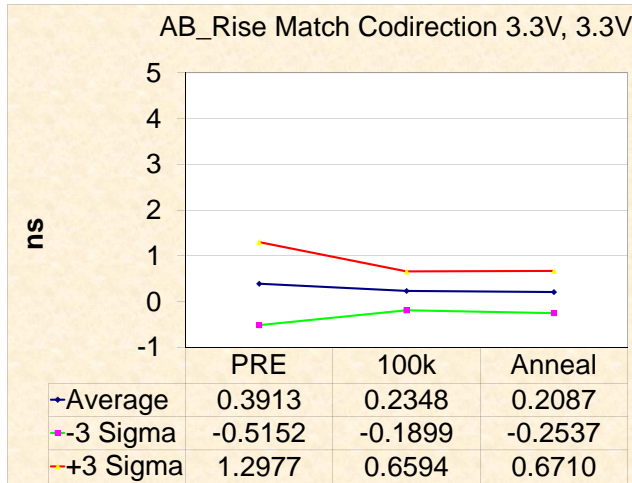
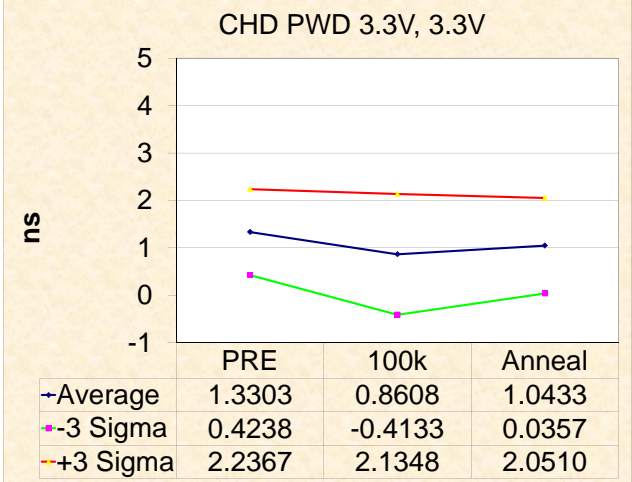
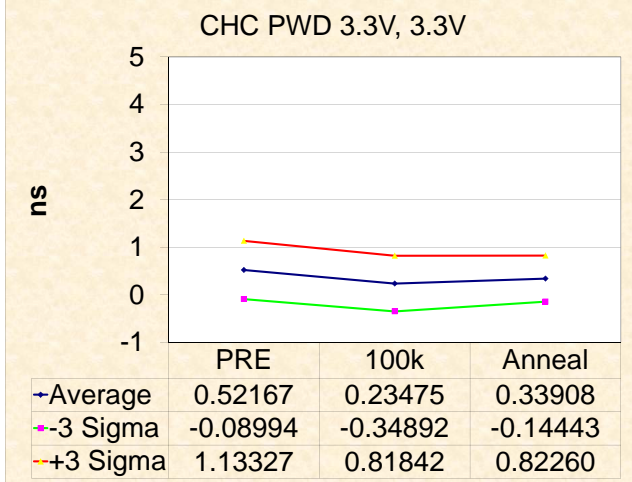
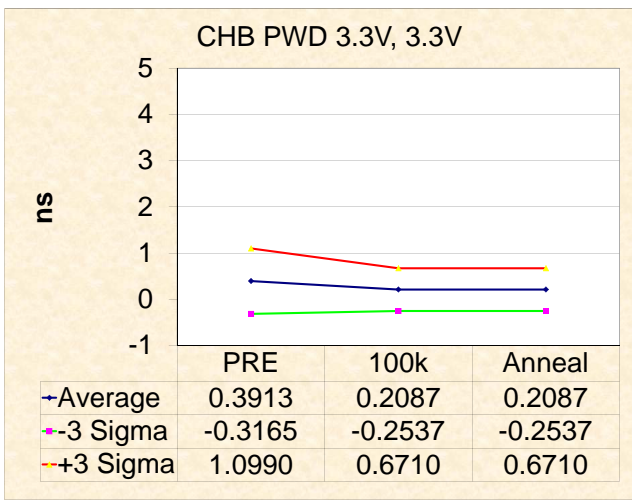
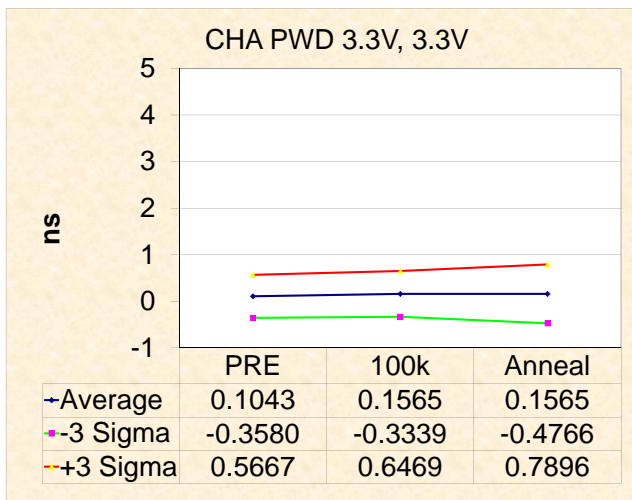
	PRE	100k	Anneal
+Average	3.5968	3.5928	3.5944
-3 Sigma	3.5923	3.5894	3.5927
+3 Sigma	3.6012	3.5961	3.5962



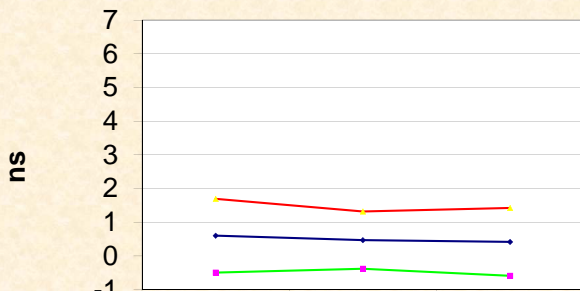






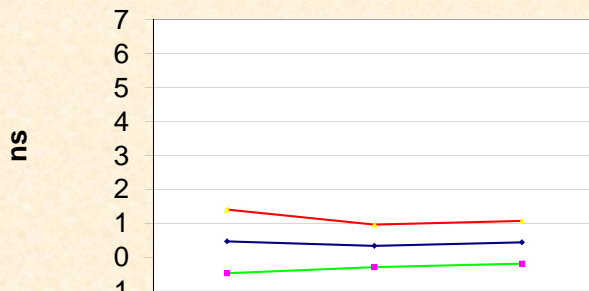


AB_C Rise Match Opposing 3.3V, 3.3V



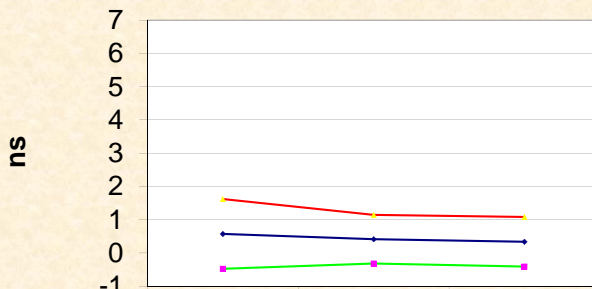
	PRE	100k	Anneal
Average	0.59992	0.46950	0.41733
-3 Sigma	-0.49355	-0.37986	-0.59030
+3 Sigma	1.69338	1.31886	1.42496

AB_C Fall Match Opposing 3.3V, 3.3V



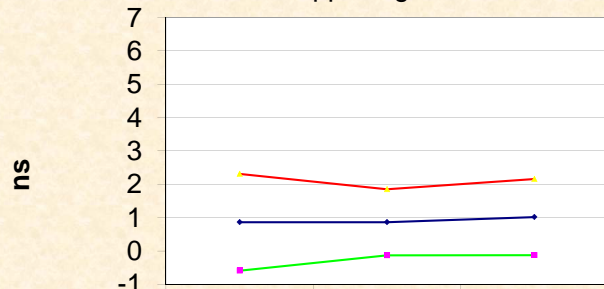
	PRE	100k	Anneal
Average	0.4695	0.3391	0.4434
-3 Sigma	-0.4695	-0.2887	-0.1844
+3 Sigma	1.4085	0.9669	1.0712

AB_D Rise Match Opposing 3.3V, 3.3V



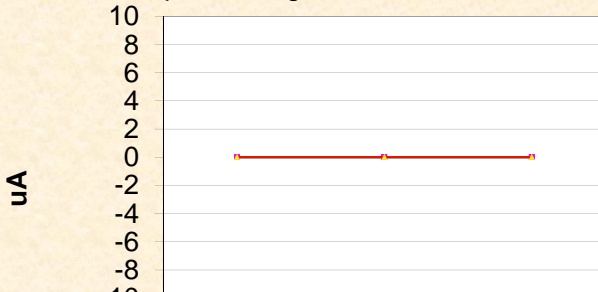
	PRE	100k	Anneal
Average	0.5738	0.4173	0.3391
-3 Sigma	-0.4728	-0.3137	-0.4055
+3 Sigma	1.6205	1.1483	1.0837

AB_D Fall Match Opposing 3.3V, 3.3V



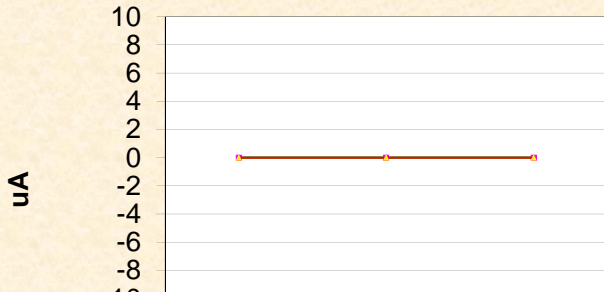
	PRE	100k	Anneal
Average	0.86075	0.86075	1.01725
-3 Sigma	-0.58980	-0.13017	-0.12404
+3 Sigma	2.31130	1.85167	2.15854

ChA Input Leakage @ 0V 3.3V, 3.3V



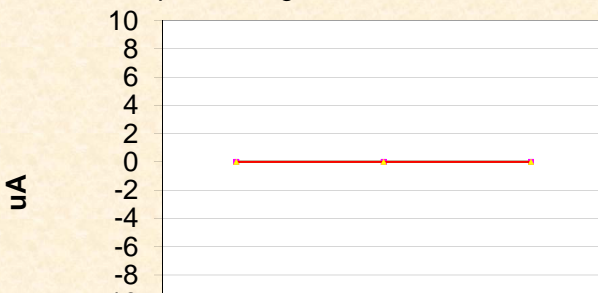
	PRE	100k	Anneal
Average	-0.0002	-0.0005	0.0001
-3 Sigma	-0.0028	-0.0026	-0.0019
+3 Sigma	0.0024	0.0016	0.0022

ChB Input Leakage @ 0V 3.3V, 3.3V



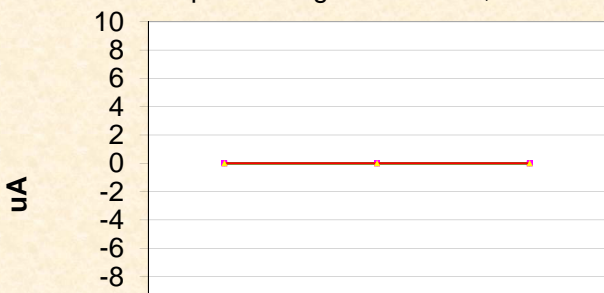
	PRE	100k	Anneal
Average	0.0001	-0.0002	-0.0005
-3 Sigma	-0.0017	-0.0020	-0.0024
+3 Sigma	0.0019	0.0015	0.0013

ChC Input Leakage @ 0V 3.3V, 3.3V



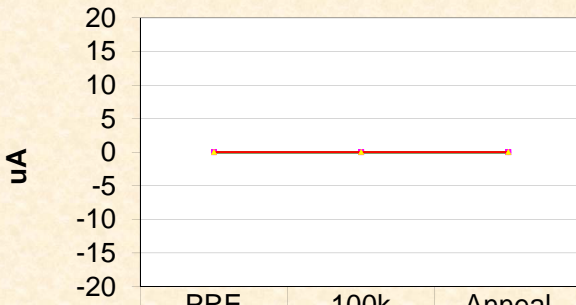
	PRE	100k	Anneal
Average	0.00007	-0.00046	-0.00020
-3 Sigma	-0.00268	-0.00271	-0.00321
+3 Sigma	0.00282	0.00179	0.00280

ChD Input Leakage @ 0V 3.3V, 3.3V



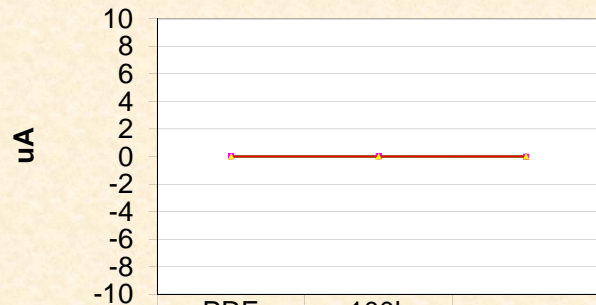
	PRE	100k	Anneal
Average	0.0000	-0.0007	0.0000
-3 Sigma	-0.0026	-0.0023	-0.0024
+3 Sigma	0.0026	0.0009	0.0024

ChA Input Leakage @ Vdd1 3.3V, 3.3V



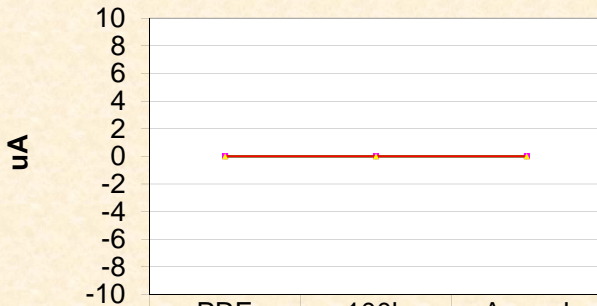
	PRE	100k	Anneal
Average	-0.0002	-0.0005	0.0001
-3 Sigma	-0.0028	-0.0026	-0.0019
+3 Sigma	0.0024	0.0016	0.0022

ChB Input Leakage @ Vdd1 3.3V, 3.3V



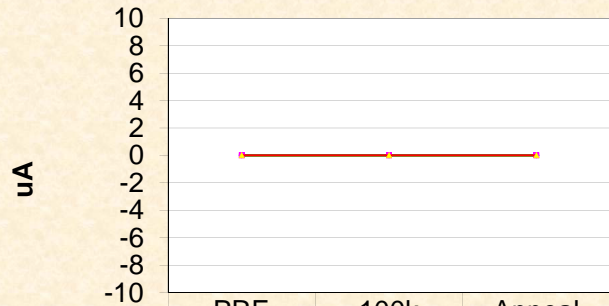
	PRE	100k	Anneal
Average	0.00115	0.00121	0.00124
-3 Sigma	-0.00043	-0.00069	-0.00169
+3 Sigma	0.00274	0.00312	0.00417

ChC Input Leakage @ Vdd2 3.3V, 3.3V



	PRE	100k	Anneal
Average	0.0002	0.0004	0.0001
-3 Sigma	-0.0024	-0.0012	-0.0026
+3 Sigma	0.0028	0.0021	0.0029

ChD Input Leakage @ Vdd2 3.3V, 3.3V



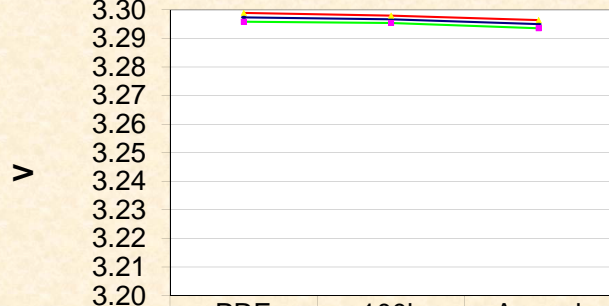
	PRE	100k	Anneal
Average	0.0004	0.0007	0.0003
-3 Sigma	-0.0025	-0.0016	-0.0027
+3 Sigma	0.0032	0.0030	0.0033

Voh_A @ -20uA 3.3V, 3.3V



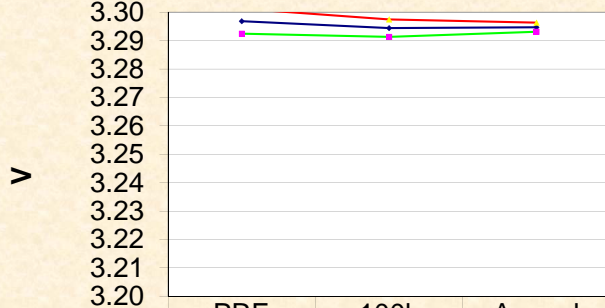
	PRE	100k	Anneal
Average	3.29756	3.29676	3.29269
-3 Sigma	3.29602	3.29520	3.29125
+3 Sigma	3.29910	3.29831	3.29412

Voh_B @ -20uA 3.3V, 3.3V



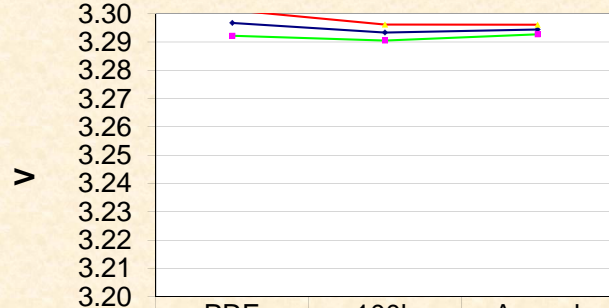
	PRE	100k	Anneal
Average	3.2974	3.2967	3.2950
-3 Sigma	3.2959	3.2954	3.2936
+3 Sigma	3.2989	3.2980	3.2964

Voh_C @ -20uA 3.3V, 3.3V



	PRE	100k	Anneal
Average	3.2968	3.2944	3.2947
-3 Sigma	3.2925	3.2913	3.2931
+3 Sigma	3.3012	3.2974	3.2963

Voh_D @ -20uA 3.3V, 3.3V



	PRE	100k	Anneal
Average	3.29671	3.29334	3.29441
-3 Sigma	3.29220	3.29052	3.29271
+3 Sigma	3.30121	3.29616	3.29611

