

# High Resolution 16- and 18-Bit Digital-to-Analog Converters

## AC1136/1138

### FEATURES

DAC1138 18-Bit Resolution and Accuracy ( $38\mu$ V, 1 Part in 262,144) Nonlinearity 1/2LSB max (DAC1138K) Excellent Stability Settling to 1/2LSB (0.0002%) in  $10\mu$ s Hermetically-Sealed Semiconductors

#### DAC1136

16-Bit Resolution and Accuracy (152μV, 1 Part in 65,536) Low Cost

Nonlinearity 1/2LSB max (DAC1136K, L) Settling to 1/2LSB max (0.0008%) in  $6\mu$ s

DEGLITCHER IV Eliminates DAC Glitches

Available on DAC1136/1138 Card Mounted Assembly

#### GENERAL DESCRIPTION

The DAC1136/1138 are complete self-contained current or voltage output modular digital to-analog converters with resolutions and accuracies of 16 and 18 bits.

The DAC1136/1138 combine precision current sources with state-of-the-art steering switches to produce a very linear output. Inputs to these converters are compatible with TTL levels. The converters have a current output of -2mA full scale. A voltage output can be obtained by connecting the internal amplifier to the current output by means of jumpers. By using additional jumpers, the user can select any one of the following output ranges: 0 to +5V, 0 to +10V, ±5V, or ±10V.

The DAC1136/1138 are available on Card-Mounted Assemblies. In this configuration, selectable options include: input codes, output amplifiers, and a high speed transient-suppressing Deglitcher Module, Deglitcher IV.

#### WHERE TO USE HIGH RESOLUTION DACS

The DAC1136/1138 deliver exceptional accuracy for a broad range of display, test and instrumentation applications. The DAC1136, with a resolution of 16 bits or 1 part in 65,536, and the DAC1138 with a resolution of 18 bits or 1 part in 262,144 are ideally suited for applications requiring wide dynamic range measurement and control. Applications include data acquisition systems, high resolution CRT displays, automatic semiconductor testing, photo-typesetting, frequency synthesis and nuclear reactor control.



CERTIFICATE OF CALIBRATION Each DAC1138 has been calibrated with equipment and methods that are traceable to the National Bureau of Standards (NBS). A Certificate of Performance is sent with each



Figure 1. Block Diagram and Pin Designations

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices.

 P.O.
 Box
 280;
 Norwood,
 Massachusetts
 02062
 U.S.A.

 Tel:
 617/329-4700
 Twx:
 710/394-6577

 Telex:
 924491
 Cables:
 ANALOG NORWOODMASS

# **SPECIFICATIONS** (typical @ + 25°C, rated power supplies unless otherwise noted; specifications for mounting card with amplifier/deglitcher options same as module unless otherwise noted)

	J DAC	1136 Module K	L	DAC1136 Amplifier/ Deglitcher IV (Internal AD542K)	on Mounting Card with Deglitcher Options. Low Drift 234L   High Speed 44K w/wo Deglitcher   w/wo Deglitcher	(
RESOLUTION, BITS		16	*		· · · · · · · · · · · · · · · · · · ·	
ACCURACY Integral Nonlinearity Differential Nonlinearity Gain and Offset Error (Externally Adjustable)	± 1LSB max ± 1LSB max	± 1/2LSB max ± 1/2LSB max	$\pm 1/2$ LSB max $\pm 1/2$ LSB max	Gain, offset provid	and glitch–nulling adjustments ded on the mounting card.	
ANALOG OUTPUT Unipolar Mode Bipolar Mode Voltage Output Range (Pin Selectable)	0 to +	- 2mA to 0mA - 1mA to + 1mA 5V, 0 to + 10V, ±5	A V, ±10V			-
DIGITAL INPUTS	Т	TL/CMOS; See Fig	gure 2			-
INPUT CODES Unipolar Mode Bipolar Mode	Compl Compleme	ementary Binary (Co ntary Offset Binary (	OMP BIN) (COMP OBIN)	BIN, COMP B C SIGN PLUS MAG B	IN, 2's COMP, COMP 2's COMP DBIN, COMP OBIN IN, COMP SIGN PLUS MAG BIN	
STROBE INPUT		None		One standard triggered,	d series 74LS load, leading-edge pulse width 100ns minimum.	
DYNAMIC CHARASTERISTICS Setting Time to 1/2LSB Current Full Scale Step LSB Step Voltage Uncoolar (10VStep) Bipolar (20V Step) LSB Step Slew Rate		8μs 6μs 90μs 250μs 8μs 1Vμs		V 80μs 90μs 8μs 2V/μs	'oltage Output, Only 'oltage Output, Only 45μs 25μs 60μs 30μs 8μs 8μs 6V/μs 20V/μs	. (
TEMPERATURE COEFFICIENTS (ppm of FSR/°C) <sup>1</sup> Integral Nonlinearity Differential Nonlinearity Gain (Excluding V <sub>REF</sub> ) Offset Unipolar Mode Bipolar Mode	±1 ±5	± ± ±5 ±0.5 ±5	±1.5 max ±1.5 max ±3 max			
$\begin{array}{l} STABILITY, LONG TERM \\ (ppm of FSR/1,000 hrs.)^2 \\ Gain (Excluding V_{REF}) \\ Offset \end{array}$		±5 ±6		±1	±0.5 ±25	Γ
NOISE (Include $V_{REF}$ ; Double for Bipolar Mode) Output Current (BW = 100kHz) Output Voltage (BW = 0.1–10Hz) ( $ii$ 0V (A11 1's Code; "ZERO") ( $ii$ 5V (MSB = 0 Code; "Half Scale") ( $ii$ 10V (A11 0's Code; "Full Scale") Output Voltage (BW = 100kHz)		0.5nA rms 4µV pk-pk 6µV pk-pk 9µV pk-pk 30µV rms		20µV rms	40µ.V rms 35µ.V rms	]
$\label{eq:VOLTAGE COMPLIANCE (Amplifier Offset, E_{OS}) \\ Max E_{OS} Allowed for Rated Accuracy \\ Initial E_{OS} (Factory Adj.) \\ E_{OS} Drift \\ Current Output (pin 69) \\ Voltage Protection \\ Source Resistance \\ Unipolar Mode \\ Bipolar Mode \\ Source Capacitance \\ \end{tabular}$	via	$\pm 2mV max$ $\pm 100\mu V$ $\pm 10\mu V/°C$ Internal Schottky I >33kΩ >5kΩ 150nF	Diodes	±50μV ±5μV/°C	$\begin{array}{c c} \pm 20\mu V \\ \pm 0.1\mu V/^{\circ}C \end{array} \\ \begin{array}{c c} \pm 100\mu V \\ \pm 15\mu V/^{\circ}C \end{array}$	(
$\label{eq:REFERENCE VOLTAGE (V_{REF})} \\ \begin{tabular}{l} Voltage (Z_{OUT} \approx 2001) \\ Noise (BW = 0.1 - 10 Hz) \\ Tempco \end{tabular}$	+ 6.000	V (Maximum Error 3µV pk-pk 5ppm/°C	, ±0.024V)			
POWER SUPPLY REQUIREMENTS <sup>3</sup> + 5V dc, ± 5% ± 15V dc, ± 5%		9mA ± 30mA		± 38mA	95mA ± 37mA + 40mA	
POWER SUPPLY REJECTION (±15V dc) Gain or Offset vs. FSR Differential Nonlinearity		80dB ± 1/4LSB per Volt 4	AV <sub>S</sub>	100dB	100dB 75dB	
ENVIRONMENTAL Operating Temperature Storage Temperature Humidity	5%	0 to + 70°C - 55°C to + 85°C 6 to 95%, Nonconde	nsing	– 55°C to + 85°C	- 55°C to + 85°C - 55°C to + 85°C	(

NOTES <sup>1</sup>Maximum temperature coefficients guaranteed from 15°C to 35°C, typical from 0 to +70°C. <sup>2</sup>Recommended DNL calibration check: 6 months. <sup>3</sup>Recommended Power Supply: Analog Devices Model 923.

Specifications subject to change without notice.

(

(

(

(

C

**SPECIFICATIONS** (typical  $@ + 25^{\circ}$ C, rated power supplies unless otherwise noted; specifications for mounting card with amplifier/deglitcher options same as module unless otherwise noted)

of controllo	ampimer/uegittchei	options same as mot	Jule unless oulerwise in	
			DAC1138 on 1	Mounting Card with
			Amplifier/Deg	litcher Options.
	DAC1138 Mod	ule	Deglitcher IV	Low Drift 234L
	J	K	(Internal AD542K)	w/wo Deglitcher
RESOLUTION, BITS		18	1	
ACCURACY				
Integral Nonlinearity	± 1LSB max	± 1/2LSB max		
Differential Nonlinearity	± 1LSB max	± 1/2LSB max		
Gain and Offset Error (Externally Adjustable)			Gain, offset and glitch	-nulling adjustments
			provided on the	mounting card.
ANALOGOUTPUT				
Unipolar Mode		2mA to 0mA		
Bipolar Mode	- 1	$mA to \pm 1mA$		
Voltage Output Range (Pin Selectable)	0 to + 5V 0 t	$t_0 + 10V + 5V + 10V$		
	010+51,0	10 + 10V, ± 5V, ± 10V	1	
DIGITALINPUTS	TTL/C	MOS; See Figure 2		
INPUT CODES				
Unipolar Mode	Complementa	ary Binary (COMP BIN)	BIN, COMP BIN, 2's	COMP, COMP 2's COMP
Bipolar Mode	Complementary C	Offset Binary (COMPOBIN)	OBIN, C	OMPOBIN
			SIGN PLUS MAG BIN, CO.	MP SIGN PLUS MAG BIN
STROBEINPUT		None	One standard series	41 Sload leading-edge
ornobbinitor		Hone	triggered, pulse wi	dth 100ns minimum
DVI TUDOULD LOTEDIOTION			l inggereu, puise wi	
DUNAMIC HARACTERISTICS				
Setting Lime to 1/2LSB				
Evil Seek Stat		10		
I SP State		10µs	Voltage	utput, Only
Voltage	$\sim$	oµs	Voltage C	utput, Only
Unipoler (10V Ster)	$  \frown \rangle$	175.0.8	80	15
Rimular (20V Step)		140us	00µs	40µs
LSB Ste		1845	1846	18
Slew Rate	N > /	2W/us	21/100	6V/uc
		2 4/ μ.5	2 1/ μs	[
TEMPERATURE COEFFICIENTS				
(ppm of FSR/°C)	$\sim$ // /			
Integral Nonlinearity	$\checkmark$	±0.3		
Differential Nonlinearity		±0.4		
Gain (Excluding V <sub>REF</sub> )		±0.8		
University Made				
Ripolar Mode		±0.5		
Bipolar Mode		±1		
STABILITY, LONG TERM				
(ppm of FSR/1,000 hrs.)'				
Gain (Excluding V <sub>REF</sub> )		± 2		
Offset		±2	± 1	±0.5
NOISE (Include V <sub>REF</sub> ; Double for				
Bipolar Mode)				
Output Current (BW = 100kHz)		0.5nA rms	Voltage Ou	tput, Only
Output Voltage (BW = $0.1-10$ Hz)				
(a 0V (A11 1's Code; "ZERO")		ŧμV pk-pk		
(a 5V(MSB = 0 Code; "Half Scale")		óμV pk-pk		
(a 10V (A11 0's Code; "Full Scale")	9	θμV pk-pk		1
Output Voltage (BW = $100 \text{kHz}$ )		30µV rms	20µV rms	40µV rms
VOLTAGE COMPLIANCE (Amplifier				In the contrast of the second s
Offset, E <sub>OS</sub> )				
Max E <sub>OS</sub> Allowed for	1			
Rated Accuracy	±	200µV max		
Initial E <sub>OS</sub> (Factory Adj.)	-	±100µV	± 50µ V	± 20µ.V
E <sub>os</sub> Drift		± 10µV/°C	$\pm 5\mu V/^{\circ}C$	$\pm 0.1 \mu V/^{\circ}C$
Current Output (pin 69)				=0.10.10
Voltage Protection	via Intern	al Schottky Diodes		
Source Resistance		· · · · · · · · · · · · · · · · · · ·	1	
Unipolar Mode		>33kΩ		
Bipolar Mode		$>5k\Omega$	1	
Source Capacitance		150pF		
REFERENCE VOLTAGE (VREE)	1			
Voltage ( $Z_{OUT} \approx 200\Omega$ )	+ 6.000V (Ma	ximum Error. +0.024W		
Noise $(BW = 0.1 - 10Hz)$	1 0.000 (Ma	uV nk-nk		
Tempco		Sppm/°C		
POWER SUPPLY REQUIREMENTS2		-FF		
+ 5V dc. + 5%	1	0 4		
$\pm 15V dc. \pm 5\%$		9mA + 20m A		95mA
		± 30mA	± 38pnA	± 37mA
POWER SUPPLY REJECTION (±15V dc)				
Gain or Offset vs. FSR		80dB	100dB	75dB
Differential Nonlinearity	± 1/4L	.SB per Volt $\Delta V_S$		
ENVIRONMENTAL				
Operating Temperature		0 to + 70°C		
Storage I emperature	- 5	5°C to +85°C	- 55°C to + 85°C	- 55°C to + 85°C
numidity	5% to 95	%, Noncondensing		
NOTES		and the second s		

NOTES: <sup>1</sup>Recommended DNL calibration check: 6 months. <sup>2</sup>Recommended Power Supply Analog Devices: Model 923. Specifications subject to change without notice.

### Characteristic Curves\*











Settling Time (Voltage Output) vs. %-of-Full-Scale-Error for LSB Steps (Essentially Independent of Amplifier Used). With Deglitcher IV, the LSB Step at the Major Carry Settles as Fast as the Typical LSB Step, Following the 11µs Hold Period.

#### INPUT CONSIDERATIONS

The DAC1136/1138 may be driven by TTL or CMOS as shown in Figure 2. Note that the TTL input is shown with inputs for both a direct "totem pole" TTL gate and open collector (or "pull-up") configurations.



2a. TTL Totem Pole<sup>1</sup>

2b. Switch or Relay Input<sup>2</sup>

BIT

DAC

0



#### 2c. CMOS Input

 FOR TTL WITH OPEN COLLECTOR, DO NOT USE EXTERNAL PULL-UP. CONVERTERS HAVE INTERNAL 10kΩ PULL-UP ON EACH INPUT TO 3.8V.
 USE SPST SWITCH OR RELAY TO GROUND, WHEN SWITCH IS OPEN, THE INTERNAL 10kΩ WILL PULL INPUT UP TO 3.8V.

#### Figure 2. Input Connections

OUTPUT CONNECTIONS AND GUARDING The DAC1136/1138 output connections for various voltage ranges are shown in Figure 3.

Since an LSB is only  $38\mu V$  (at 10 volts full scale for the DAC1138), care must be exercised to properly guard the current output of the converter from leakage current. Any connection made to the DAC's current output (pin 69) should be guarded. Suggested printed circuit board guarding is shown in Figure 3. The optional Care Mounted Assemblies of the DAC1136/1138 have been earefully designed for



Figure 3. Output Voltage Connections and Suggested PCB Guarding (Unipolar and Bipolar)

\*NOTE: All curves typical at rated supply voltage. F.S. = Full Scale

#### GAIN AND OFFSET ADJUSTMENTS

(

The gain and offset adjustments are made with external potentiometers which the user supplies. With the appropriate digital inputs applied, these potentiometers are adjusted until the desired output voltage is obtained. The proper connections for offset and gain are shown in Figure 4. The voltmeter used to measure the output should be capable of stable resolution of 1/4LSB in the region of zero and full scale. Because of the interaction between offset and gain adjustments, the adjustment procedure described below should be carefully followed. Offset adjustment affects gain, but gain adjustment does not affect offset.







For unipolar mode, apply a digital input of all "1's" (complementary binary code for zero output) and adjust the offset potentiometer until a 0.00000V output is obtained (see Table I). Once the appropriate offset adjustment has been made, apply a digital input of all "0's". Adjust the gain potentiometer until the plus full scale output is obtained (see Table I).

For bipolar mode, apply a digital input of all "1's" (complementary offset binary code for minus full scale) and adjust the offset potentiometer for the proper minus full scale output voltage (see Table I). Once the appropriate minus full scale adjustment has been made, apply a digital input of all "0's". Adjust the gain potentiometer until the plus full scale output shown below is obtained.

RANGE	IDEAL OUTPUT				
		DAC1138	DAC1136		
Unipolar:	All 111	All 0	00		
$0V \rightarrow +10V$	0.00000V	+9.999962V	+9.999848V		
$0V \rightarrow +5V$	0.00000V	+4.999981V	+4.999924V		
Bipolar:					
$-10V \rightarrow +10V$	-10.00000V	+9.999934V	+9.999695V		
$-5V \rightarrow +5V$	-5.00000V	+4.999962V	+4.999848V		
			-		
To adjust:	Adjust ZERO pot	Adjust G	AIN pot		

#### DIFFERENTIAL LINEARITY ADJUSTMENT

Each DAC1136/1138 has been factory calibrated and tested to achieve the performance indicated in the electrical specifications. Before attempting recalibration, it is imperative that the circuit be checked to confirm that all previously described precaution's have been taken to insure proper application at the 16- or 18-bit level. Basically, the DAC is trimmed by comparing a bit to the sum of all lower bits, and adjusting, if necessary, for a one LSB positive difference. The top 4 major carries, i.e., MSB minus the sum of bits 2-through-the-LSB, down through bit 4 minus the sum of bits 5-through-the-LSB, can be trimmed using the procedure outlined below. A differential voltmeter capable of 100µV Full Scale should be connected to VOUT of the DAC. This will resolve an LSB which at 18 bits is 38µV (10V range). A Fluke 895A or equivalent is recommended.

#### 1. Bit 4 Trim

- a. Set bit inputs to 11110 . . . 0.
- b. Read the output voltage by nulling the voltmeter.
- c. Set bit inputs to 11101 . . . 1.
- d. Read voltage by nulling voltmeter. This reading should be equal to that of step 1b plus 1LSB. Adjust bit 4 if required (see B4, Figure 6).

Set bit inputs to 1110 a. . 0. b. Read output voltage by nulling the voltmeter

- Set inputs to 1101 .... 1. d. Read voltage by nulling the voltmeter. This reading should be equal to that of step 2b plus 11 SB Adju bit 3 if required (see B3, Figure
- 3. Bit 2 Trim

3 Trim

Bit

- a. Set bit inputs to 110 . . . 0.
- b. Read output voltage by nulling the voltmeter.
- c. Set bit inputs to 101 . . . 1.
- d. Read voltage by nulling voltmeter. This reading should be equal to that of step 3b plus 1LSB. Adjust bit 2 if required (see B2, Figure 6),
- 4. Bit 1 (MSB) Trim
  - a. Set bit switches to 100 .... 0.
  - b. Read output voltage by nulling the voltmeter.
  - c. Set bit switches to 011 . . . 1.
  - d. Read voltage by nulling voltmeter. This reading should be equal to that of step 4b plus 1LSB. Adjust bit 1 (MSB) if required (see MSB, Figure 6).

If insufficient range exists on any adjustment, then a separate adjustment for the weight of bits 5-through-the-LSB (see Sum  $B5 \rightarrow LSB$ , Figure 6) should be performed. This condition will probably not occur on bit 2, 3 and 4 but might occur on the MSB. If adjustment of the sum of bits 5-through-the-LSB is made, the trim procedure for all bits should be repeated. Obviously, since the procedure affects the weight of individual bits, it affects the overall gain of the DAC. The final step should be adjustment of gain (user supplied adjustment external to module, or pot at edge of mounting card).

Table I. Full Scale Output

**OUTLINE DIMENSIONS AND PIN DESIGNATIONS** 

Dimensions shown in inches and (mm),

in the unipolar mode). When an external reference is used, pin

52, (the output of the internal reference) is left open.



Figure 6. Card-Mounted Assembly. Dimensions shown in inches and (mm).

Codi Semiconductor manufactures a reference module called

Certavolt<sup>1</sup> with a 10 volt output accurate to 0.001%. This out-

**CARD-MOUNTED ASSEMBLY JUMPER DESIGNATIONS** The output voltage range, reference source, amplifier and deglitcher configurations are programmed at the factory by means of jumpers, resistors, and capacitors (see ordering guide for details). The mounting card can be programmed by the user, if necessary, as shown below.



NOTES:

<sup>1</sup> With a 234L amplifier install C7  $(0.01\mu F, 10\%, ceramic capacitor)$ . With a 44K amplifier use a variable resistor (typ value  $\approx 429\Omega$ , 0.1W, 1%) to adjust the output voltage for a  $\pm 100\mu V$  reading as measured between pins 69 and 34 of the DAC (this step sets voltage compliance); install this value resistor (R13 position).

<sup>2</sup> With Deglitcher IV remove R20 (100 $\Omega$ ) and replace the resistor with a jumper.

<sup>3</sup> With Deglitcher IV and a 234L amplifier remove C6 (6.8pF Capacitor) and install: C7 (0.01 $\mu$ F, 10%, ceramic capacitor), C18 (100pF, 10%, ceramic capacitor), C17 (1000pF, 10%, polystyrene capacitor) and replace R20 (100 $\Omega$ ) with a jumper. With Deglitcher IV and a 44K amplifier perform the operation described in Note 1, remove C6 (6.8pF capacitor) and install: C18 (100pF, 10%, ceramic capacitor), C17 (1000pF, 10% polystyrene capacitor) and replace R20 (100 $\Omega$ ) with a jumper.



PIN	FUNCTION
1	ANALOG SENSE LOW
2	ANALOG SOURCE LOW
3	NC
4	ANALOG SOURCE HIGH
5	ANALOG SENSE HIGH
6	ANALOG REF. IN/OUT
Α	ANALOG REF. IN/OUT
В	ANALOG SENSE HIGH
С	ANALOG SOURCE HIGH
D	NC
E	ANALOG SOURCE LOW
F	ANALOG SENSE LOW

251-06-30-160 (SUPPLIED).

J1 MATES WITH CINCH P.N. 251-22-30-160 (SUPPLIED). <sup>1</sup> DAC1138 ONLY

Mounting Card Connector Designations

#### DEGLITCHER IV

The Deglitcher IV is a precision high-speed, high-isolation sample-and-hold circuit which eliminates the glitches that occur whenever a DAC is dithered through a major carry. Such momentary transients can be of concern in applications such as high-resolution CRT beam positioning, where glitch-free code transitions are often required for optimum display quality and legibility. Oscilloscope photographs in Figures 7a and 7b below show the output of a DAC1136 being dithered up and down through the major carry, between codes 1000000000000000 and 011111111111111. In Figure 7b, the Deglitcher IV is turned on virtually elminating the glitches and allowing the  $152\mu$ V LSB step to be clearly seen.



Figure 7b. Same Major-Carry Dither with Deglitcher IV (BW = 1MHz), Vertical Scale, 200µV/Division

The Deglitcher IV utilizes a proprietary sampling technique which isolates the output amplifier during the critical  $10\mu s$ period immediately following a code change. The only discernible difference in DAC performance when used with Deglitcher IV is a delay of approximately  $11\mu s$  after the strobe goes HI before the (deglitched) DAC output voltage starts slewing toward the new value.

#### GLITCH ADJUSTMENT

There are two "Glitch" adjustment potentiometers, accessible on the Card-Mounted Assembly. The adjustment on the card permits nulling of any Track-to-Hold offset, whereas the adjustment internal to the Deglitcher IV allows for precise nulling of the Hold-to-Track transient. Because of the near-infinite attenuation of the actual DAC current glitches, no current-glitch transient is visible on the output. For this reason, it is easiest to null the 2 Deglitcher adjustments while strobing the Card with a static digital input.

#### INPUT OPTIONS

The Card-Mounted Assembly contains input registers. The input code ordered by the user is set at the factory by means of various jumpers in the logic circuitry. See ordering guide for details.

Since the Card-Mounted Assembly contains input registers, the card requires a strobe pulse circuit. Strobe characteristics of input registers are:

- 1.1 Strobe Pulse: One Std. series 74LS load, Leading-Edge-Triggered. Positive pulse should remain HI for > 100ns.
- 2. The digital input code can be changed at any time up to and including that instant when the strobe command goes HI.
- R 12 If = is R SENSE HI 3. The actual transfer of the input code to the DAC will occur 0→2mA C SOURCE HI EXT  $\approx$  3µs after the strobe command; during this 3µs the digital AMP SOURCE LO IL. input code to the card assembly should not be changed, in order to prevent the possible coupling of logic noise into DAC SENSE LO the DAC output. At  $t_0 + 3\mu s$ , the deglitcher is automatically 1=0 A ANALOG for the following  $\approx 8\mu$ s. Thus there will be a delay of rabled ±15V COMM. COMM SUPPLY  $\approx 11 \mu s$  before the deglitched output starts slewing to the new -Vs SINGLE OUTPUT CIRCUIT WITH value. Actual data transfer to the DAC automatically occurs OPTIONAL AMPLIFIERS TIE POINT ±5V 43.1µs at to SUPPLY DAC ON COMM DIGITAL PUT OPTIONS MOUNTING CARD COMM Card-Mounted Assembly for the DAS1136/1138 allo for several user-selectable output configurations: TE VOI TAGE DROP BET WEEN SOURCE LO AND SENSE LO MUST OBSERVE CURRENT LIMITS FOR RATED ACCURACY. DE COMPLIANC 1. Internal Output Amplifier i MO side odule EME CANNOT BE USED WITH INTERNAL IS CONNECTION TH 2. Analog Devices model 234L; for oise lrift at PLIFIER OF T TERNAL TO TI ITCHER IV cations  $(2\mu V, \pm 0.1\mu V/^{\circ}C)$ . our-Terminal Dutput Con Figu nections e **ORDERING GUIDE** WHEN ORDERING THE DAC1136 OR DAC1138, ORDER EITHER: 1. Module only: DAC11361 DAC11381

DAC1136K DAC1136L

DAC1138K



16 BITS

**17 BITS** 

18 BITS

16 BITS

18 BITS

18 BITS

6 L

8 J

8

ODE	OUTPUT
1	INTERNAL
2	44K <sup>1</sup>
3	234L
4	DEGLITCHER IV
5	DEGLITCHER IV <sup>1</sup> AND 44K
6	DEGLITCHER IV AND 234L



- 3. Analog Devices model 44K; available only with DAC1136; recommended only for high speed or high current applications.
- 4. Deglitcher IV with self-contained precision BI-FET output amplifier (AD542K).
- 5. Deglitcher IV with model 234L output amplifier.
- 6. Deglitcher IV with model 44K output amplifier (recommended with DAC1136 only).

When using an external amplifier, a four terminal output connection can be utilized on the Card-Mounted Assembly in order to allow for compensation of connector contact resistance.