

Precision Monolithics Inc.

FEATURES

- Complete Includes Reference and Op Amp
- Bipolar Output $\pm 10V$
- Sign-Magnitude Coding
- No Bipolar Offset Adjustment Required
- 10-Bit Linearity Maintained over Full Temperature
- Multiplying Operation
- Fast $1.5\mu s$ Settling Time
- Monotonicity Guaranteed
- Reliable 100% Burned-In
- Available in Die Form

ORDERING INFORMATION†

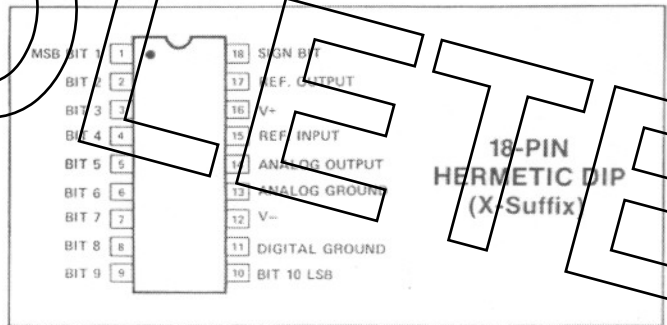
TEMPCO (ppm/°C)	NL RMS	COMMERCIAL TEMPERATURE
± 40	± 0.05	DAC210EX
± 60	± 0.05	DAC210FX
± 30 Typ	± 0.10	DAC210GX

† Burn-in is available on commercial and industrial temperature range parts in CerDIP, plastic DIP, and TO-can packages. For ordering information, see 1990/91 Data Book, Section 2.

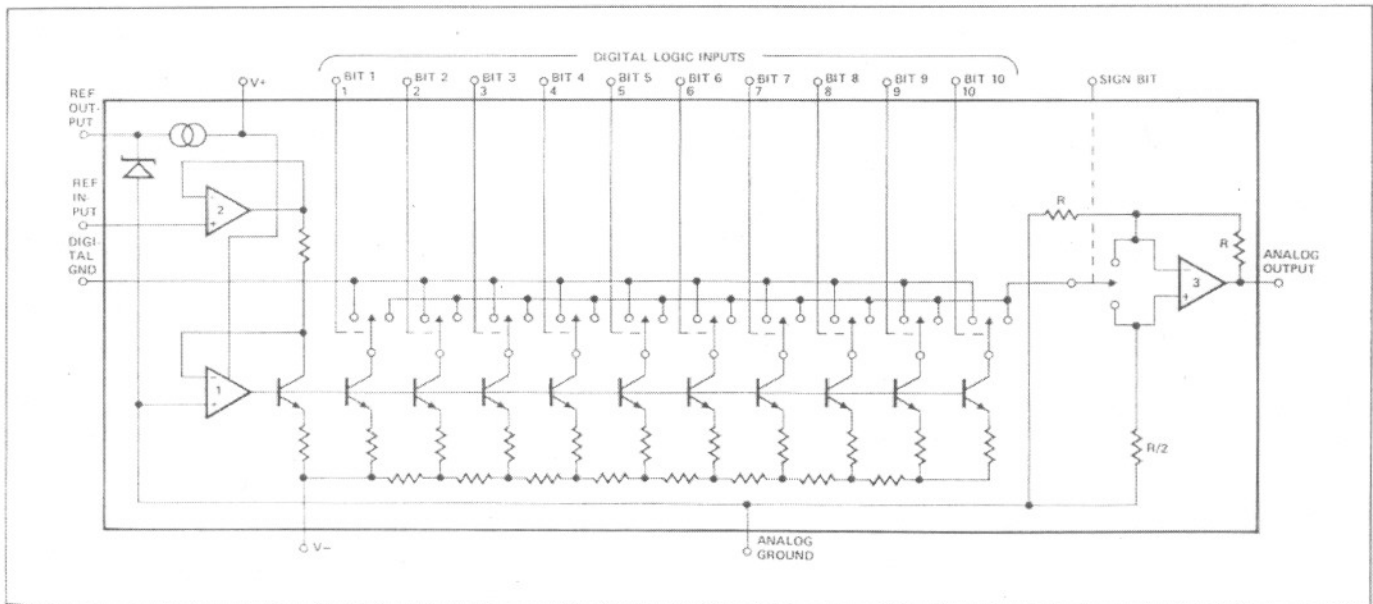
GENERAL DESCRIPTION

The DAC-210 is a complete, monolithic 10-bit plus sign DAC with a $\pm 10V$ output. A precision voltage reference, a logic controlled polarity switch and output amplifier are included. Linearity, monotonicity, and full-scale temperature coefficient are guaranteed over the full operating temperature range. Ease of application is achieved by the total D/A system specs given for nonlinearity and zero-scale offset. System specs eliminate the complex error budget analysis required by less "complete" DACs. Sign-magnitude coding minimizes the "major-carry" zero-code errors inherent in offset coding schemes.

PIN CONNECTION



SIMPLIFIED SCHEMATIC



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DIGITAL-TO-ANALOG CONVERTERS

ELECTRICAL CHARACTERISTICS at $V_S = \pm 15V$, $0^\circ C \leq T_A \leq +70^\circ C$ for E, F and G grades, unless otherwise noted.

PARAMETER	SYMBOL	CONDITIONS	DAC-210E			DAC-210F			DAC-210G			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
Resolution		Including Sign	11	—	—	11	—	—	11	—	—	Bits
Monotonicity			10	—	—	10	—	—	9	—	—	Bits
Nonlinearity	NL	$T_A = 25^\circ C$ $T_A = \text{Full Range}$	—	—	± 0.05	—	—	± 0.05	—	—	± 0.10	%FS
Zero-Scale Offset Voltage	V_{ZS}	$T_A = 25^\circ C$ $T_A = \text{Full Range}$	—	—	± 0.05	—	—	± 0.1	—	—	—	%FS
Bipolar Full Range Voltage Symmetry ($V_{FR+} - V_{FR-} $)	V_{FRS}	$T_A = 25^\circ C$ $T_A = \text{Full Range}$	—	—	40	—	—	60	—	—	80	mV
Zero-Scale Voltage Symmetry ($ V_{ZS+} - V_{ZS-} $)	V_{ZSS}	$T_A = \text{Full Range}$	—	—	1	—	—	1	—	—	2	mV
Gain Tempco	T_C	Internal Reference External Reference	—	—	± 40	—	—	± 60	—	± 30	—	ppm/ $^\circ C$
Output Voltage Range	V_{OR+} V_{OR-}	$R_L = 2k\Omega$	+10.0	—	+1.5	+10.0	—	+11.5	10.0	—	+11.5	V
Differential Nonlinearity	DNL	$T_A = 25^\circ C$	—	—	± 1	—	—	± 1	—	—	± 1	LSB
Settling Time	T_S	(Note 4)	—	1.5	—	—	1.5	—	—	1.5	—	μs
Reference Input Slew Rate	SR_{REF}		—	1.5	—	—	1.5	—	—	1.5	—	V/ μs
Reference Input Impedance	Z_{IN}		—	200	—	—	200	—	—	200	—	M Ω
Reference Input Multiplying Range	IVR_m	For 0.1% Typical Nonlinearity (Note 1)	3	—	10	3	—	10	3	—	10	V
Reference Amplifier Bandwidth	BW		—	1	—	—	1	—	—	1	—	MHz
Reference Output Voltage	V_{REF}	(Note 5)	—	7.6	—	—	7.6	—	—	7.6	—	V
DAC Output Current	I_O	(Note 3)	0	—	5	0	—	5	0	—	5	mA
Reference Output Current	I_{REF}		—	100	—	—	100	—	—	100	—	μA
Output Slew Rate	SR_O		—	10	—	—	10	—	—	10	—	V/ μs
Logic Input Current	I_{IN}	$-5V \leq V_I \leq V+$	—	± 2	± 10	—	± 2	± 10	—	± 2	± 10	μA
Logic "0" Input Voltage	V_{INL}		—	—	0.8	—	—	0.8	—	—	0.8	V
Logic "1" Input Voltage	V_{INH}		2.0	—	—	2.0	—	—	2.0	—	—	V
Power Supply Sensitivity (Note 2)	P_{SS}	$T_A = 25^\circ C$ $T_A = \text{Full Range}$	—	0.015	0.05	—	0.015	0.05	—	0.015	0.1	% V_{FS}/V
Positive Supply Current	$I+$		—	7	9	—	7	9	—	7	9	mA
Negative Supply Current	$I-$		—	-10	-12	—	-10	-12	—	-10	-12	mA

NOTES:

- Guaranteed by design.
- Power Supplies – The DAC-210 will operate within specifications for power supplies ranging from $\pm 12V$ to $\pm 18V$. Power supplies should be bypassed near the package with a 0.1 μF disk capacitor.
- Guaranteed by V_{OR} test, $R_L = 2k\Omega$.
- To within $\pm 5mV$ of final settled value, (± 10 volt output step, $R_L = 2k\Omega$.)
- For applications where long-term stability is critical, an external voltage reference is recommended (See PMI REF01/02).

ABSOLUTE MAXIMUM RATINGS (Note 1)

Operating Temperature Range	DAC-210E, F, G	0°C to +70°C
Junction Temperature (T _j)		-65°C to +150°C
Storage Temperature Range		-65°C to +150°C
V ₊ Supply to Analog Ground		0 to +18V
Analog Ground to Digital Ground		0 to ±0.5V
Logic Inputs to Digital Ground		-5V to (V ₊ - 0.7V)
V ₊ Supply to V ₋ Supply		36V
Internal Reference Output Current		300µA

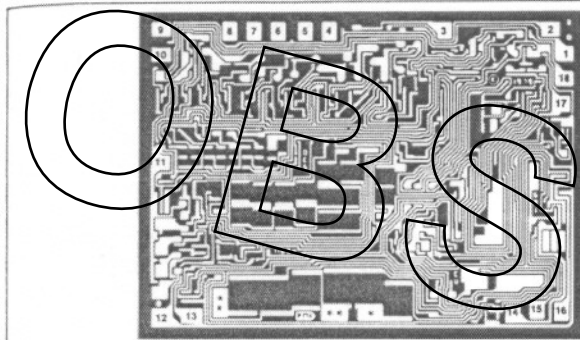
Reference Input Voltage	0 to +10V
Lead Temperature (Soldering, 60 sec)	300°C
Output Short-Circuit Duration	Indefinite (Short-circuit may be ground to either supply)

PACKAGE TYPE	θ _{JA} (Note 2)	θ _{IC}	UNITS
18-Pin Hermetic DIP (X)	79	11	°C/W

NOTES:

1. Absolute maximum ratings apply to both DICE and packaged parts, unless otherwise noted.
2. θ_{JA} is specified for worst case mounting conditions, i.e., θ_{JA} is specified for device in socket for CerDIP package.

DICE CHARACTERISTICS



1. B1 (MSB)
2. B2
3. B3
4. B4
5. B5
6. B6
7. B7
8. B8
9. B9
10. B10 (LSB)
11. DIGITAL GROUND
12. V₋
13. ANALOG GROUND
14. ANALOG OUTPUT
15. REFERENCE INPUT
16. V₊
17. REFERENCE OUTPUT
18. SIGN BIT

NOTE: For 5 volt output option (+5V only) * is connected to analog output. ** is connected to analog ground.

DIE SIZE 0.118 × 0.087 inch, 10,266 sq. mils
(2.997 × 2.210 mm, 6,623 sq. mm)

For additional DICE ordering information refer to 1990/91 Data Book, Section 2.

WAFER TEST LIMITS at V_S = ±15V, +10V full-scale output, T_A = 25°C, unless otherwise noted.

PARAMETER	CONDITIONS	DAC-210N LIMIT	DAC-210G LIMIT	DAC-210GR LIMIT	UNITS
Resolution	Bipolar Output	11	11	11	Bits MAX
	Unipolar Output	10	10	10	
Monotonicity		10	9	8	Bits MIN
Nonlinearity		±0.05	±0.1	±0.2	%FS MAX
Zero-Scale Offset	Sign-Bit High, All Other Inputs Low	±5	±10	±10	mV MAX
Zero-Scale Symmetry	V _{ZS+} - V _{ZS-}	±1	±2	±2	mV MAX
Full-Scale Bipolar Symmetry	±10V Full-Scale	±40	±80	±80	mV MAX
Power Supply Rejection	V _S = ±12V to ±18V	0.05	0.05	0.1	%V _{FS} /V MAX
Power Consumption	I _{OUT} = 0	300	300	300	mW MAX
Logic Input "0"		0.8	0.8	0.8	V MAX
Logic Input "1"		2	2	2	V MIN
Analog Output Voltage (All Bits High)	V ₊ (Sign-Bit High)	11.5 10	11.5 10	11.5 10	V MAX V MIN
	V ₋ (Sign-Bit Low)	-10 -11.5	-10 -11.5	-10 -11.5	V MAX V MIN
Differential Nonlinearity		±1	±1	±1	LSB MAX

NOTE:

Electrical tests are performed at wafer probe to the limits shown. Due to variations in assembly methods and normal yield loss, yield after packaging is not guaranteed for standard product dice. Consult factory to negotiate specifications based on dice lot qualification through sample lot assembly and testing.

TYPICAL ELECTRICAL CHARACTERISTICS at V_S = ±15V and +10V full-scale output, unless otherwise noted.

PARAMETER	SYMBOL	CONDITIONS	DAC-210N TYPICAL	DAC-210G TYPICAL	DAC-210GR TYPICAL	UNITS
Full-Scale Tempco	TCV _{FS}	Internal Reference	15	30	30	ppm/°C
Settling Time (T _A = 25°C)	t _S	To ±1/2 LSB 10 Volt Step	1.5	1.5	1.5	µs
Logic Input Current	I _{IN}	T _A = 25°C	1	1	1	µA

