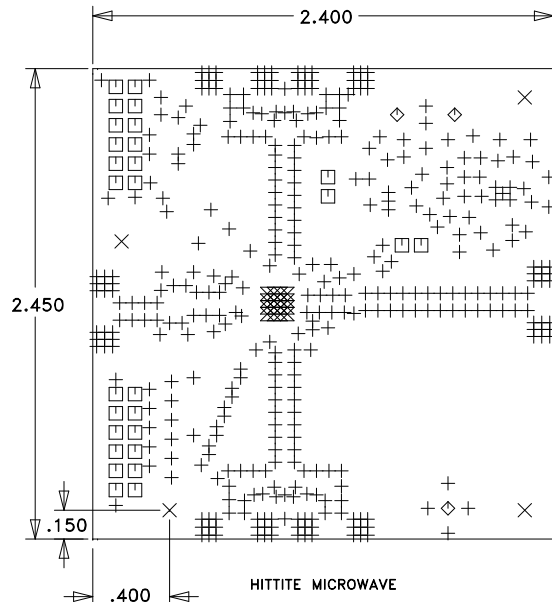
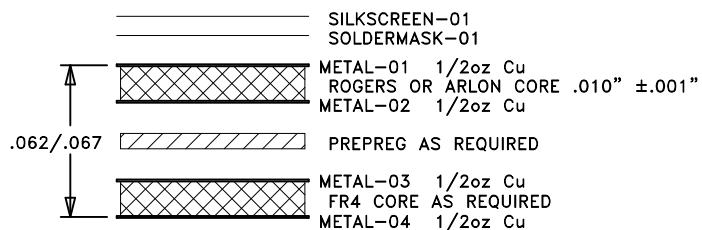


DO NOT SCALE PRINT

HITTITE MICROWAVE
PCB #125087-2
DRILL DRAWING

SIZE	QTY	SYM	PLATED	TOL
10	16	⊗	YES	+/-3
14	397	+	YES	+/-3
40	28	□	YES	+/-3
43	3	◇	YES	+/-3
125	4	×	YES	+/-3



LAYER STACKUP

PROPRIETARY TO HITTITE MICROWAVE CORPORATION

UNLESS OTHERWISE SPECIFIED:	DWN BY:
DIMENSIONS ARE IN INCHES (MM)	M. GIRDVAINIS 3/15/09
DRAWING PRACTICES PER MIL-STD-100	ENGINEER:
	DON YOUNG

TOLERANCES:	
.XX	+/- 0.010
.XXX	+/- 0.005
.XXXX	+/- 0.002
ANGLES	+/- .5 DEG

DWN BY:	M. GIRDVAINIS 3/15/09
ENGINEER:	DON YOUNG

HITTITE MICROWAVE CORPORATION
20 Alpha Road Chelmsford, MA 01824TITLE
PCB, SOLDERED EVAL
HMC795LP5E

SIZE	CODE ID NO.	DRAWING NO.	REV
A	1CN88	125087	2
SCALE:		WT	SHEET 1 of 1

NOTES:

MATERIALS AND TOLERANCES:

1. MATERIAL: MULTILAYER. OVERALL STACKUP AS SHOWN. TYPE ROGER 4350 OR ARLON25FR, HALF OUNCE COPPER BOTH SIDES, TOPSIDE ONLY. FR4 TO BE USED AS FILLER TO MEET CRITICAL OVERALL THICKNESS.
2. FINISH: GOLD PER ASTM B-488 TYPE III CODE A 8-40 MICROINCHES OVER NICKEL PER QQ-N-290, 100 MICROINCHES MINIMUM.
3. APPLY LPI SOLDERMASK TOP SIDE ONLY. COLOR: GREEN.
4. SILKSCREEN TOP SIDE WITH WHITE NON-CONDUCTIVE INK. ENSURE SILKSCREEN DOES NOT COVER COMPONENT PADS - CLIP SILKSCREEN IF NECESSARY.
5. MANUFACTURE PER IPC-600 CLASS 2.
6. ALL HOLES TO BE LOCATED WITHIN $\pm .003$ " OF CENTER OF PAD OR OTHER TRUE POSITION.
7. COPPER PLATE ALL HOLES MINIMUM .001" COPPER WALL THICKNESS.
8. "SIZE" IN DRILL LEGEND IS IN MILS AND REFERS TO FINISHED HOLE SIZE.
9. WARPAGE: $< .008$ " PER LINEAR INCH.
10. FRONT TO BACK REGISTRATION: $\pm .003$ " MAX.

SPECIAL REQUIREMENTS:

11. CRITICAL LINE WIDTH OF $.016$ " $\pm .001$ " ON METAL-01. ADJUST PROCESS TO ACHIEVE WIDTH
- VENDOR NOTES:

12. VENDOR TO COMPARE IPC NET LIST TO GERBER FILES FOR CONTINUITY. ANY DISCREPANCIES MUST BE REPORTED TO HITTITE MICROWAVE.
13. VENDOR MAY ADD E-TEST STAMP TO PCB. VENDOR SHALL NOT ADD NAME, LOGO, DATE CODE, UL LISTING OR ANY OTHER MARKING TO ANY VISIBLE LAYER.
14. BOARDS MUST PASS VISUAL INSPECTION PER IPC-600 CLASS 2.
15. FINISHED BOARDS ARE TO BE BARE BOARD TESTED.