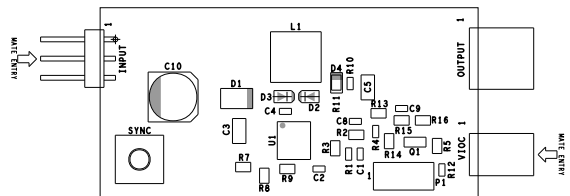


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	21NOV18	J. VIERNES
B	AS PER ECR-086386	07MAY19	J. VIERNES

ASSEMBLY NOTES:


1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5.
- INSTALL SOLDER BLOB USING THE TABLE BELOW (SEE DETAIL A) AND PLACE MARK ON TICK BOX IN THE PRIMARY SIDE



BOM	PARTS	TICK BOX
05-051160-01-b	R17, R18, R19, R23, R24, R25, R29, R30, R31	BUCK
05-051160-02-b	R20, R21, R22, R26, R27, R28	INVERTER

DETAIL A
SCALE FACTOR: 1:4

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div> <div>TITLE ASSEMBLY SCP-LT1956 EVALZ</div>		
TOLERANCES			TEMPLATE ENGINEER		-				
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES		-				
.XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050			HARDWARE SYSTEMS		-				
MATERIAL			TEST ENGINEER		-		SIZE		
			COMPONENT ENGINEER		-				
			TEST PROCESS		-				
			HARDWARE RELEASE		-				
FINISH			DESIGNER C. BAUTISTA		21NOV18		FSCM NO		
			PTD ENGINEER J. VIERNES		21NOV18				
			CHECKER J. VIERNES		21NOV18				
DO NOT SCALE DWG							SCALE 1 / 1		
							DRAWING NUMBER		
							REV		
							B		
							SHEET 1 OF 1		

EVALUATION BOARD