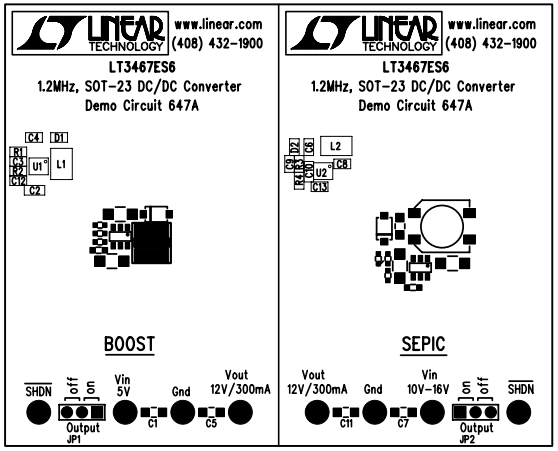
REVISIONS			
REV	DESCRIPTION	APPR	DATE

Silkscreen Top



NOTES :

CAUTION : PLEASE BREAK BOARDS AS A " ONE SET (TWO BOARDS TOGETHER) " LIKE THE DRAWING ABOVE,
DO NOT BREAK BOARD INDIVIDUALLY

INSTALL SHUNTS "OFF" POSITION FOR JP1-JP2

Assembly Top Drawing

APPROVALS		
	INIT	DATE
DRAWN		
CHECK		
DESIGN		
ENGR		
SCALE = NONE		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

LT3467ES6
1.2MHz, SOT-23 DC/DC Converter

SIZE	Demo Circuit	REV.
A	DC647A	
		SHT 1 of 1