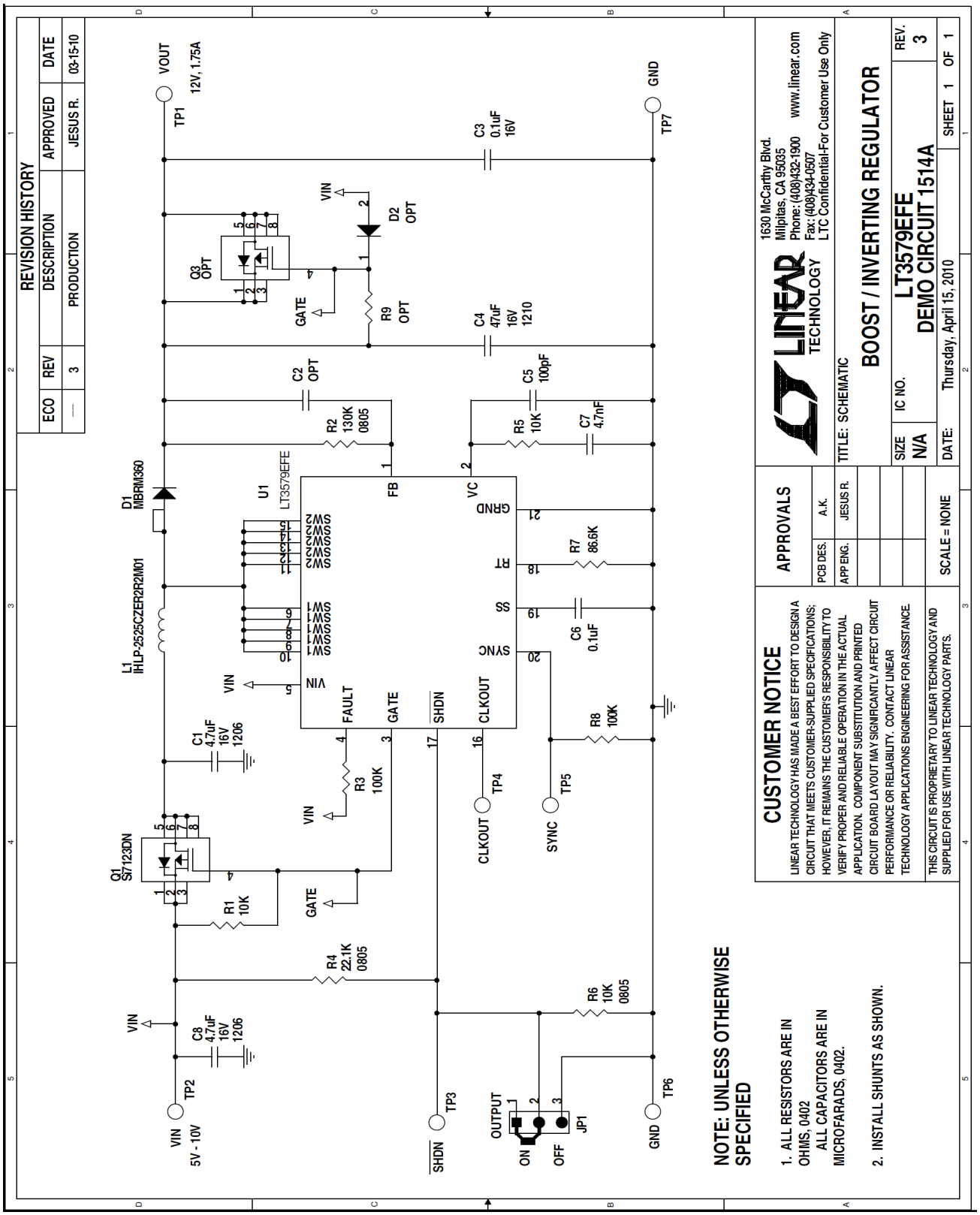
REVISION HISTORY
ECO REV DESCRIPTION APPROVED DATE
— 3 PRODUCTION JESUS R. 03-15-10
VOUT
12V, 1.75A
TP1
VIN
5V - 10V
TP2
SHDN
TP3
OUTPUT
ON
OFF
JP1
GND
TP6
GND
TP7
CLKOUT
TP4
SYNC
TP5
L1
IHLP-2525CZER2R2M01
D1
MBRM360
U1
LT3579EFE
Q1
Si7123DN
Q3
OPT
D2
OPT
R1
10K
R2
130K
0805
R3
100K
R4
22.1K
0805
R5
10K
R6
10K
0805
R7
86.6K
R8
100K
R9
OPT
C1
4.7uF
16V
1206
C2
OPT
C3
0.1uF
16V
C4
47uF
16V
1210
C5
100pF
C6
0.1uF
C7
4.7nF
C8
4.7uF
16V
1206
FB
VC
GRND
RT
SS
SYNC
CLKOUT
FAULT
GATE
SHDN
VIN
SW1
SW2
CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.
APPROVALS
PCB DES. A.K.
APP ENG. JESUS R.
LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only
TITLE: SCHEMATIC
BOOST / INVERTING REGULATOR
SIZE N/A
IC NO. LT3579EFE
DEMO CIRCUIT 1514A
REV. 3
DATE: Thursday, April 15, 2010
SHEET 1 OF 1
SCALE = NONE
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.
NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE IN OHMS, 0402
ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS AS SHOWN.