

TOP SILKSCREEN
 LINEAR TECHNOLOGY
 DC2093A-1 LT4275XIDD LT4321IUF DATE: 06-26-13
 LTPOE++ POWERED DEVICE

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	KAUNG H.	6-26-13

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON TABLE BELOW:

ASSY	SHUNT POSITION
DC2093A-A	JP1 & JP2
DC2093A-B	N/A
DC2093A-C	JP1


5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL D3 AS SHOWN BELOW:



8. MARK EACH LTC PART NUMBER AND ASSEMBLY TYPE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	U1
DC2093A-A	LT4275AIDD
DC2093A-B	LT4275BIDD
DC2093A-C	LT4275CIDD

9. DO NOT BREAK THIS SCORE LINE.

APPROVALS		 <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div>		
PCB DES.	KAUNG H.			
APP ENG.	KAUNG H.	TITLE: TOP ASSEMBLY DRAWING		
		LTPOE++ POWERED DEVICE		
		SIZE N/A	IC NO. LT4275XIDD, LT4321IUF DEMO CIRCUIT 2093A	REV. 2
SCALE = NONE		FILENAME: DC2093A-2.PCB		SHT 1 OF 2