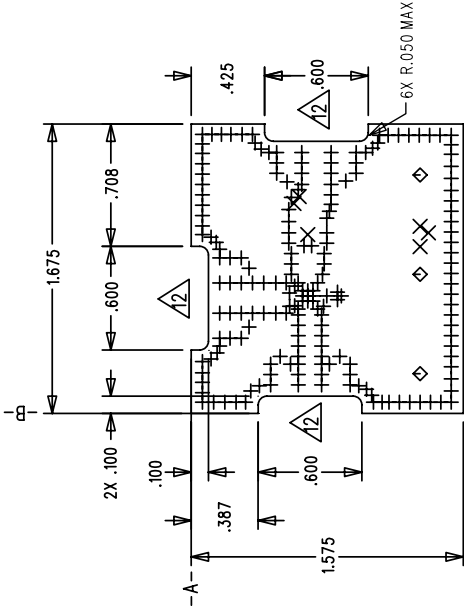


REVISION HISTORY

ECO	REV	DESCRIPTION	DATE	APPROVED
	1	PROTO	06/05/03	

NOTES: UNLESS OTHERWISE SPECIFIED

- ARTWORK P/N DC689A REV 1.
- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FINISHED THICKNESS TO BE .062 +/- .005 INCH  
WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND  
1 OZ. COPPER ON TWO INTERNAL LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .
- OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 30 MIL  
TRACE. SEE STACKUP DIAGRAM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
-A- AND -B- ARE PRIMARY DATUMS.
- BOARD: SELECTIVE PLATED BOARD. SOLDER  
MASK OVER BARE COPPER.  
WHITE TIN BOTH SIDES.  
SOLDER MASK TYPE: WET PHOTO-IMAGEABLE  
THE PCB COLOR: GREEN  
SILKSCREEN BOTH SIDES WITH WHITE NON-  
CONDUCTIVE INK.  
PLATE THRU ALL HOLES WITH COPPER  
MIN. PLATING THICKNESS: 1 OZ. EXCEPT  
WHERE PLATING NOT REQUIRED  
7. DRILL: ALL HOLES SHALL BE DRILLED +/- .003 INCH  
WITH RESPECT TO CTR. OF DRILLED PAD.  
ALL HOLES FINISHED SIZE AFTER PLATING.  
8. DROP ALL UNUSED PADS ON INNER LAYERS.  
9. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
10. CONTROLLED 50 OHM IMPEDANCE(AT 2.5GHz FREQ.) FOR LAYER 1-2.  
11. 18 MILLS THICKNESS REFLECTS DIELECTRIC CONSTANT OF 4.1 @ 2GHz.  
SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC  
CONSTANT DEVIATIONS.  
12. INNER AND OUTER LAYER COPPER SHALL BE EXPOSED IN  
TWO INSET AREAS ALONG BOARD EDGES.  
DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE.



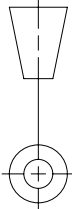
COMPONENT SIDE

HOLE LEGEND

TOLERANCE	SIZE	QTY	SYM	PLTD
+/-0.003	10	189	+	PLTD
+/-0.003	15	6	X	PLTD
+/-0.005	93	3	◇	PLTD

UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON ANGLE +/-1 °  
2 PLACE ±01 3 PLACE ±005  
INTERPRET DIM AND TOL  
PER ASME Y14.5M - 1994

THIRD ANGLE PROJECTION



DO NOT SCALE DRAWING

CONTRACT NO

APPROVALS	DATE
DRAWN MEI	04/30/03
CHECKED	
APPROVED	
ENGINEER	



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TITLE

FAB, LT5522EUF, DOWN CONVERTING MIXER

SIZE	CAGE CODE	DWG NO	REV
A		DC689A	1
SCALE	1/1	FILENAME: 689A-1.PCB	SHEET 1 OF 1