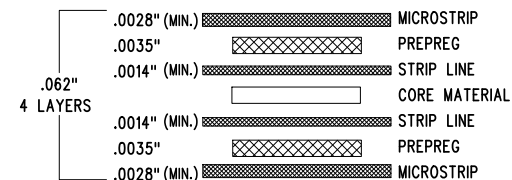


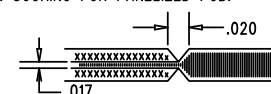
SIZE	QTY	SYM	PLATED	TOL
0.006	75	⊕ ^A	YES	+/-0.003
0.01	703	⊗	YES	+/-0.003
0.035	6	⊠	YES	+/-0.003
0.095	11	□	YES	+/-0.003
0.189	4	⊕ ^B	YES	+/-0.003
0.215	4	◇	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.14232	4	⊕ ^C	NO	+/-0.003
0.187	4	⊕	NO	+/-0.003

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
-FOR VIAS SIZE > 0.01", VIAS HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



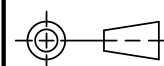
ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

- PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
- SOLDERABILITY BOARD WITH TEST RESULTS.
- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
- VACUUM PACKED WITH DESICCANT.
- FULL PANEL WITH NO REJECT

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



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HIGH POWER SYNCHRONOUS 4-SWITCH BUCK-BOOST
REGULATOR WITH SPREAD SPECTRUM

SIZE N/A IC NO. LT8390EFE
DC2431A

FILENAME: DC2431A-4.PCB SHT 1 OF 1