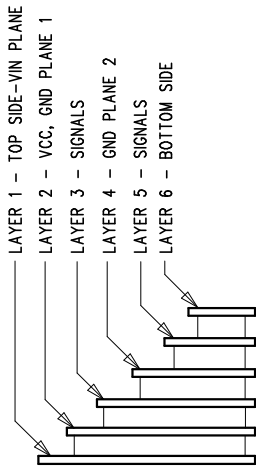


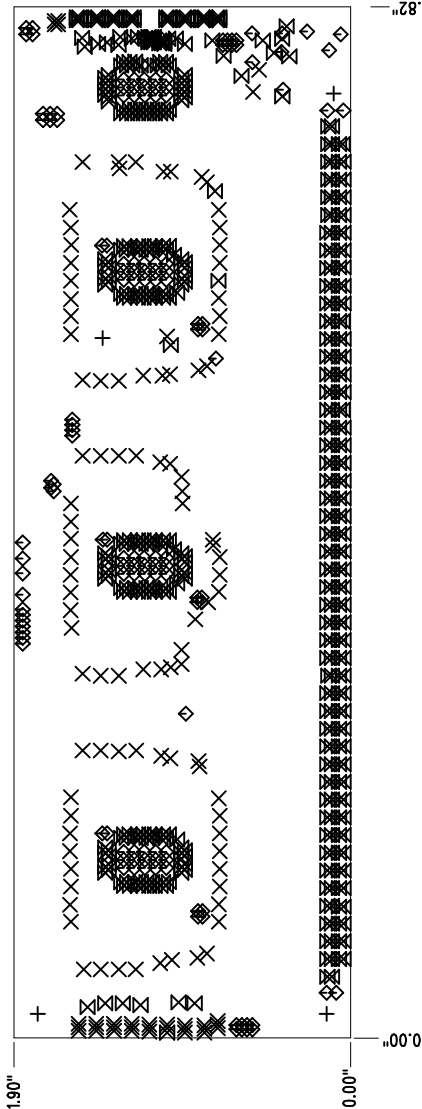
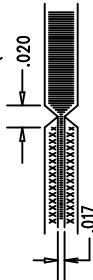
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	3	PRODUCTION	KEITH S.
			DATE
			8-03-11

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062" +/- 0.005"
 - TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SNOBIC USING LPI BOTH SIDES, COLOR BLACK.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.125	4	+	NO	+/-0.003
0.008	128	X	YES	+/-0.003
0.012	137	◇	YES	+/-0.003
0.006	471	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED	
DIMENSIONS ARE IN INCHES	
TOLERANCES:	
0.XX" = ±0.01"	
0.XXX" = ±0.005"	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994	
THIRD ANGLE PROJECTION	

APPROVALS		TITLE: FABRICATION DRAWING	
PCB DES.	JW	48-CHANNEL LED DRIVER WITH PWM GENERATOR AND SERIAL INTERFACE	
APP ENG.	KEITH S.		
		SIZE	IC NO. LT8500EUAH/LT3595AEUHH
		N/A	REV 3
		SCALE = NONE	FILENAME: DC1552A-3.PCB
		SHT 1 OF 1	

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