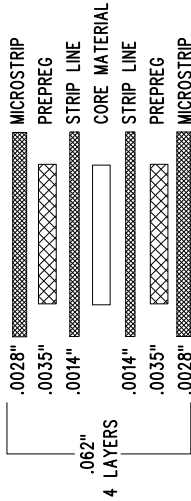
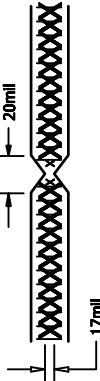


LAYER STRUCTURE



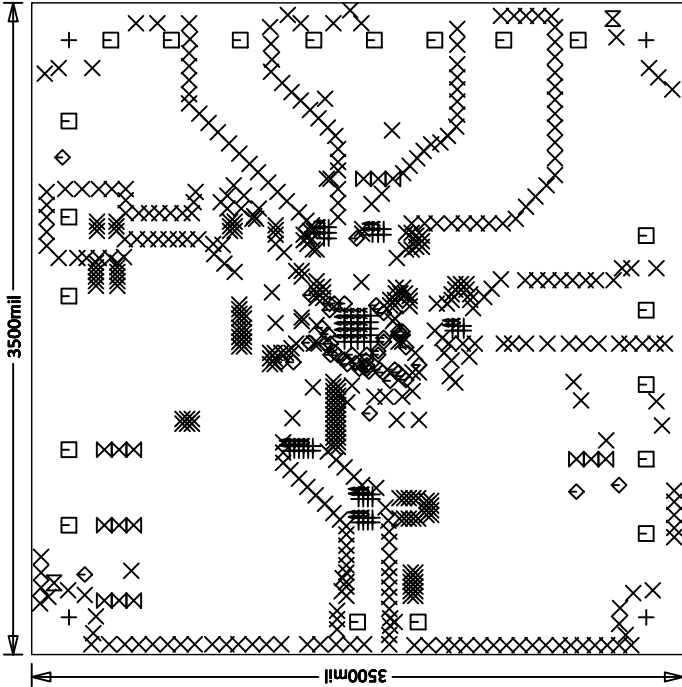
NOTES : UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL:
 - LEAD FREE ASSEMBLY COMPLIANT ISOLA FR-370HR OR EQUIVALENT.
 - 2 OZ. COPPER FINISH ON EXTERNAL LAYERS.
 - 1 OZ. COPPER FINISH ON INTERNAL LAYERS.
 - THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
 - 0.00 ARE PRIMARY DATUMS.
3. SIZE: DIMENSIONS SHOWN AND TOLERANCES +/- .010
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING. HOLE LOCATION TOLERANCES ARE +/- .3MIL INCH IN RELATION TO CENTER
5. FINISH: SWOBC USING LPI BOTH SIDES COLOR GREEN. GOLD IMMERSION BOTH SIDES.
6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT MAY MODIFY PAD SIZE TO MEET END FINISH.
8. PCB's SHOULD BE RoHS COMPLIANT.
9. SCORING: FOR PANELIZED FABRICATION ONLY



ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
- ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:
 1. PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
 2. SOLDERABILITY BOARD WITH TEST RESULTS.
 3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
 4. VACUUM PACKED WITH DESICCANT.
 5. FULL PANEL WITH NO REJECT.



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.01	495	X	YES	+/-0.003
0.095	20	□	YES	+/-0.003
0.006	52	◇	YES	+/-0.003
0.031	15	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.012	71	+	YES	+/-0.003

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TITLE: FABRICATION DRAWING
42V, LOW IQ, QUAD OUTPUT TRIPLE MONOLITHIC BUCK
CONVERTER AND SEPIC CONVERTER

SIZE	IC NO.	REV
N/A	LT8603EJ	3
DC2469A		

FILENAME: DC2469A-3.PCB

SHT 1 OF 1