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THE ATTACHED

TEST REPORT No.

NET2/022701/98

Registered under: L004E001

for *Linear Technology Corporation, LTC1543 / LTC1544 / LTC1344A Chipset*

consists of

Cover Sheet	1 page
NET Test Report (NTR)	54 pages
Annex A Diagram	1 pages

Total	56 pages

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The results of all measurements stated in this test report only relate to the product tested.

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NET TEST REPORT
for
equipment tested against the requirements
specified in
EUROPEAN TELECOMMUNICATION STANDARDS
NET 1 (Layer 1), NET 2 (Layer 1)

**NTR Number:
NET2/022701/98**



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1 IDENTIFICATION SUMMARY

1.1 NET TEST REPORT (NTR)

NTR Number : NET2/022701/98
NTR Date : March 4, 1998

Laboratory Manager:

March 4, 1998

NET2

Bill Crannick

Date

Section

Name

Signature

1.2 Test Laboratory

DETECON Inc, T.A.D.
1775 Old Highway 8, Suite 107
St. Paul, MN USA 55112
Tel. +1 612 639-0775
Fax. +1 612 639-0873

1.3 Limits and Reservations

This test report satisfies European Standard EN 45001. The test results in this test report apply only to the particular System Under Test (SUT) and component Items Under Test (IUTs) declared in this test report.

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1.4 Client Information

Name : **Linear Technology Corporation**
Street : **1630 McCarthy Blvd.**
City : **Milpitas, CA 95035**
Country : **USA**
Phone : **+1 (408) 954-8400**
Fax : **+1 (408) 428-9413**

Contact Person : **David Soo**
Phone : **+1 (408) 954-8400 ext. 3867**
Fax : **+1 (408) 428-9413**

1.5 Dates

Date of receipt of SUT : **February 24, 1998**
Date of Test : **February 27, 1998**

1.6 Description of System under Test (SUT)

Name of Supplier : **Linear Technology Corporation**
Street : **1630 McCarthy Blvd.**
City : **Milpitas, CA 95035**
Country : **USA**
Phone : **+1 (408) 954-8400**
Fax : **+1 (408) 428-9413**

Description of SUT:

LTC 1543 / LTC 1544 / LTC 1344 chip set supporting X.21, V.24, V.35, and V.36 interface circuits.



1.6.1 IUT (Item Under Test) Identification

Name	LTC 1543 / LTC 1544 / LTC 1344A Chipset
Version/Model	--
Part No.	--
Serial No.	--
Hardware Configuration for testing (PC, Bus System, Clock etc.)	Linear Tech Chipset LTC1543, LTC1544, LTC1344A mounted on Multi-protocol Transceiver Demo CKT: DC196A
Operating System	--
Version No.	--
Communication Software	--
Version No.	--
Software handling layer 2 and 3	--
Version No.	--

1.7 Nature of Conformance Testing

The purpose of Conformance Testing is to increase the probability that different implementations can interwork. However, the complexity of OSI protocols makes exhaustive testing impractical on both technical and economic grounds. Furthermore, there is no guarantee that an IUT which has passed all the relevant tests conforms to a specification. Neither there is any guarantee that such an IUT will interwork with other real open systems. Rather, the passing of the tests gives confidence that the IUT has the stated capabilities and that its behaviour conforms consistently in representative instances of communication.



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1.8 Implemented physical capabilities

1.8.1 CCITT Recommendation X.21/X.27 Interface

Implemented Yes (X) No ()
 Tested Yes (X) No ()

Interface board	LTC 1543, LTC 1544, LTC 1344A		
minimum speed (bit/s)	--		
maximum speed (bit/s)	8 Mbit/s		
V.11 Transmitter	LTC 1543, LTC 1544		
V.11 Receivers	LTC 1543		
Connector type	--		
Cable, name or part No.	--		
Length	--	shielded	unshielded

supported
 X.27 (V.11)

- T(A) (X)
- C(A) (X)
- R(A) (X)
- I(A) (X)
- S(A) (X)
- B(A) (X)
- G (X)
- T(B) (X)
- C(B) (X)
- R(B) (X)
- I(B) (X)
- S(B) (X)
- B(B) (X)

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1.8.2 CCITT Recommendation V.24/V.28 (RS232) Interface

Implemented Yes (X) No ()

Tested Yes (X) No ()

Interface board	LTC 1543, LTC 1544, LTC 1344A		
minimum speed (bit/s)	--		
maximum speed (bit/s)	19.2 kbit/s		
V.28 Transmitter	LTC 1543, LTC 1544		
V.28 Receivers	LTC 1543, LTC 1544		
Connector type	--		
Cable, name or part No.	--		
Length	--	shielded	unshielded

CCITT circuit No.	supported
102	(X)
103	(X)
104	(X)
105	(X)
106	(X)
107	(X)
108	(X)
109	(X)
111	()
112	()
113	(X)
114	(X)
115	(X)
125	()
140	()
141	(X)
142	()



1.8.3 CCITT Recommendation V.35 Interface

Implemented Yes (X) No ()
 Tested Yes (X) No ()

Interface board	LTC 1543, LTC 1544, LTC 1344A		
minimum speed (bit/s)	--		
maximum speed (bit/s)	640 kbit/s		
V.35 Transmitter	LTC 1543		
V.35 Receivers	LTC 1543		
V.11 Transmitter	--		
V.11 Receivers	--		
V.28 Transmitter	LTC 1544		
V.28 Receivers	LTC 1544		
Connector type	--		
Cable, name or part No.	--		
Length	--	shielded	unshielded

CCITT circuit No.	Electrical characteristics		supported
102a			()
102			(X)
103	V.35 (X)	V.11 ()	(X)
104	V.35 (X)	V.11 ()	(X)
105	V.28 (X)	V.10 ()	(X)
106	V.28 (X)	V.10 ()	(X)
107	V.28 (X)	V.10 ()	(X)
108	V.28 (X)	V.10 ()	(X)
109	V.28 (X)	V.10 ()	(X)
113	V.35 (X)	V.11 ()	(X)
114	V.35 (X)	V.11 ()	(X)
115	V.35 (X)	V.11 ()	(X)
125	V.28 ()	V.10 ()	()
140	V.28 ()	V.10 ()	()
141	V.28 (X)	V.10 ()	(X)
142	V.28 ()	V.10 ()	()



1.8.4 CCITT Recommendation V.36 (RS449) Interface

Implemented Yes (X) No ()
 Tested Yes (X) No ()

Interface board	LTC 1543, LTC 1544, LTC 1344A		
minimum speed (bit/s)	--		
maximum speed (bit/s)	8 Mbit/s		
V.11 Transmitter	LTC 1543, LTC 1544		
V.11 Receivers	LTC 1543		
V.10 Transmitter	LTC 1544		
V.10 Receivers	--		
Connector type	--		
Cable, name or part No.	--		
Length	--	shielded	unshielded

CCITT circuit No.	Electrical characteristics	supported
102		()
102a		()
102b		()
103	V.11 (X)	(X)
104	V.11 (X)	(X)
105	V.11 (X) V.10 ()	(X)
106	V.11 (X)	(X)
107	V.11 (X)	(X)
108	V.11 (X) V.10 ()	(X)
109	V.11 (X)	(X)
113	V.11 (X)	(X)
114	V.11 (X)	(X)
115	V.11 (X)	(X)
125	V.10 ()	()
140	V.10 ()	()
141	V.10 (X)	(X)
142	V.10 ()	()



2 Test Conditions

2.1 Environmental Conditions

Temperature	: In the range of 15° to 35°	<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No
Relative humidity	: In the range of 25% to 75%	<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No
Air pressure	: In the range of 86 to 106 kPa	<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No

2.2 Power Supply Limitations

All tests were carried out within +/- 5% of the normal operating voltage

2.3 Abstract Test Suites (ATS)

NET 1, second edition 1994
 NET 2, second edition 1994

2.4 Test Equipment

Equipment used for measurements of layer 1, NET 1 and NET 2

	Serial-No.	Last Calibration
- Schnittstellen-Messbox VX	V0001	March '97
- Digital Multimeter HP 34401A	3146A25410	Jan '98
- Digital Multimeter HP 34401A	3146A34621	Jan '98
- Power Supply HP6235A	2450A08776	Jan '98
- Oscilloscope HP 54520A	3415A00472	Dec '97
- Frequency Generator HP 33120A	US34014718	Jan '98
- Capacitance/Resistance Decade RCS-500	A0010	March '97

The DETECON test equipment is in the tolerance of the requirements of NET 1, 5.4.3 and NET 2, 7.2.3. Compliance is regularly checked by DETECON.



3 System Conformance Test Report Summary

This IUT has not been shown by conformance assessment to be non-conforming to the specified standard.

The tables in this section indicate for each test both the test case selection that was performed by the test laboratory and the results of testing. The tables are set up as followed below. Notes on the information that the test laboratory shall complete in the columns are profited below, and referenced as *n*).

ATS-Reference	Selected	Run	Verdict	Observation
a)	b)	c)	d)	e)

- a) Reference to the abstract test case of the ATS standard.
- b) Indicates whether or not the test was selected according to the PICS and PIXIT.
- c) Indicates whether or not the test did run to completion.
- d) Indicates the verdicts as assigned during the test campaign.

Possible verdicts:

- PASS : The test purpose according to the ATS is achieved, test did run as defined in ETS to completion.
- INCONCLUSIVE : The test purpose according to the ATS is not achieved, test did run not as defined in ETS to completion.
- FAIL : The test purpose according to the ATS is not achieved

- e) Indicates a reference to any observations made in section 8 of this test report.



3.1 Parameterized Executable Test Suite (PETS), Layer 1

The selection of the tests is limited by the implemented capabilities.

3.1.1 Clause X.21/V.11

NET 1 Clause	Selected	Run	Verdict	Observation
8.1.1.2, CCITT V.11, 5.2	yes	yes	pass	
8.1.1.2, CCITT V.11, 5.3	yes	yes	pass	
8.1.1.2.2a	yes	yes	pass	
8.1.1.2.2b	yes	yes	pass	
8.1.1.2.2c	no			
8.1.2	no			
8.1.3	no			

3.1.2 Clause V.24/V.28

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.1.1	no			
8.2.1.2	no			
8.2.2.1, Annex A, Table A-1	no			
8.2.4.1, Annex B.1.1	yes	yes	pass	
8.2.4.1, Annex B.1.2	yes	yes	pass	
8.2.4.1, Annex B.1.3	yes	yes	pass	
8.2.4.1, Annex B.1.4	yes	yes	pass	
8.2.4.1, Annex B.1.5	yes	yes	pass	
8.2.4.1, Annex B.1.6	yes	yes	pass	
8.2.4.1, Annex B.2.1	yes	yes	pass	

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8.2.4.1, Annex B.2.2	yes	yes	pass	
8.2.4.1, Annex B.2.3	yes	yes	pass	
8.2.4.1, Annex B.2.4	yes	yes	pass	
8.2.4.1, Annex B.3.1	yes	yes	pass	
8.2.4.1, Annex B.3.2	no			
8.2.4.1, Annex B.3.3	yes	yes	pass	
8.2.4.1, Annex B.3.4	yes	yes	pass	
8.2.4.1, Annex B.4	yes	yes	pass	
8.2.4.1, Annex B.5	yes	yes	pass	
8.2.4.1, Annex B.7	no			

3.1.3 Clause V.35

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.1.1	no			
8.2.1.2	no			
8.2.2.2, Annex A, Table A-2	no			
8.2.4.2, Annex C.1.1	yes	yes	pass	
8.2.4.2, Annex C.1.2	yes	yes	pass	
8.2.4.2, Annex C.1.3	yes	yes	pass	
8.2.4.2, Annex C.1.4	yes	yes	pass	
8.2.4.2, Annex C.2	no			
8.2.4.2, Annex C.3.1	yes	yes	pass	
8.2.4.2, Annex C.3.2	yes	yes	pass	
8.2.4.2, Annex C.4	yes	yes	pass	
8.2.4.1, Annex B.1.2	yes	yes	pass	
8.2.4.1, Annex B.1.3	yes	yes	pass	
8.2.4.1, Annex B.1.4	yes	yes	pass	
8.2.4.1, Annex B.1.5	yes	yes	pass	
8.2.4.1, Annex B.2.1	yes	yes	pass	
8.2.4.1, Annex B.2.2	yes	yes	pass	
8.2.4.1, Annex B.4	yes	yes	pass	
8.2.4.1, Annex B.5	yes	yes	pass	



3.1.4 Clause V.36/V.10

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.2.3, Annex A, Table A-3	no			
8.2.4.4, CCITT V.10, 5.2.1	yes	yes	pass	
8.2.4.4, CCITT V.10, 5.2.2	yes	yes	pass	
8.2.4.4, CCITT V.10, 5.2.3	yes	yes	pass	
NET 1 8.1.1.1.2	no			

3.1.5 Clause V.36/V.11

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.1.1	no			
8.2.1.2	no			
8.2.2.3, Annex A, Table A-3	no			
8.2.4.5, CCITT V.11, 5.2	yes	yes	pass	
8.2.4.5, CCITT V.11, 5.3	yes	yes	pass	
NET 1 8.1.1.2.2a	yes	yes	pass	
NET 1 8.1.1.2.2b	yes	yes	pass	
NET 1 8.1.1.2.2c	no			



3.2 Protocol Conformance Test Report (PCTR)

The selection of test cases is limited by the implemented Capabilities

3.2.1 Parameterized Executable Test Suite (PETS) Layer 2

NET 2 Clause	Selected	Run	Verdict	Observation
9.1.1.1	no			
9.1.1.2	no			
9.1.2.1	no			
9.1.2.2	no			
9.1.3.1	no			
9.1.3.2	no			
9.1.4.1	no			
9.1.4.2	no			
9.2.1	no			
9.2.2	no			
9.2.3	no			
9.2.4	no			
9.3.1	no			
9.3.2	no			
9.4.1	no			
9.5.1	no			
9.5.2	no			
9.5.3	no			
9.5.4	no			
9.6.1	no			
9.6.2	no			
9.7.1	no			
9.7.2	no			
9.7.3	no			
9.8.1	no			
9.8.2	no			
9.8.3	no			
9.9.1	no			
9.9.2	no			

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9.10.1	no			
9.10.2	no			
9.10.3	no			
9.11.1	no			
9.11.2	no			
9.11.3	no			

3.2.2 Parameterized Executable Test Suite (PETS) Layer 3

NET 2 Clause	SVC	Selected	Run	Verdict	Observation
10.1.1		no			
10.1.2		no			
10.1.3		no			
10.2.1		no			
10.2.2		no			
10.2.3		no			
10.2.4		no			
10.2.5		no			
10.2.6		no			
10.3.1		no			
10.3.2		no			
10.4.1		no			
10.4.2		no			
10.4.3		no			
10.4.4		no			
10.5.1		no			
10.5.2		no			
10.5.3		no			
10.5.4		no			
10.6.1		no			
10.6.2		no			
10.6.3		no			
10.6.4		no			
10.6.5		no			
10.6.6		no			

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10.6.7	no			
10.6.8	no			
10.7.1	no			
10.7.2	no			
10.7.3	no			
10.7.4	no			
10.7.5	no			
10.8.1	no			
10.8.2	no			
10.8.3	no			
10.8.4	no			

NET 2 Clause	PVC	Selected	Run	Verdict	Observation
10.1.1		no			
10.1.2		no			
10.1.3		no			
10.2.1		no			
10.2.2		no			
10.2.3		no			
10.2.4		no			
10.2.5		no			
10.2.6		no			
10.3.1		no			
10.3.2		no			
F2.2.1		no			



4 Test Report Physical Layer

4.1 Test Report X.21/V.11

The following tests are performed at the DCE end of the DTE-DCE cable supplied by the applicant.

4.1.1 Basic Interconnection Test

NET 1 Clause	Selected	Run	Verdict	Observation
8.1.2	no			

4.1.2 Generator Characteristics

4.1.2.1 Open Circuit Measurement (V.11, 5.2.1)

A - B terminated with 3.9 kOhms

4.1.2.1.1 Circuit T (pin 2 and 9)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.90 V	pass	
A - C	$V_{oa} \leq 6.0V$	4.95 V	pass	
B - C	$V_{ob} \leq 6.0V$	0.05 V	pass	

4.1.2.1.2 Circuit C (pin 3 and 10)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.93 V	pass	
A - C	$V_{oa} \leq 6.0V$	0.02 V	pass	
B - C	$V_{ob} \leq 6.0V$	4.96 V	pass	



4.1.2.1.3 Circuit B (pin 7 and 14)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.92 V	pass	
A - C	$V_{oa} \leq 6.0V$	4.96 V	pass	
B - C	$V_{ob} \leq 6.0V$	0.03 V	pass	

4.1.2.2 Test Termination Measurement (V.11, 5.2.2)

Generator terminated with 2 x 50 Ohms

4.1.2.2.1 Circuit C (pin 3 and 10)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	-3.21 V	pass	
	$V_{os} \leq 3.0V$	-2.38 V	pass	

4.1.2.2.2 Circuit B (pin 7 and 14)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.09 V	pass	
	$V_{os} \leq 3.0V$	2.36 V	pass	

4.1.2.2.3 Circuit T (pin 2 and 9)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.10 V	pass	
	$V_{os} \leq 3.0V$	2.36 V	pass	

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B binary state 2

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq Vt \leq 0.5Vo$	-3.11 V	pass	
	$Vt1 - Vt2 \leq 0.4V$	0.01 V	pass	
	$Vos \leq 3.0V$	-2.36 V	pass	
	$Vos1 - Vos2 \leq 0.4V$	0 V	pass	

4.1.2.3 Short Circuit Measurement (V.11, 5.2.3)

4.1.2.3.1 Circuit T (pin 2 and 9)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	108 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0 mA	pass	

4.1.2.3.2 Circuit C (pin 3 and 10)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	0 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	111 mA	pass	

4.1.2.3.3 Circuit B (pin 7 and 14)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	103 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0 mA	pass	



4.1.2.4 Power-off Measurement (V.11, 5.2.4)

Power-off applied voltage $\pm 0.25V$

4.1.2.4.1 Circuit T (pin 2 and 9)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	

4.1.2.4.2 Circuit C (pin 3 and 10)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	

4.1.2.4.3 Circuit B (pin 7 and 14)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu A$	$< 1 \mu A$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu A$	$< 1 \mu A$	pass	



4.1.3 Receiver Characteristics (V.11, 6.1)

4.1.3.1 Circuit R (pin 4 and 11)

The receiver i_s terminated with an optional cable resistance.

Limits according to NET 1, Revision 1, Figure 1

Description	Limits	Results	Verdict	Observation
Via = 6V	$I \leq 60.75 \text{ mA}$	54.5 mA	pass	
Via = 3V	$I \leq 30.75 \text{ mA}$	27.4 mA	pass	
Via = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.18 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -6V	$I \leq 60.75 \text{ mA}$	-53.9 mA	pass	
Via = -3V	$I \leq 30.75 \text{ mA}$	-27.3 mA	pass	
Via = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.22 mA	pass	
Vib = 6V	$I \leq 60.75 \text{ mA}$	54.4 mA	pass	
Vib = 3V	$I \leq 30.75 \text{ mA}$	27.3 mA	pass	
Vib = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.19 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -6V	$I \leq 60.75 \text{ mA}$	-54.2 mA	pass	
Vib = -3V	$I \leq 30.75 \text{ mA}$	-27.3 mA	pass	
Vib = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.21 mA	pass	



4.1.3.2 Circuit B (pin 7 and 14)

The receiver is terminated with an optional cable resistance.

Limits according to NET 1, Revision 1, Figure 1

Description	Limits	Results	Verdict	Observation
Via = 6V	$I \leq 60.75 \text{ mA}$	54.2 mA	pass	
Via = 3V	$I \leq 30.75 \text{ mA}$	27.3 mA	pass	
Via = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.14 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -6V	$I \leq 60.75 \text{ mA}$	-54.0 mA	pass	
Via = -3V	$I \leq 30.75 \text{ mA}$	-27.2 mA	pass	
Via = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.13 mA	pass	
Vib = 6V	$I \leq 60.75 \text{ mA}$	54.2mA	pass	
Vib = 3V	$I \leq 30.75 \text{ mA}$	27.3 mA	pass	
Vib = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.13 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -6V	$I \leq 60.75 \text{ mA}$	-54.0 mA	pass	
Vib = -3V	$I \leq 30.75 \text{ mA}$	-27.3 mA	pass	
Vib = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.14 mA	pass	



4.1.3.3 Circuit I (pin 5 and 12)

The receiver *is not* terminated with an optional cable resistance.

Limits according to CCITT Recommendation V.11, Figure 6

Description	Limits	Results	Verdict	Observation
Via = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Via = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -10V	$I \leq 3.25 \text{ mA}$	-0.35 mA	pass	
Via = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	
Vib = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Vib = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -10V	$I \leq 3.25 \text{ mA}$	-0.35 mA	pass	
Vib = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	

4.1.3.4 Circuit S (pin 6 and 13)

The receiver *is* terminated with an optional cable resistance.

Limits according to NET 1, Revision 1, Figure 1

Description	Limits	Results	Verdict	Observation
Via = 6V	$I \leq 60.75 \text{ mA}$	54.9 mA	pass	
Via = 3V	$I \leq 30.75 \text{ mA}$	27.9 mA	pass	
Via = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.2 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -6V	$I \leq 60.75 \text{ mA}$	-54.8 mA	pass	
Via = -3V	$I \leq 30.75 \text{ mA}$	-27.8 mA	pass	
Via = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.22 mA	pass	
Vib = 6V	$I \leq 60.75 \text{ mA}$	55.0 mA	pass	
Vib = 3V	$I \leq 30.75 \text{ mA}$	27.9 mA	pass	
Vib = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.21 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -6V	$I \leq 60.75 \text{ mA}$	-55.0 mA	pass	
Vib = -3V	$I \leq 30.75 \text{ mA}$	-27.8 mA	pass	
Vib = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.21 mA	pass	

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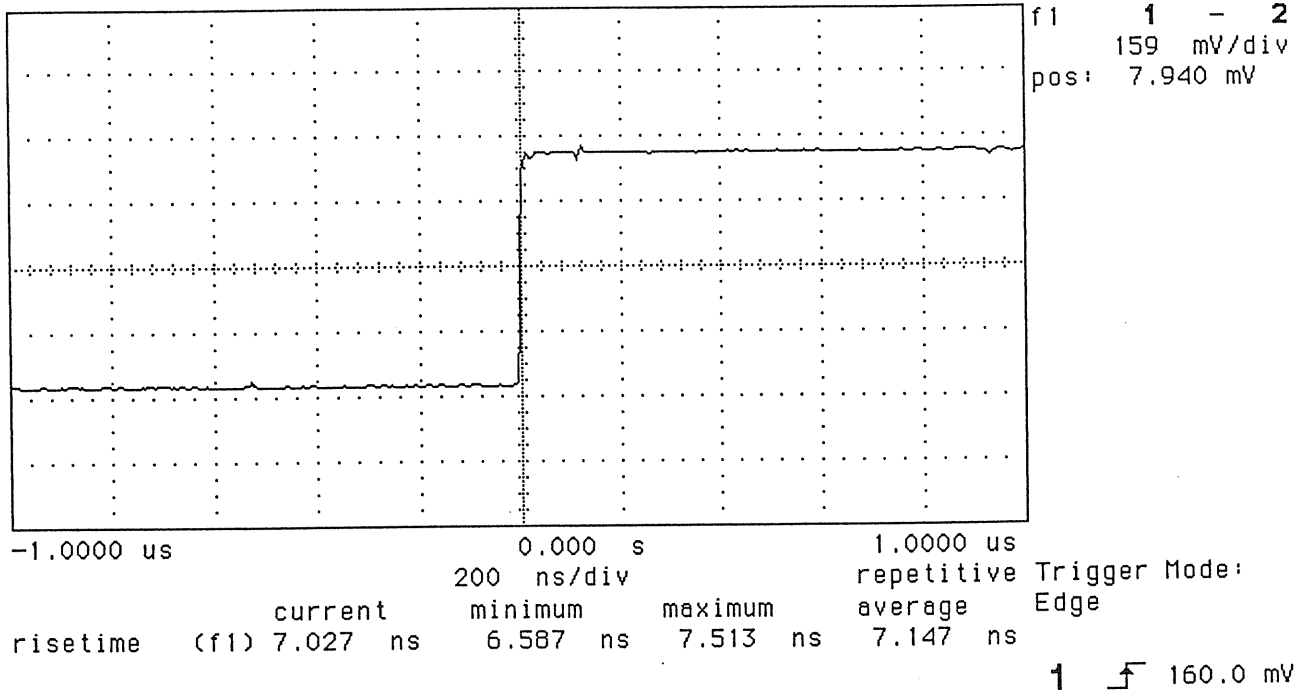


4.1.4 Dynamic Rise Time Measurement (V.11, 5.3)

Figure O-1

Circuit T (pin 2 and 9)

hp stopped



20ns ≥ t or t ≤ 0.1tb

Verdict: Pass



4.2 Test Report V.24/V.28

The following tests are performed at the DCE end of the DTE-DCE cable supplied by the applicant.

4.2.1 Basic Interconnection Test

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.2.1	no			

4.2.2 Generator Characteristics

4.2.2.1 Protection against short circuit conditions (Annex B.1.1)

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
105	no damage	no damage	pass	
108	no damage	no damage	pass	
113	no damage	no damage	pass	
141	no damage	no damage	pass	

4.2.2.2 Generator Output Current Limit (Annex B.1.2)

Circuit	Limits	Results	Verdict	Observation
103	$I \leq 0.5A$	27.2 mA	pass	
105	$I \leq 0.5A$	32.3 mA	pass	
108	$I \leq 0.5A$	32.2 mA	pass	
113	$I \leq 0.5A$	27.1 mA	pass	
141	$I \leq 0.5A$	105.2 mA	pass	

4.2.2.3 Generator Output Voltage open circuit (Annex B.1.3)

Circuit	Limits	Results	Verdict	Observation
103	$U \leq 25V$	9.2 V	pass	
105	$U \leq 25V$	9.2 V	pass	
108	$U \leq 25V$	9.1 V	pass	
113	$U \leq 25V$	9.2 V	pass	
141	$U \leq 25V$	8.9 V	pass	



4.2.2.4 Generator Output Voltage limit under maximum load (Annex B.1.4)

Circuit	Limits	Results	Verdict	Observation
103	$U \geq 5V$	8.4 V	pass	
105	$U \geq 5V$	8.5 V	pass	
108	$U \geq 5V$	8.8 V	pass	
113	$U \geq 5V$	8.6 V	pass	
141	$U \geq 5V$	8.6 V	pass	

4.2.2.5 Generator Output Voltage limit under minimum load (Annex B.1.5)

Circuit	Limits	Results	Verdict	Observation
103	$U \leq 15V$	8.8 V	pass	
105	$U \leq 15V$	9.0 V	pass	
108	$U \leq 15V$	9.0 V	pass	
113	$U \leq 15V$	8.8 V	pass	
141	$U \leq 15V$	8.7 V	pass	

4.2.2.6 Generator Output Power (Annex B.1.6)

plus an additional capacitance of 2500pF (Annex B.1.7)

(X) o.k. - see test Annex B.3.1 b

4.2.3 Load Characteristics

4.2.3.1 Load Resistance Conditions (Annex B.2.1)

Measurement with 3V applied

Circuit	Limits	Results	Verdict	Observation
104	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4707 Ω	pass	
106	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4758 Ω	pass	
107	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4795 Ω	pass	
109	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4800 Ω	pass	
114	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4610 Ω	pass	
115	$3 \text{ k}\Omega \leq R \leq 7 \text{ k}\Omega$	4593 Ω	pass	



Measurement with 15V applied

Circuit	Limits	Results	Verdict	Observation
104	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4706 Ω	pass	
106	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4890 Ω	pass	
107	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4896 Ω	pass	
109	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4617 Ω	pass	
114	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4388 Ω	pass	
115	$3\text{ k}\Omega \leq R \leq 7\text{ k}\Omega$	4662 Ω	pass	

4.2.3.2 Maximum Load open circuit voltage (Annex B.2.2)

Circuit	Limits	Results	Verdict	Observation
104	$U \leq 2\text{V}$	0.03 V	pass	
106	$U \leq 2\text{V}$	0.03 V	pass	
107	$U \leq 2\text{V}$	0.01 V	pass	
109	$U \leq 2\text{V}$	0.03 V	pass	
114	$U \leq 2\text{V}$	0.03 V	pass	
115	$U \leq 2\text{V}$	0.03 V	pass	

4.2.3.3 Maximum Load Shunt Capacitance (Annex B.2.3)

Circuit	Limits	Results	Verdict	Observation
104	$C \leq 2500\text{pF}$	0 pF	pass	See Note 1
114	$C \leq 2500\text{pF}$	0 pF	pass	See Note 1
115	$C \leq 2500\text{pF}$	0 pF	pass	See Note 1

4.2.3.4 Load Impedance (Annex B.2.4)

Circuit	Limits	Results	Verdict	Observation
104	capacitive	capacitive	pass	See Note 1
114	capacitive	capacitive	pass	See Note 1
115	capacitive	capacitive	pass	See Note 1



4.2.4 Transition between significant signal states

4.2.4.1 Wave Form (Annex B.3.1)

Circuit	Limits	Results	Verdict	Observation
103 slope positive	monotone, no negative components	monotone, no negative components	pass	see figure O-2
103 slope negative	monotone, no negative components	monotone, no negative components	pass	see figure O-2

4.2.4.2 Maximum Transition Time on Control Interchange Circuits (Annex B.3.2)

Circuit	Limits	Results	Verdict	Observation
105		no test		
108		no test		

4.2.4.3 Maximum Transition Time on Data and Timing Interchange Circuits (Annex B.3.3)

Transmission speed: 19.2 Kbps (3% Tb: 19.2 kbit/s = 1,56µs)

Test under consideration of NET 2, Annex B7

(X) B7 no : The generator was terminated with 3000 Ohms and 2500 pF

Circuit	Limits	Results	Verdict	Observation
103	t ≤ 3% Tb	770 ns	pass	see figure O-3, O-4

4.2.4.4 Maximum Instantaneous Rate of Voltage Change (Annex B.3.4)

Circuit	Limits	Results	Verdict	Observation
103	t ≥ 200ns	324 ns	pass	see figure O-5, O-6



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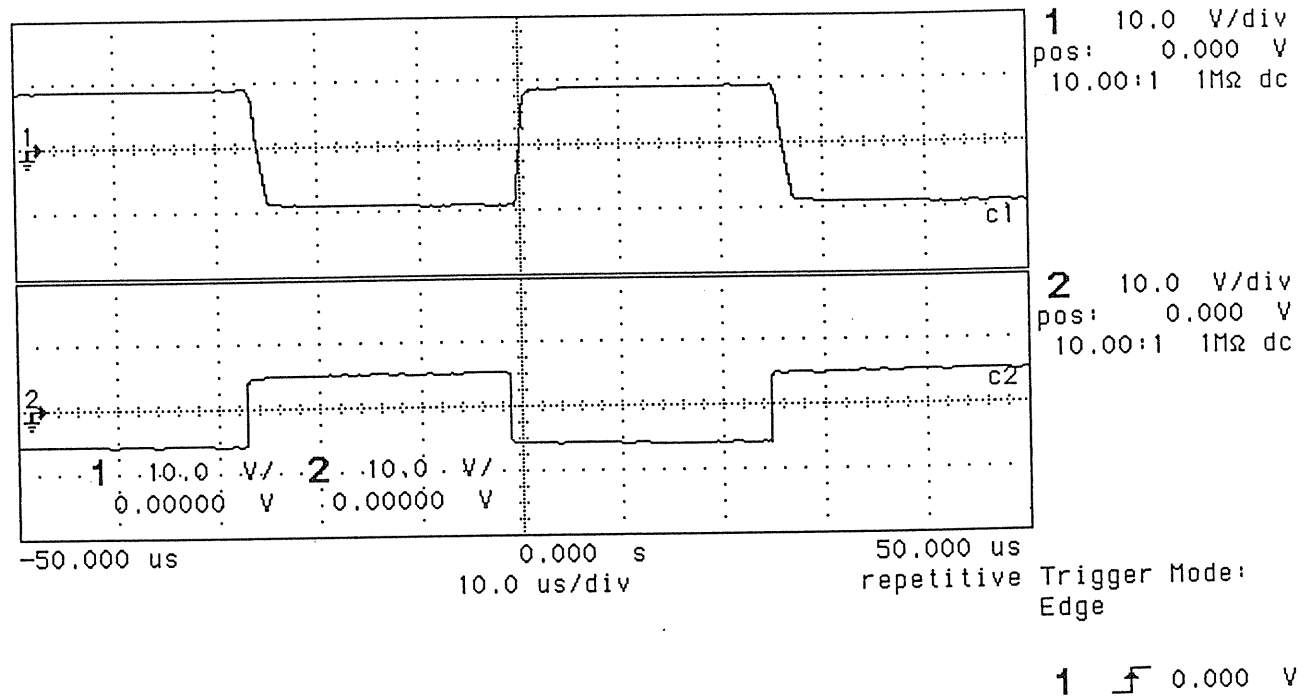
Figure O-2

Reference V.28 Wave Form

Ch1 = Circuit 103

Ch2 = Circuit 114

hp stopped





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Figure O-3

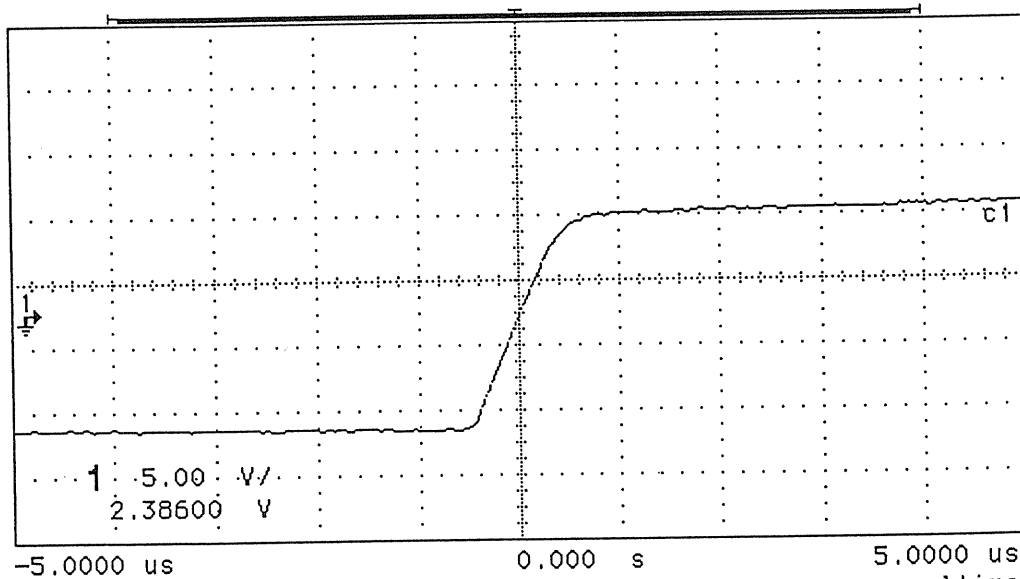
Transition between significant signal states

Circuit = CCITT 103, Send Data
Load = see paragraph 4.4.3.3

SLOPE POSITIVE

Transition time shall be $\leq 3\%$ of the nominal period

hp stopped



	current	minimum	maximum	average
risetime (1)	344.675ns	322.325ns	345.700ns	335.642ns

EDGE TRIGGER

trig'd **auto**

edge patt state
delay tv glitch

source **Channel 1**

adjust center
level **1.500 div**

noise rej off
coupling **lfrej**

holdoff
time **40.000 ns**



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Figure O-4

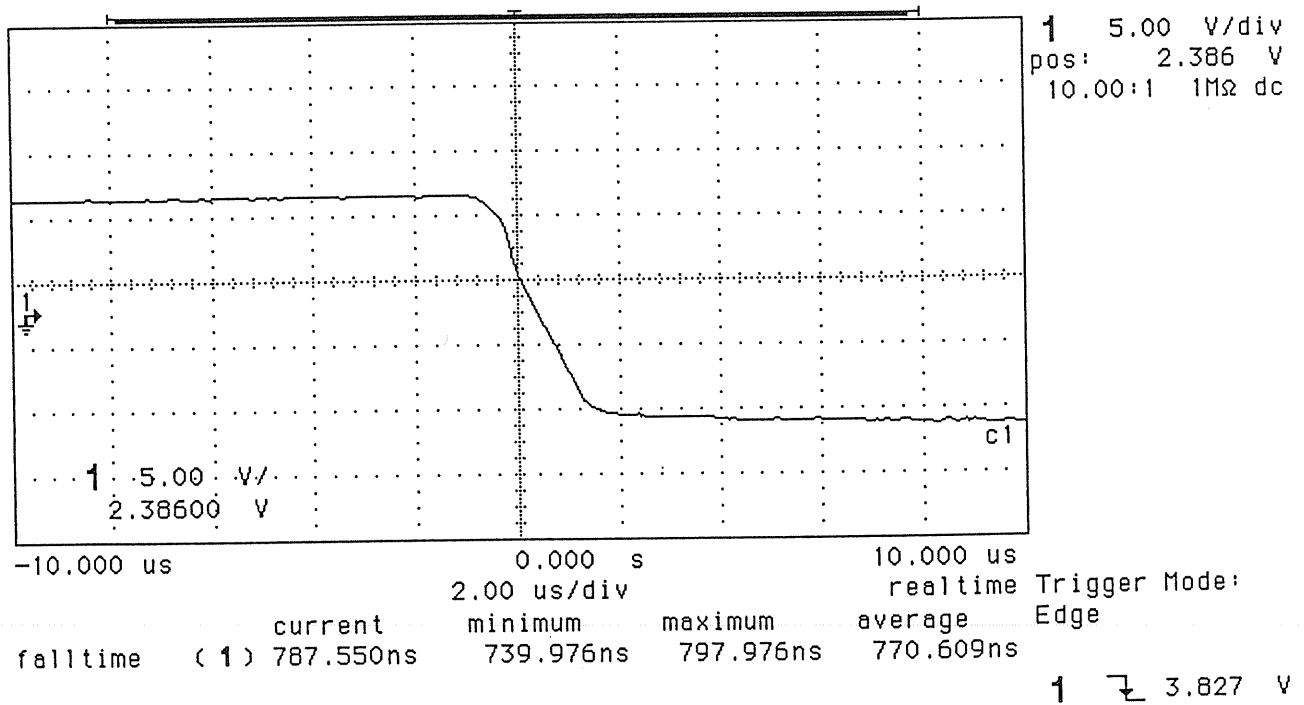
Transition between significant signal states

Circuit = CCITT 103, Send Data
Load = see paragraph 4.4.3.3

SLOPE NEGATIVE

Transition time shall be $\leq 3\%$ of the nominal period

hp stopped





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Figure O-5

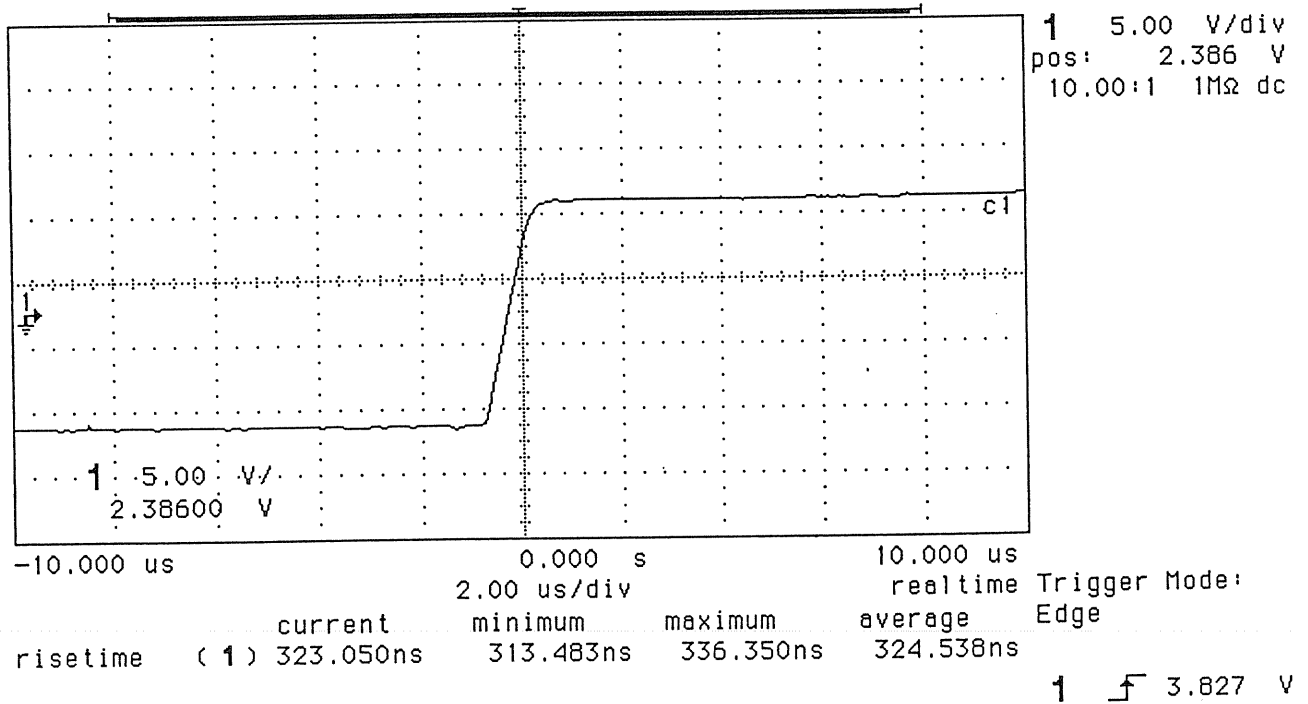
Maximum Instantaneous Rate of Voltage Change

Circuit = CCITT 103, Send Data
Load = 7000 Ohms (purely resistive)

SLOPE POSITIVE

Rate of Voltage Change shall be $\geq 200\text{ns}$ ($30\text{V per } \mu\text{s} = 6\text{V per } 200\text{ns}$)

hp stopped





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Figure O-6

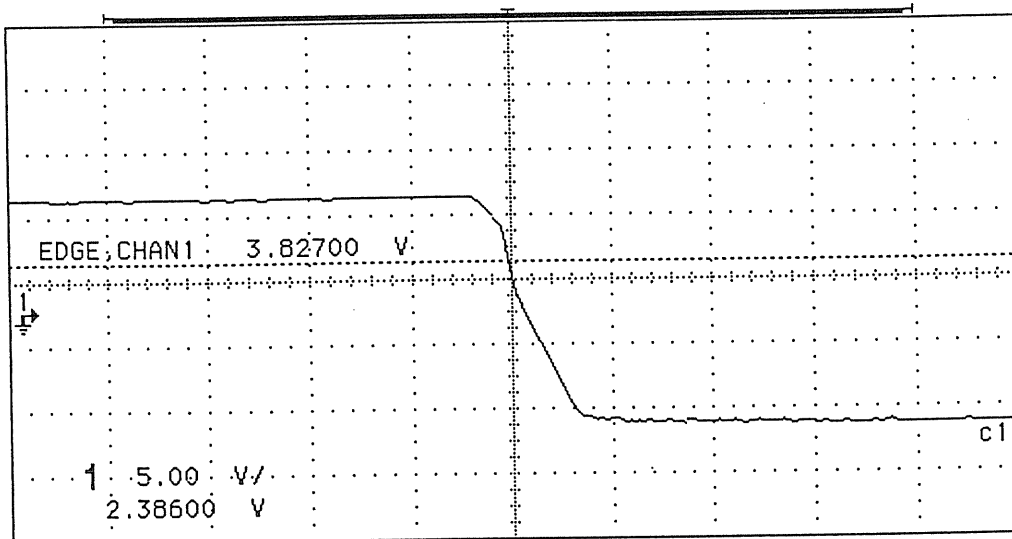
Maximum Instantaneous Rate of Voltage Change

Circuit = CCITT 103, Send Data
 Load = 7000 Ohms (purely resistive)

SLOPE NEGATIVE

Rate of Voltage Change shall be $\geq 200\text{ns}$ ($30\text{V per } \mu\text{s} = 6\text{V per } 200\text{ns}$)

hp stopped



-10.000 us	0.000 s	10.000 us
2.00 us/div		
falltime (1)	current	minimum
628.375ns	616.875ns	655.375ns
	maximum	average
	632.202ns	

EDGE TRIGGER

trig'd **auto**

edge patt state
 delay tv glitch

source **Channel 1**

adjust_center
 level **3.82700 V**

noise rej **off**
 coupling dc

holdoff
 time **40.000 ns**



4.2.5 Power off Measurement (Annex B.4)

Circuit	Limits	Results	Verdict	Observation
105	$R \geq 300\Omega$	$\infty \Omega$	pass	
107	$R \geq 300\Omega$	25.9 K Ω	pass	
108	$R \geq 300\Omega$	$\infty \Omega$	pass	

4.2.6 Short Circuit Test (Annex B.5)

A binary state 0

Connected to common return

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
105	no damage	no damage	pass	
108	no damage	no damage	pass	

Connected to a load of 3000 Ohms where the open circuit voltage is -2V

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
105	no damage	no damage	pass	
108	no damage	no damage	pass	

B binary state 1

Connected to common return

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
105	no damage	no damage	pass	
108	no damage	no damage	pass	



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Connected to a load of 3000 Ohms where the open circuit voltage is +2V

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
105	no damage	no damage	pass	
108	no damage	no damage	pass	

4.2.7 Allowance for Capacitance of Connections Leads (Annex B.7)

Circuit	Limits	Results	Verdict	Observation
All Leads	$C \leq 2500\text{pF}$	0 pF	pass	See Note 1



4.3 Test Report V.35

The following tests are performed at the DCE end of the DTE-DCE cable supplied by the applicant.

4.3.1 Basic Interconnection Test

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.2.2	no			

4.3.2 Generator Characteristics

4.3.2.1 Source Impedance (Annex C.1.1)

Circuit	Limits	Results	Verdict	Observation
103	$100 \pm 50 \Omega$	105 Ω	pass	
113	$100 \pm 50 \Omega$	103 Ω	pass	

4.3.2.2 Resistance A&B-C (Annex C.1.2)

Circuit	Limits	Results	Verdict	Observation
103 2V applied	$150 \pm 15 \Omega$	151 Ω	pass	
103 -2V applied	$150 \pm 15 \Omega$	153 Ω	pass	
113 2V applied	$150 \pm 15 \Omega$	152 Ω	pass	
113 -2V applied	$150 \pm 15 \Omega$	154 Ω	pass	

4.3.2.3 Test Termination Measurement (Annex C.1.3)

Circuit	Limits	Results	Verdict	Observation
103 P/S	$0.55 \pm 0.11V$	0.58 V	pass	
113 U/W	$0.55 \pm 0.11V$	0.57 V	pass	

4.3.2.4 Measurement Rise/Fall Time (Annex C.1.4)

Circuit	Limits	Results	Verdict	Observation
103	0.01 tb	14.6 ns	pass	see figure O-7



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Figure O-7

Dynamic Rise Time Measurement (V.35, II.3, d)

Speed = 64000 bit/s 1%tb = 156ns

hp stopped

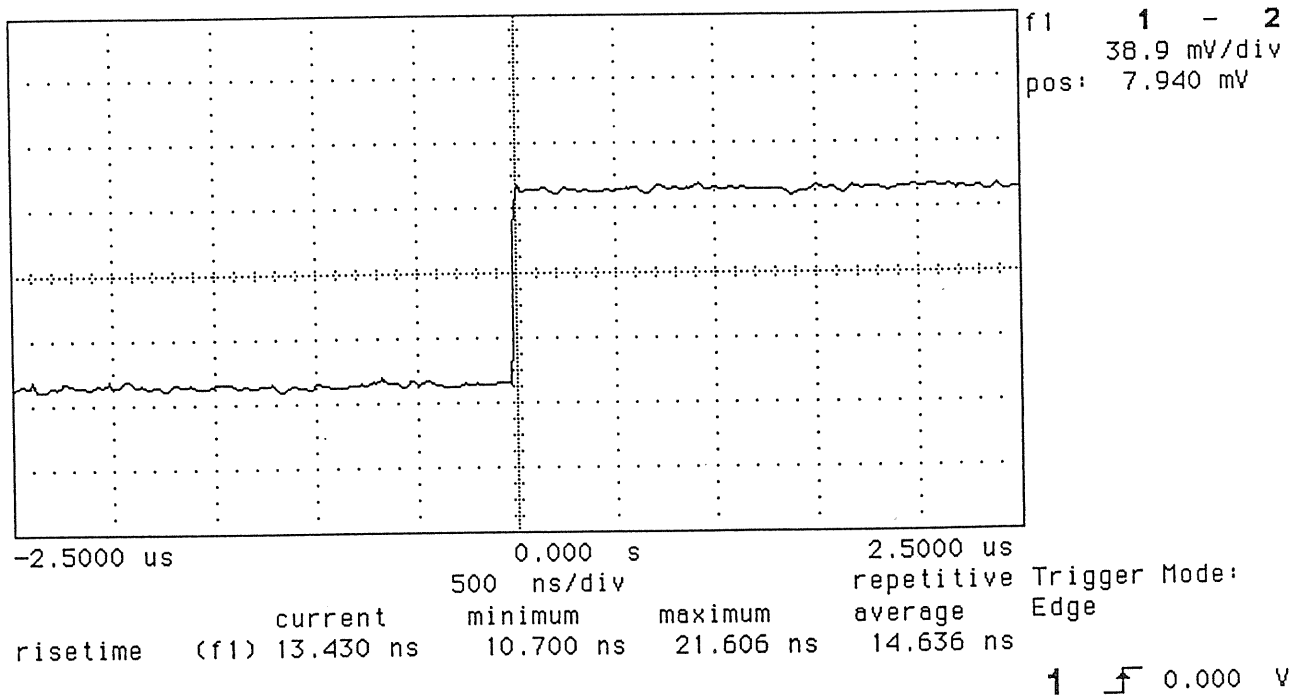
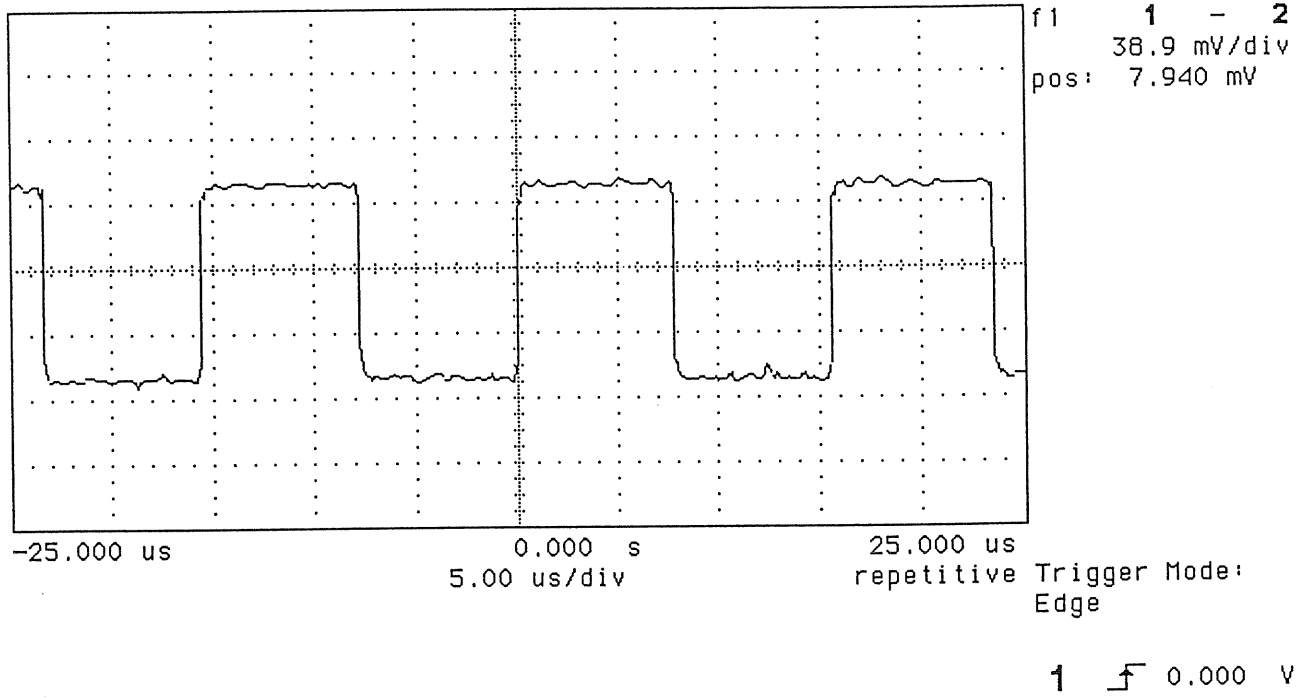




Figure O-8

Reference V.35 Wave Form

hp stopped



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4.3.3 Load Characteristics (Annex C.3)

4.3.3.1 Impedance (Annex C.3.1)

Circuit	Limits	Results	Verdict	Observation
104	100 ± 10 Ω	105 Ω	pass	
114	100 ± 10 Ω	103 Ω	pass	
115	100 ± 10 Ω	105 Ω	pass	

4.3.3.2 Resistance A&B-C (Annex C.3.2)

Circuit	Limits	Results	Verdict	Observation
104 +2V applied	150 ± 15 Ω	151 Ω	pass	
104 -2V applied	150 ± 15 Ω	153 Ω	pass	
114 +2V applied	150 ± 15 Ω	148 Ω	pass	
114 -2V applied	150 ± 15 Ω	148 Ω	pass	
115 +2V applied	150 ± 15 Ω	148 Ω	pass	
115 -2V applied	150 ± 15 Ω	148 Ω	pass	

4.3.4 Generator/Load Protection (Annex C.4)

Circuit	Limits	Results	Verdict	Observation
103	no damage	no damage	pass	
104	no damage	no damage	pass	
114	no damage	no damage	pass	
115	no damage	no damage	pass	



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4.3.5 Unbalanced Circuits

4.3.5.1 Generators

Circuit	Description	Limits	Results	Verdict	Observation
C	Max. Voltage	$U_0 \leq 25V$	8.69 V	pass	
H	Max. Voltage	$U_0 \leq 25V$	8.70 V	pass	
L	Max. Voltage	$U_0 \leq 25V$	7.69 V	pass	
C	Max. Load 3 kohm	$U_I \geq 5V$	8.13 V	pass	
H	Max. Load	$U_I \geq 5V$	8.12 V	pass	
L	Max. Load	$U_I \geq 5V$	7.48 V	pass	
C	Min. Load 7 kohm	$U_I \leq 15V$	8.36 V	pass	
H	Min. Load	$U_I \leq 15V$	8.36 V	pass	
L	Min. Load	$U_I \leq 15V$	7.55 V	pass	
C	Short Circuit	$I_k \leq 500mA$	28.5 mA	pass	
H	Short Circuit	$I_k \leq 500mA$	28.7 mA	pass	
L	Short Circuit	$I_k \leq 500mA$	84.2 mA	pass	
C	Short Circuit State 0 Common Return 3 Kohm / -2 V				
		no damage	no damage	pass	
H	Short Circuit State 0 Common Return 3 Kohm / -2 V				
		no damage	no damage	pass	
L	Short Circuit State 0 Common Return 3 Kohm / -2 V				
		no damage	no damage	pass	
C	Short Circuit State 1 Common Return 3 Kohm / +2 V				
		no damage	no damage	pass	
H	Short Circuit State 1 Common Return 3 Kohm / +2 V				
		no damage	no damage	pass	
L	Short Circuit State 1 Common Return 3 Kohm / +2 V				
		no damage	no damage	pass	
H	Resistance power off	$R \geq 300\Omega$	$\infty \Omega$	pass	



4.3.5.2 Receivers

Circuit	Description	Limits	Results	Verdict	Observation
D	Voltage without Terminator	$EL \leq 2V$	0.03 V	pass	
E	Voltage without Terminator	$EL \leq 2V$	0.01 V	pass	
F	Voltage without Terminator	$EL \leq 2V$	0.03 V	pass	
D	Rmin, 3V applied	$7k\Omega \geq R \geq 3k\Omega$	4815 Ω	pass	
E	Rmin, 3V applied	$7k\Omega \geq R \geq 3k\Omega$	4731 Ω	pass	
F	Rmin, 3V applied	$7k\Omega \geq R \geq 3k\Omega$	4854 Ω	pass	
D	Rmax, 15V applied	$7k\Omega \geq R \geq 3k\Omega$	4934 Ω	pass	
E	Rmax, 15V applied	$7k\Omega \geq R \geq 3k\Omega$	4901 Ω	pass	
F	Rmax, 15V applied	$7k\Omega \geq R \geq 3k\Omega$	4792 Ω	pass	



4.4 Test Report V.36

The following tests are performed at the DCE end of the DTE-DCE cable supplied by the applicant.

4.4.1 Basic Interconnection Test

NET 2 Clause	Selected	Run	Verdict	Observation
8.2.2.3	no			

4.4.2 Generator Characteristics

4.4.2.1 Open Circuit Measurement (V.11, 5.2.1)

A - B terminated with 3.9 kOhms

4.4.2.1.1 Circuit 103 (pin 4 and 22)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.92 V	pass	
A - C	$V_{oa} \leq 6.0V$	4.96 V	pass	
B - C	$V_{ob} \leq 6.0V$	0.03 V	pass	

4.4.2.1.2 Circuit 105 (pin 7 and 25)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.93 V	pass	
A - C	$V_{oa} \leq 6.0V$	4.96 V	pass	
B - C	$V_{ob} \leq 6.0V$	0.02 V	pass	

4.4.2.1.3 Circuit 113 (pin 17 and 35)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.92 V	pass	
A - C	$V_{oa} \leq 6.0V$	4.95 V	pass	
B - C	$V_{ob} \leq 6.0V$	0.03 V	pass	



4.4.2.1.4 Circuit 108 (pin 12 and 30)

Description	Limits	Results	Verdict	Observation
A - B	$V_o \leq 6.0V$	4.93 V	pass	
A - C	$V_{oa} \leq 6.0V$	0.02 V	pass	
B - C	$V_{ob} \leq 6.0V$	4.96 V	pass	

4.4.2.2 Test Termination Measurement (V.11, 5.2.2)

Generator terminated with 2 x 50 Ohms

4.4.2.2.1 Circuit 103 (pin 4 and 22)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.11 V	pass	
	$V_{os} \leq 3.0V$	2.36 V	pass	

B binary state 2

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	-3.10 V	pass	
	$V_{t1} - V_{t2} \leq 0.4V$	0.01 V	pass	
	$V_{os} \leq 3.0V$	-2.36 V	pass	
	$V_{os1} - V_{os2} \leq 0.4V$	0 V	pass	

4.4.2.2.2 Circuit 105 (pin 7 and 25)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.22 V	pass	
MP - Gnd	$V_{os} \leq 3.0V$	2.38 V	pass	



4.4.2.2.3 Circuit 113 (pin 17 and 35)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.10 V	pass	
MP - Gnd	$V_{os} \leq 3.0V$	2.36 V	pass	

4.4.2.2.4 Circuit 108 (pin 12 and 30)

A binary state 1

Description	Limits	Results	Verdict	Observation
A - B	$2V \leq V_t \leq 0.5V_o$	3.22 V	pass	
MP - Gnd	$V_{os} \leq 3.0V$	2.38 V	pass	

4.4.2.3 Short Circuit Measurement (V.11, 5.2.3)

4.4.2.3.1 Circuit 103 (pin 4 and 22)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	104.0 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0.02 mA	pass	

4.4.2.3.2 Circuit 105 (pin 7 and 25)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	111.0 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0.04 mA	pass	

4.4.2.3.3 Circuit 113 (pin 17 and 35)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	122.0 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0.04 mA	pass	

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4.4.2.3.4 Circuit 108 (pin 12 and 30)

Description	Limits	Results	Verdict	Observation
A - C	$I_{sa} \leq 150 \text{ mA}$	110.0 mA	pass	
B - C	$I_{sb} \leq 150 \text{ mA}$	0.04 mA	pass	

4.4.2.4 Power-off Measurement (V.11, 5.2.4)

Power-off applied voltage $\pm 0.25\text{V}$

4.4.2.4.1 Circuit 103 (pin 4 and 22)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	

4.4.2.4.2 Circuit 105 (pin 7 and 25)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	

4.4.2.4.3 Circuit 113 (pin 17 and 35)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	



4.4.2.4.4 Circuit 108 (pin 12 and 30)

Description	Limits	Results	Verdict	Observation
+0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
+0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xa} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	
-0.25V applied	$I_{xb} \leq 100 \mu\text{A}$	$< 1 \mu\text{A}$	pass	

4.4.3 Receiver Characteristics (V.11, 6.1)

4.4.3.1 Circuit 104 (pin 6 and 24)

The receiver i_s terminated with an optional cable resistance.

Limits according to NET 1, Revision 1, Figure 1

Description	Limits	Results	Verdict	Observation
Via = 6V	$I \leq 60.75 \text{ mA}$	54.2 mA	pass	
Via = 3V	$I \leq 30.75 \text{ mA}$	27.1 mA	pass	
Via = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.1 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -6V	$I \leq 60.75 \text{ mA}$	-53.5 mA	pass	
Via = -3V	$I \leq 30.75 \text{ mA}$	-26.9 mA	pass	
Via = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.2 mA	pass	
Vib = 6V	$I \leq 60.75 \text{ mA}$	54.0 mA	pass	
Vib = 3V	$I \leq 30.75 \text{ mA}$	27.1 mA	pass	
Vib = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.1 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -6V	$I \leq 60.75 \text{ mA}$	-53.5 mA	pass	
Vib = -3V	$I \leq 30.75 \text{ mA}$	-27.0 mA	pass	
Vib = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.2 mA	pass	



4.4.3.2 Circuit 106 (pin 9 and 27)

The receiver *is not* terminated with an optional cable resistance.

Limits according to CCITT Recommendation V.11, Figure 6

Description	Limits	Results	Verdict	Observation
Via = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Via = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -10V	$I \leq 3.25 \text{ mA}$	-0.35 mA	pass	
Via = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	
Vib = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Vib = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -10V	$I \leq 3.25 \text{ mA}$	-0.35 mA	pass	
Vib = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	

4.4.3.3 Circuit 109 (pin 13 and 31)

The receiver *is not* terminated with an optional cable resistance.

Limits according to CCITT Recommendation V.11, Figure 6

Description	Limits	Results	Verdict	Observation
Via = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Via = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -10V	$I \leq 3.25 \text{ mA}$	-0.37 mA	pass	
Via = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	
Vib = 10V	$I \leq 3.25 \text{ mA}$	0.35 mA	pass	
Vib = 3V	$I \leq 1.5 \text{ mA}$	0.10 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -10V	$I \leq 3.25 \text{ mA}$	-0.35 mA	pass	
Vib = -3V	$I \leq 1.5 \text{ mA}$	-0.10 mA	pass	



4.4.3.4 Circuit 114 (pin 5 and 23)

The receiver is terminated with an optional cable resistance.

Limits according to NET 1, Revision 1, Figure 1

Description	Limits	Results	Verdict	Observation
Via = 6V	$I \leq 60.75 \text{ mA}$	54.9 mA	pass	
Via = 3V	$I \leq 30.75 \text{ mA}$	27.6 mA	pass	
Via = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.2 mA	pass	
Via = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Via = -6V	$I \leq 60.75 \text{ mA}$	-54.5 mA	pass	
Via = -3V	$I \leq 30.75 \text{ mA}$	-27.5 mA	pass	
Via = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.2 mA	pass	
Vib = 6V	$I \leq 60.75 \text{ mA}$	54.7 mA	pass	
Vib = 3V	$I \leq 30.75 \text{ mA}$	27.6 mA	pass	
Vib = 1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	9.2 mA	pass	
Vib = 0V	$-0.75 \leq I \leq 0.75 \text{ mA}$	0.0 mA	pass	
Vib = -6V	$I \leq 60.75 \text{ mA}$	-54.7 mA	pass	
Vib = -3V	$I \leq 30.75 \text{ mA}$	-27.5 mA	pass	
Vib = -1V	$-0.5 \leq I \leq 10.75 \text{ mA}$	-9.2 mA	pass	



4.4.4 V.10 Measurements on circuit CCITT 141 (optional)

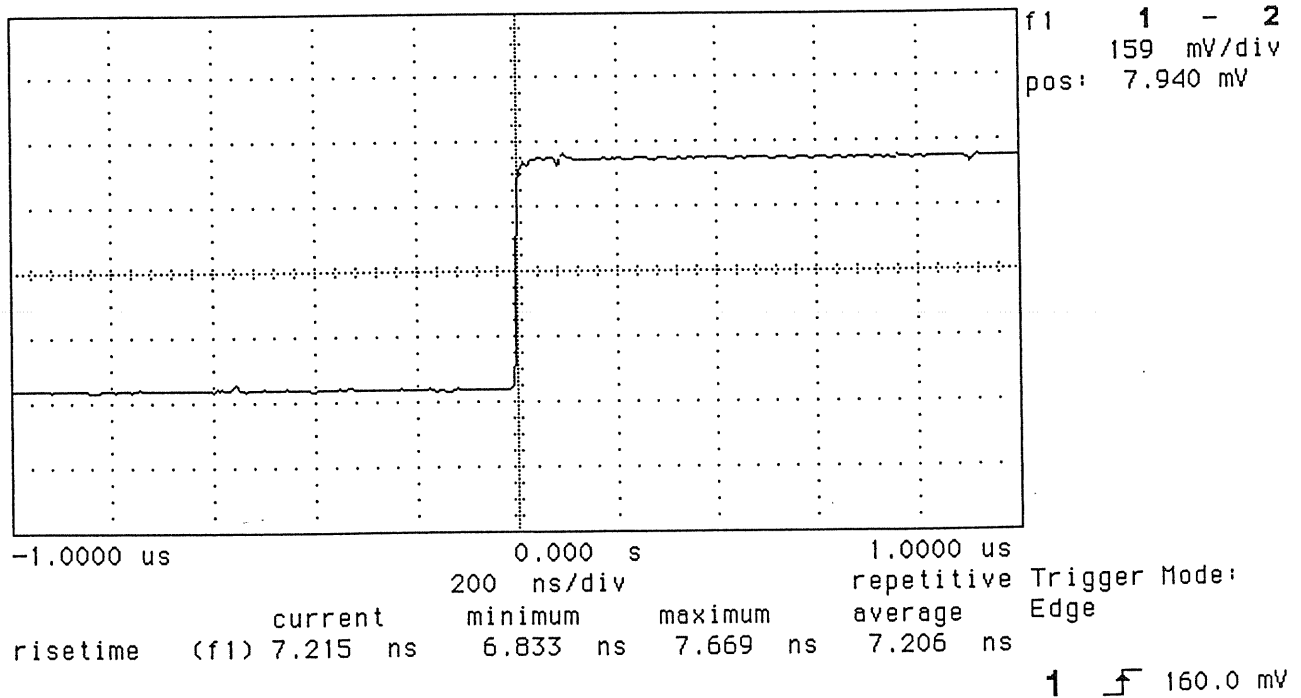
Reference	Description	Limits	Results	Verdict	Observation
V.10 5.2.1	A - C, terminated with 3.9 kOhms	$4V \leq V_o \leq 6V$	5.73 V	pass	
V.10 5.2.2	A - C, terminated with 450 Ohms	$V_t \geq 0.9 * V_o$	5.32 V	pass	
V.10 5.2.3	Short Circuit	$I_s \leq 150mA$	102 mA	pass	
V.10 5.2.4	Power off 0.25 V applied	$I_x \leq 100\mu A$	9.4 μA	pass	
V.10 5.2.4	Power off -0.25 V applied	$I_x \leq 100\mu A$	-9.4 μA	pass	

4.4.5 Dynamic Rise Time Measurement (V.11, 5.3)

Figure O-9

Circuit 103 (pin 4 and 22)

hp stopped



20ns ≥ t or t ≤ 0.1tb

Verdict : Pass



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5 Test Report Frame Layer

Test Result:

The tested implementation has not been shown by conformance assessment to be non-conforming to NET 2, Clause 9 (CCITT X.25, Layer 2, Frame Layer).

Test Carried out:

Not Applicable

6 Test Report Packet Layer

Test Result:

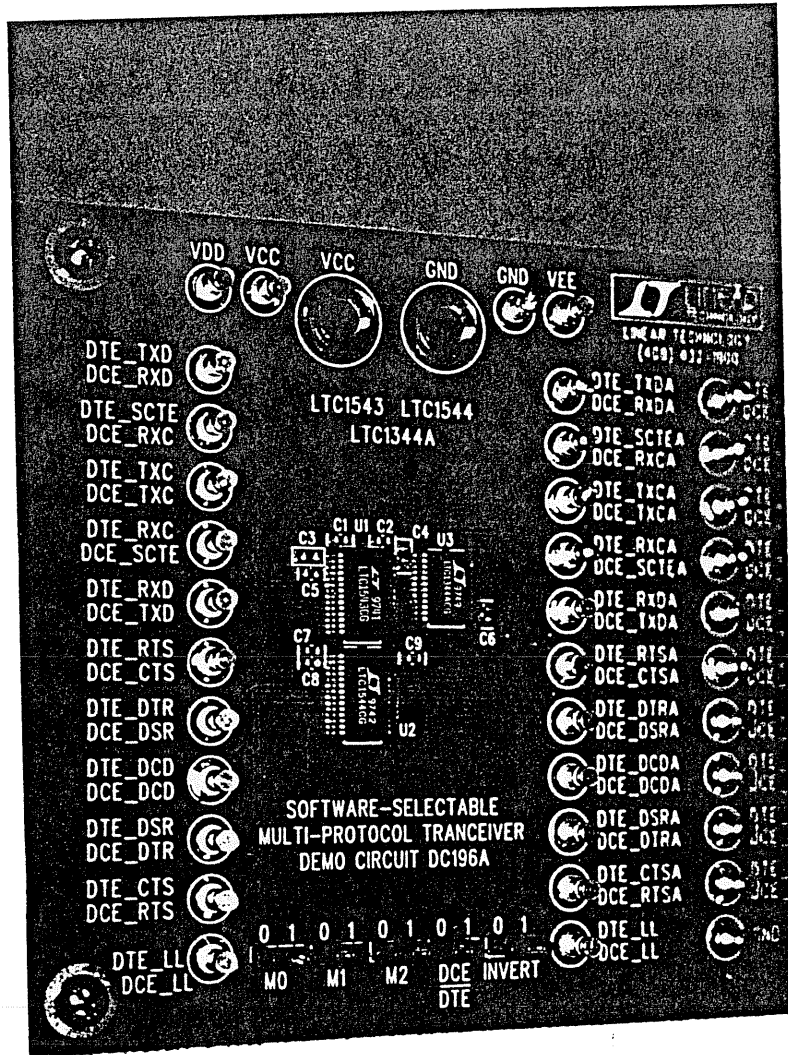
The tested implementation has not been shown by conformance assessment to be non-conforming to NET 2, Clause 10 (CCITT X.25, Layer 3, Packet Layer).

- () Switched Virtuel Channel (SVC) supported and tested
- () Permanent Virtuel Channel (PVC) supported and tested

Test Carried out:

Not Applicable

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8 Observations

Note 1: The capacitance was below the measurable limit.

