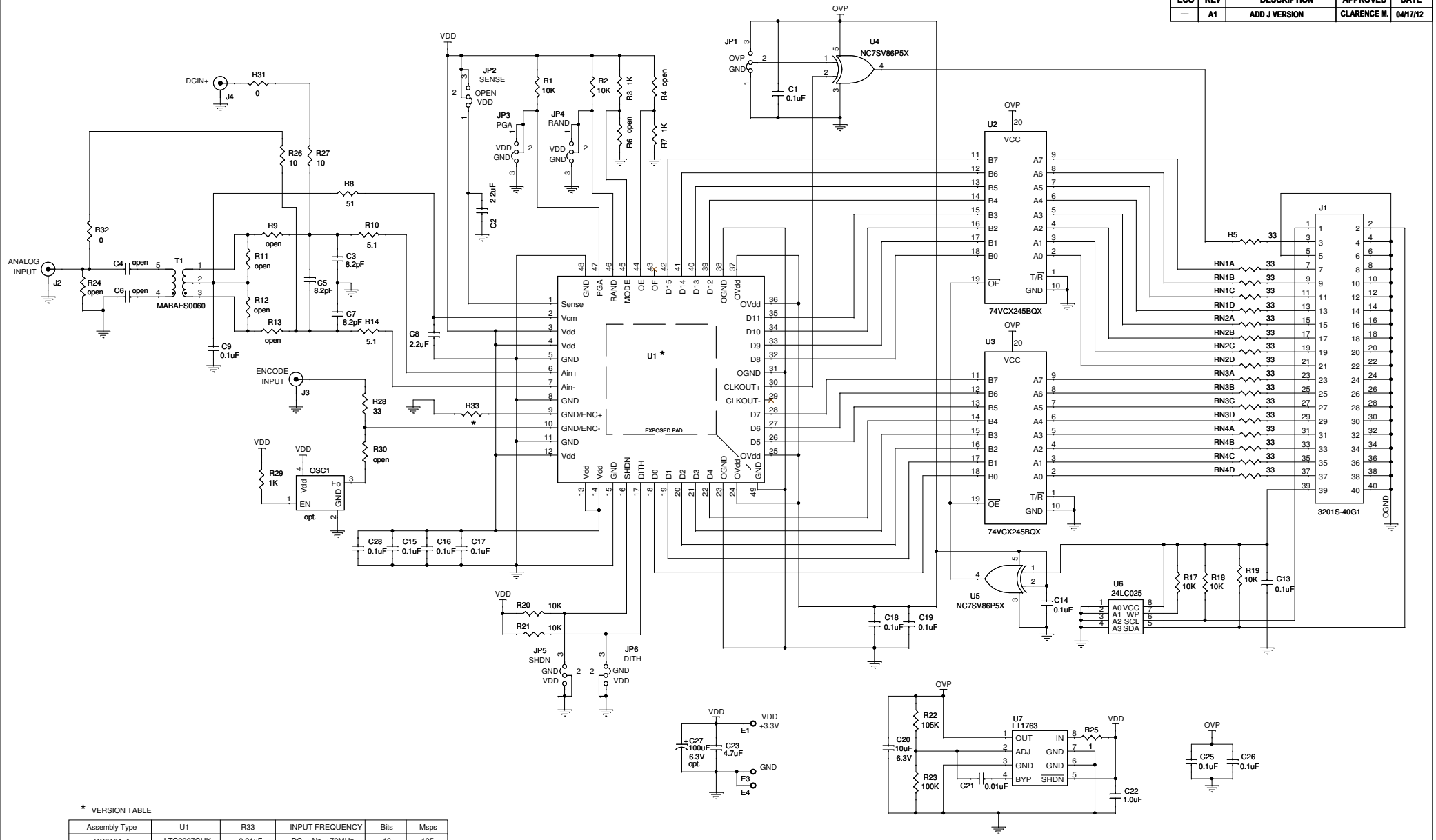
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	A1	ADD J VERSION	CLARENCE M.	04/17/12



* VERSION TABLE

Assembly Type	U1	R33	INPUT FREQUENCY	Bits	Msps
DC919A-A	LTC2207CUK	0.01uF	DC < Ain < 70MHz	16	105
DC919A-B	LTC2206CUK	0.01uF	DC < Ain < 70MHz	16	80
DC919A-C	LTC2205CUK	0.01uF	DC < Ain < 70MHz	16	65
DC919A-D	LTC2204CUK	0.01uF	DC < Ain < 70MHz	16	40
DC919A-E	LTC2203CUK	0 OHM	DC < Ain < 70MHz	16	25
DC919A-F	LTC2202CUK	0 OHM	DC < Ain < 70MHz	16	10
DC919A-G	LTC2207CUK-14	0.01uF	DC < Ain < 70MHz	14	105
DC919A-H	LTC2206CUK-14	0.01uF	DC < Ain < 70MHz	14	80
DC919A-I	LTC2205CUK-14	0.01uF	DC < Ain < 70MHz	14	65
DC919A-J	LTC2201CUK	0 OHM	DC < Ain < 70MHz	16	20

CONTRACT NO.

APPROVALS		DATE
DRAWN	June Wu	04/17/12
CHECKED		
APPROVED		
ENGINEER	D. Redmayne	04/17/12
DESIGNER		

Tuesday, April 17, 2012

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TITLE: LTC2203 FAMILY
16-BIT HIGH SPEED ADC

SIZE	CAGE CODE	DWG NO	REV
		DC919A	A1

SCALE: | FILENAME: | SHEET 1 OF 1