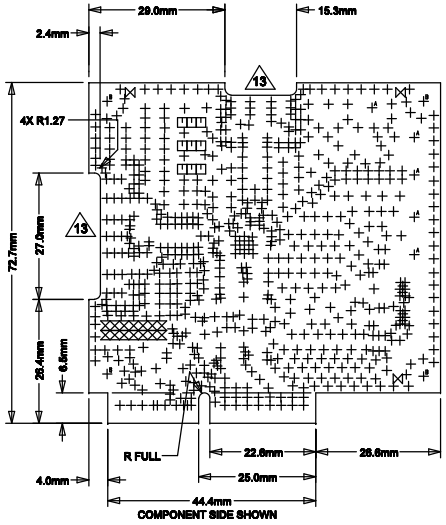
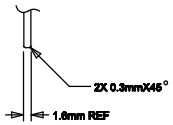
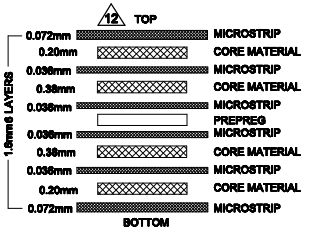
DWG NO.	SH	REV
DC1369A	1	3

LTC CONFIDENTIAL - For Customer Use Only

REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	3	PROTO	06/13/08	







NOTES: UNLESS OTHERWISE SPECIFIED,

1. ARTWORK P/N DC1369A REV 3.
2. FAB PER IPC-A-600
3. PCB SHALL MEET RoHS COMPLIANCE.
4. MATERIAL:
   - A. LAMINATE: EPOXY FIBERGLASS, NEMA GRADE FR-4 FLAMMABILITY RATING: 94 V-2 MINIMUM .
   - B. THICKNESS: 1.6 +/- 0.1 mm
   - C. CLAD: 2 OZ. (50um) EXTERNAL LAYERS. 1 OZ. (25um) COPPER ON TWO INTERNAL LAYERS.
5. FINISH:
   - A. COATING: SOLDERMASK OVER BARE COPPER, COLOR, GREEN LPI.
   - B. PLATING: GOLD IMMERSION BOTH SIDES.
   - C. SILKSCREEN: COMPONENT SIDE WITH WHITE NON-CONDUCTIVE INK.
   - D. FINGER AREA: GOLD PLATE TO MIN. OF 0.0008mm PER MIL-G-45204, TYP II, CL1, GRADE C, OVER 0.005mm LOW STRESS NICKEL PER QQ-N-290, CL2. SOLDER MASK TYPE: WET PHOTO-IMAGEABLE
6. TOLERANCES:
   - A. WARPAGE: (0.075mm) MAX. ALONG THE LONGEST DIAG. AS PER IPC-TM650 METHOD 2.4.22.
   - B. ETCHING: +0.03mm, -0.3mm OF MASTER PATTERN.
   - C. REGISTRATION: 0.075mm MAX.
   - D. MASK THICKNESS: 0.025mm MIN., 0.075mm MAX.
   - E. HOLE PLATING: 22um +/- 3um.
7. SIZE:
   - A. CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00 ARE PRIMARY DATUMS.
8. HOLE DIAMETERS ARE POST-PLATED SIZES.
9. DEBURR AND BREAK ALL SHARP EDGES.
10. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
11. OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 1.143mm TRACE. SEE STACKUP DIAGRAM.

12 0.4mm THICKNESS REFLECTS DIELECTRIC CONSTANT OF 4.1@ 2GHz SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTIRC CONSTANT DEVIATIONS.

13 INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE.

SIZE	QTY	SYM	PLATED	TOL
0.25	726	+	YES	+/-0.08
0.8	9	□	YES	+/-0.08
0.9	14	⧖	YES	+/-0.08
1.8	3	⋈	NO	+/-0.13
2.4	5	+A	YES	+/-0.13
4.7	4	+B	NO	+/-0.13

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS
TOLERANCES ON ANGLE ±1°
.XX ± 0.25
.XXX ± 0.127

THIRD ANGLE PROJECTION

DO NOT SCALE DRAWING

CONTRACT NO

APPROVALS	DATE
DRAWN MI	12/14/07
CHECKED	
APPROVED	
ENGINEER	

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900
Fax: (408)434-0507

TITLE
FAB, LTC2261CUJ, HIGH SPEED LOW POWER 125MSPS ADC FAMILY, LVDS

SIZE	CAGE CODE	DWG NO	REV
B		DC1369A	3

SCALE 1/1 | FILENAME:1369A-3.PCB | SHEET 1 OF 1