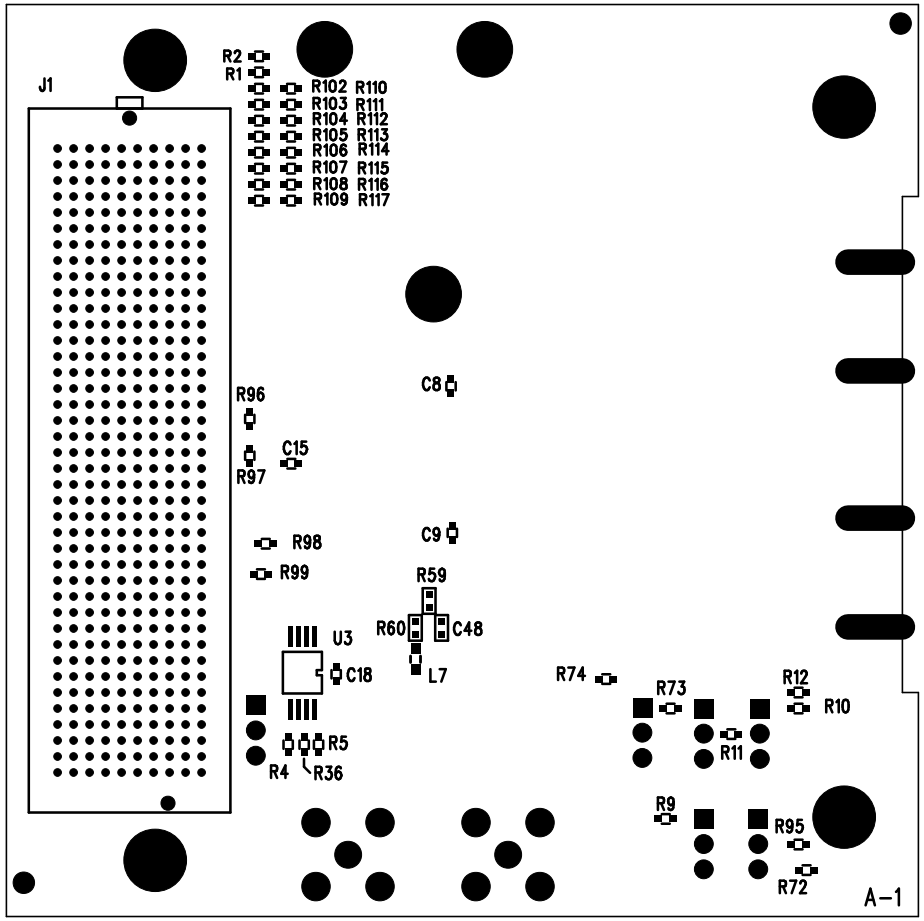
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	1	1ST PROTOTYPE	CLARENCE M.	10-22-10



APPROVALS	
PCB DES.	M.H.
APP ENG.	CLARENCE M.
SCALE = NONE	

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TITLE: BOTTOM ASSEMBLY DRAWING
16-BIT DUAL, HIGH SPEED, SERIAL LVDS ADC

SIZE	IC NO.	REV
N/A	LTC2195CUKG DEMO CIRCUIT 1763A	1

FILENAME: DC1763A-1.PCB