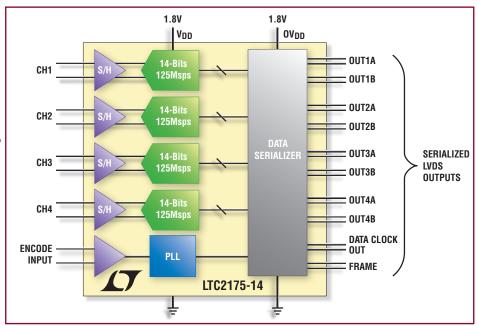
14-Bit/12-Bit 125Msps Quad/Dual ADC Family

Features

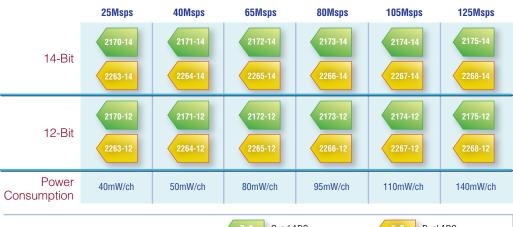
- Quad/Dual-Channel Simultaneous Sampling ADCs (LTC2175/LTC2268)
- 73.1dB SNR (14-Bit Resolution)
- 88dB SFDR
- Low Power: 140mW/Channel at 125Msps
- Single 1.8V Analog & Digital Supplies
- Serial LVDS Outputs
- Selectable Input Ranges: 1V_{P-P} to 2V_{P-P}
- 800MHz Full-Power Bandwidth S/H
- Optional Data Output Randomizer
- Optional Clock Duty Cycle Stabilizer
- Shutdown and Nap Modes
- Serial SPI Port for Configuration
- Pin-Compatible 14-Bit and 12-Bit Versions
- Easy Multichannel Evaluation Using PScope™ Tool

140mW/Channel



One-Third the Power of Comparable High Speed ADCs

Our newest high speed ADC family achieves one-third the power consumption of alternate solutions without compromising AC performance. Operating from a low 1.8V supply, the 14-bit, 125Msps LTC®2175 dissipates only 140mW/channel while maintaining 73.1dB SNR and 88dB SFDR at baseband. Digital outputs can be configured as single lane (<65Msps) or dual lane serial LVDS.



7x8 Quad ADC Serial LVDS Outputs





