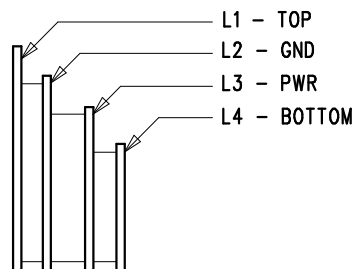


LAYER STRUCTURE



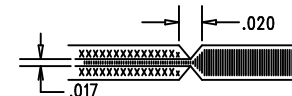
REVISION HISTORY


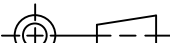
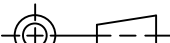
ECO	REV	DESCRIPTION	APPR	DATE
	1	1ST PROTOTYPE	G. HOOVER	11-02-09

SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003"
0.055	15	□	YES	+/-0.003"
0.01	214	◇	YES	+/-0.003"
0.035	31	⊠	YES	+/-0.003"
0.02	19	⊕ ^B	YES	+/-0.003"
0.188	4	⊕ ^C	YES	+/-0.003"
0.064	9	⊕ ^D	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS			 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
		NAME	DATE	
	PCB DES.	K. TRAN	11-02-09	
	ENG.	G. HOOVER	11-02-09	TITLE: FABRICATION DRAWING
THIRD ANGLE PROJECTION				LOW NOISE HIGH SPEED SAR ADC FAMILY
				SIZE A
DO NOT SCALE DRAWING	SCALE: NONE			IC NO. LTC23XXCLX FAMILY REV. DEMO CIRCUIT 1500A
FILENAME: DC1500A.PCB				SHT 1 of 1