

TOP SILKSCREEN
 LINEAR TECHNOLOGY
 DC1010A-1 DATE: 03-26-07
 LTC2493/2489/2487CDE
 2/4 CHANNEL 24,16,16/PGA-BIT ADC WITH I2C


VERSIONS	U1	BITS
-A	LTC2493CDE	24
-B	LTC2489CDE	16
-C	LTC2487CDE	16/PGA

REVISIONS			
REV	DESCRIPTION	APPR	DATE
A-1	FINAL FABRICATION	MT	03-26-07

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JP1-JP4 PIN 1 AND 2.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
5. MARKED LTC DEMO CIRCUIT ASSY DASH LETTER AND THE IC PART NUMBER (SEE TABLE) WITH BLACK PERMANENT MARKER, APPROX. WHERE SHOWN. (SEE BELOW EXAMPLE).

EXAMPLE: MARKED

APPROVALS			 <div> 1630 MCCATHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div>	
	INIT	DATE		
DRAWN			TITLE: TOP ASSEMBLY DRAWING 2/4 CHANNEL 24,16,16/PGA-BIT ADC WITH I2C	
CHECK				
DESIGN	KIM T.	03-26-07		
ENGR	MARK T.	03-26-07		
SCALE = NONE			SIZE A	DEMO DC1010A-1 LTC2493/2489/2487CDE
			REV. A-1	
			SHT 1 of 1	