

*** FOR ASSEMBLY**

VERSIONS	U1	BITS
-A	LTC2493CDE	24
-B	LTC2489CDE	16
-C	LTC2487CDE	16/PGA

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. INSTALL SHUNT ON JP1-JP4 PIN 1 AND 2.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS


DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: MARK T.

DESIGNER:



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TITLE: SCHEMATIC

2/4 CHANNEL 24,16,16/PGA-BIT ADC WITH I2C

SIZE A	DWG NO. DC1010A-1	REV A-1
DATE: Monday, March 26, 2007		SHEET 1 OF 1