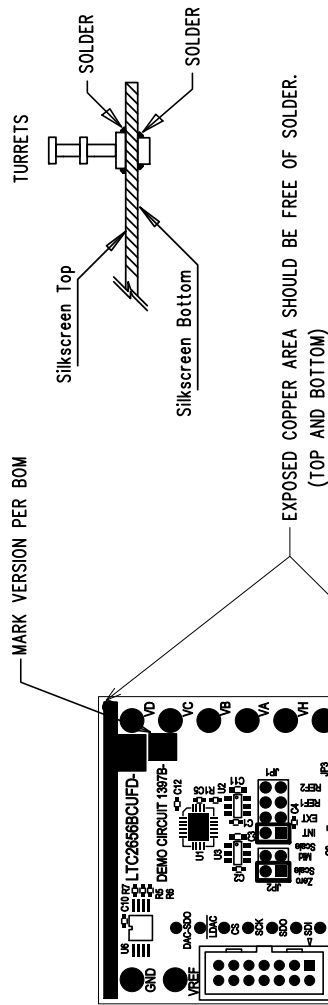


REVISIONS			
REV	DESCRIPTION	APPR	DATE




Silkscreen Top

### NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
3. JUMPER SHUNTS INSTALLATION: JP1 : INT  
JP2 : Zero Scale  
JP3 : Enable

## ASSY DRAWING

<div>APPROVALS</div>			<div>LINEAR TECHNOLOGY</div>		1630 McCarthy Blvd. Milpitas, CA 95035 Phi: (408)432-1900	
PCB DESIGNER			INIT	DATE		
ENGINEER			J. WU	11/10/10		
CHECK			L. CHEN	11/10/10		
TITLE:			LTC2656BCUFD			
			12/16-BIT, OCTAL DAC W/REFERENCE			
SIZE	DEMO	DC1397B	REV.	1		
SCALE = NONE			SHT 1 of 1			