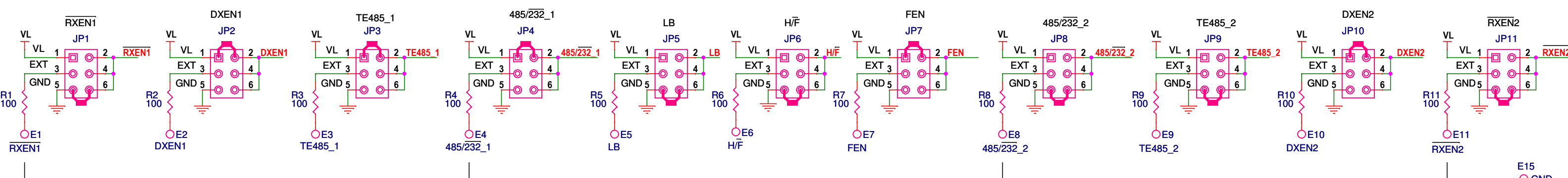
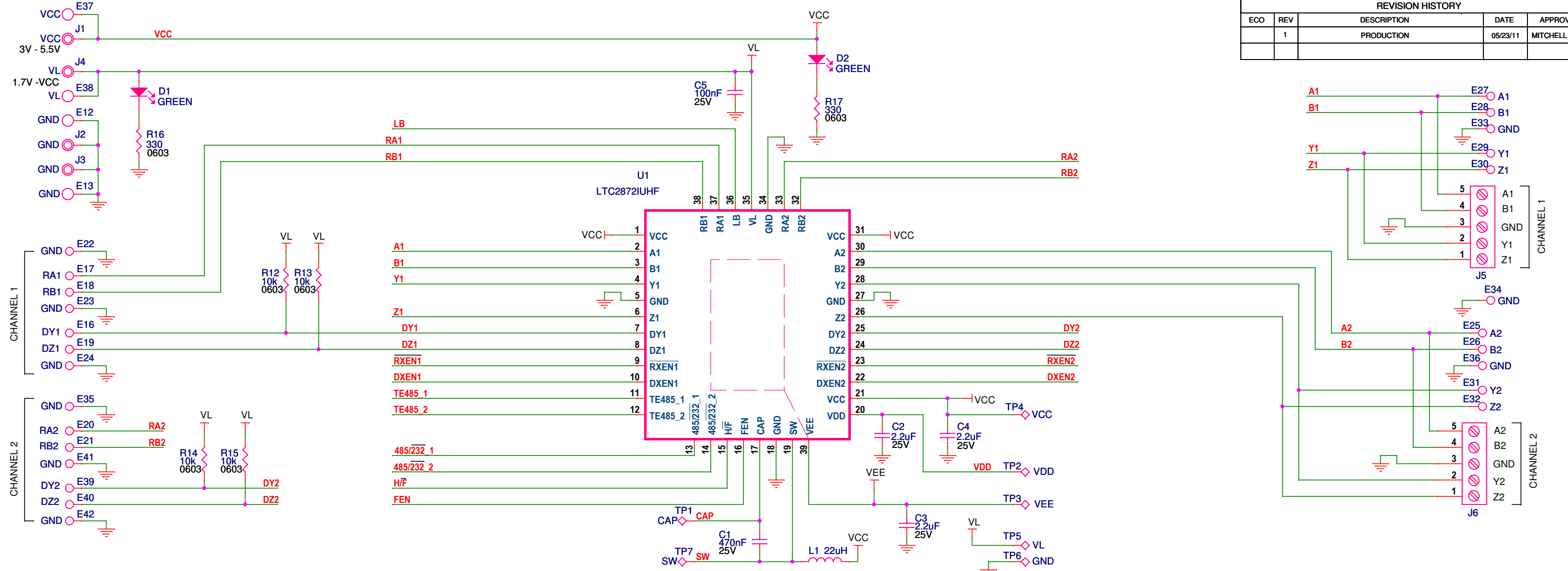
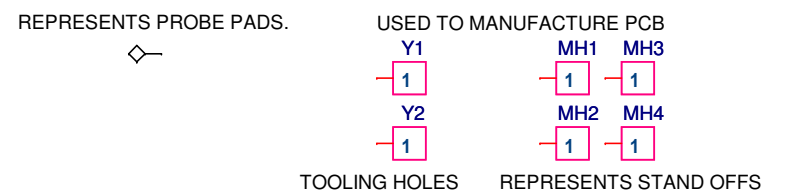


REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	1	PRODUCTION	05/23/11	MITCHELL LEE



NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS AND CAPACITORS ARE 1206.



<b>CUSTOMER NOTICE</b> LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.	<b>APPROVALS</b> PCB DES. MI APP ENG. MITCHELL L.				1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only
	<b>TITLE: SCHEMATIC</b> <b>RS232/RS485 DUAL MULTIPROTOCOL TRANSCEIVER WITH INTEGRATED TERMINATION</b>			SIZE N/A IC NO. LTC2872IUHF DEMO CIRCUIT 1851A	REV. 1
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE	DATE: Wednesday, November 02, 2011	SHEET 1 OF 1	