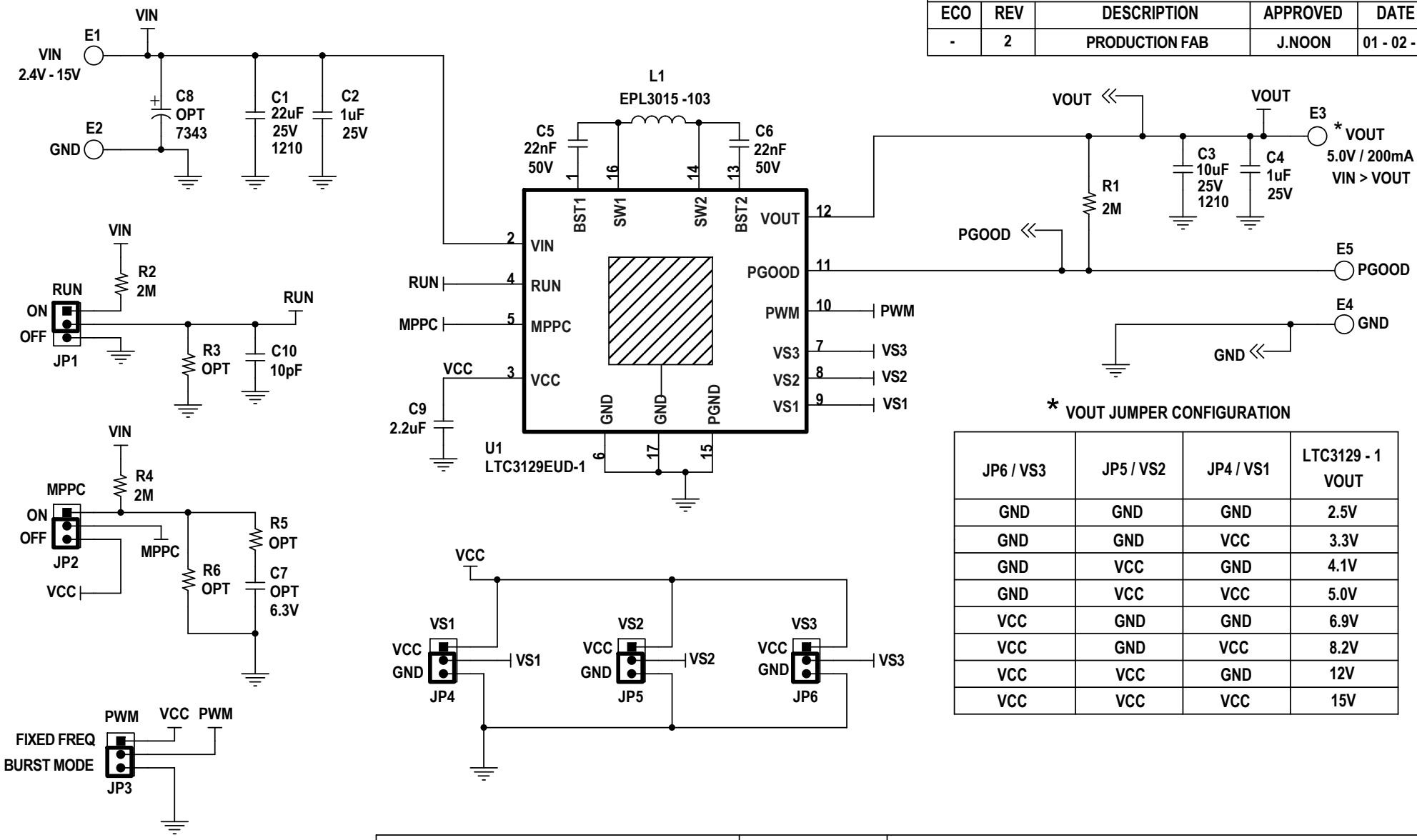
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION FAB	J.NOON	01 - 02 - 13



* VOUT JUMPER CONFIGURATION

JP6 / VS3	JP5 / VS2	JP4 / VS1	LTC3129 - 1 VOUT
GND	GND	GND	2.5V
GND	GND	VCC	3.3V
GND	VCC	GND	4.1V
GND	VCC	VCC	5.0V
VCC	GND	GND	6.9V
VCC	GND	VCC	8.2V
VCC	VCC	GND	12V
VCC	VCC	VCC	15V

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402
   ALL CAPACITORS ARE IN MICROFARADS, 0603.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	NC
APP ENG.	J.NOON

SCALE = NONE

LINEAR TECHNOLOGY
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TITLE: SCHEMATIC

15V, 200mA SYNCHRONOUS BUCK - BOOST DC / DC CONVERTER WITH 1.3uA QUIESCENT CURRENT

SIZE	IC NO.	REV.
N/A	LTC3129EUD-1 DEMO CIRCUIT 1923A	2

DATE: 01 - 02 - 13

SHEET 1 OF 1