



FOR BOARD ASSEMBLY

*	U1	JP2	C8	R1	R3	R4	R6	R7	R8	VOUT
DC863A-A	LTC3203BEDD	OPT	4.7PF	267K	280K	105K	0	OPT		3.3V@500mA
DC863A-B	LTC3203EDD-1	INSTALLED	OPT	OPT	OPT	3.32M	OPT	402K	357K	4.5V OR 5V@500mA BURST MODE
DC863A-C	LTC3203BEDD-1	INSTALLED	OPT	OPT	OPT	3.32M	OPT	402K	357K	4.5V OR 5V@500mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
2. INSTALL SHUNT ON JP1(1&2), JP2 (1&3, 2&4).

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: JULIAN Z.

DESIGNER:



1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

500mA LOW NOISE, HIGH EFFICIENCY CHARGE PUMP

SIZE

DWG NO.

DC863A-LTC3203

REV

DATE: Wednesday, June 28, 2006

SHEET 1 OF 1