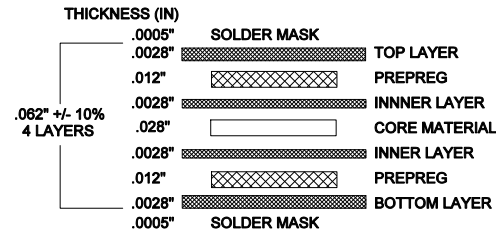


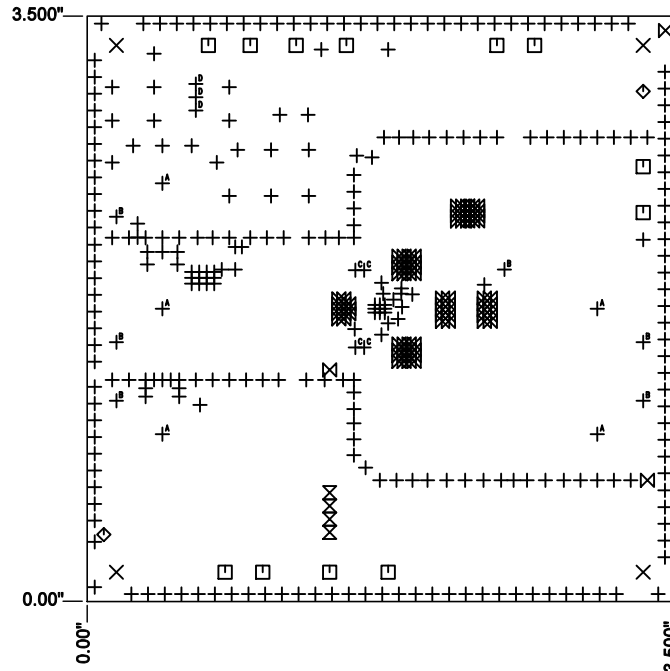
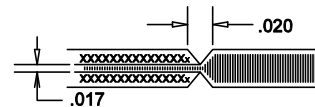
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	03	PRODUCTION	WL	09-13-21

LAYER STRUCTURE

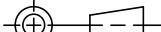



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600, CLASS 2.
- MATERIAL: - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. Cu ON EXTERNAL AND 2 OZ. Cu ON INTERNAL LAYERS
 - FLAMMABILITY RATING: 94 V-O MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING: - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH: - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.005	4	⊕	YES	+/-0.003"
0.01	270	+	YES	+/-0.003"
0.012	88	⊗	YES	+/-0.003"
0.035	4	⊗	YES	+/-0.003"
0.04	3	⊕	YES	+/-0.003"
0.065	12	⊗	YES	+/-0.003"
0.07	2	⊕	NO	+/-0.003"
0.095	6	⊕	YES	+/-0.003"
0.19	4	⊗	YES	+/-0.003"
0.205	5	⊕	YES	+/-0.003"

<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = ±0.01"</div> <div>0.XXX" = ±0.005"</div> <div>INTERPRET DIM AND TOL</div> <div>PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>	<div>APPROVALS</div>		<div> POWER BY LINEAR™</div> <div>FOR ADI CUSTOMER USE ONLY</div>	
	PCB DES	NC	<div>TITLE: FABRICATION DRAWING</div> <div>3.3V TO 1.0V AT 12A, 2MHz DUAL-PHASE</div> <div>LOW OUTPUT RIPPLE BUCK REGULATOR</div>	
	APP ENG	WL		
		<div>SIZE</div> <div>N/A</div>	<div>IC NO.</div> <div>LTC3312SA</div> <div>DEMO CIRCUIT 3092A</div>	<div>REV</div> <div>03</div>
SCALE = NONE		SHT 1 OF 1		