



U2, U3, U4 FUNCTION TABLE

I2	I1	I0	Y
L	L	L	L
L	L	H	H

$$Y = (I_0) \cdot (I_2) + (I_1) \cdot (I_2)$$

OVERVOLTAGE TOLERANT BUFFERS
TRANSLATE THE HIGH PULL-UP
VOLTAGES FROM THE LTC3330 TO THE
VOUT VOLTAGE DRIVING THE
PROCESSOR I/O BUS, WHICH IS VOUT.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES. NC

APP ENG. JD

SCALE = NONE



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TITLE: SCHEMATIC

NANOPOWER BUCK - BOOST DC / DC
WITH ENERGY HARVESTING BATTERY LIFE EXTENDER

SIZE

N/A

IC NO.

LTC3330EUH
DEMO CIRCUIT 2048A

REV.

3

DATE: 7 - 29 - 13

SHEET 2 OF 2