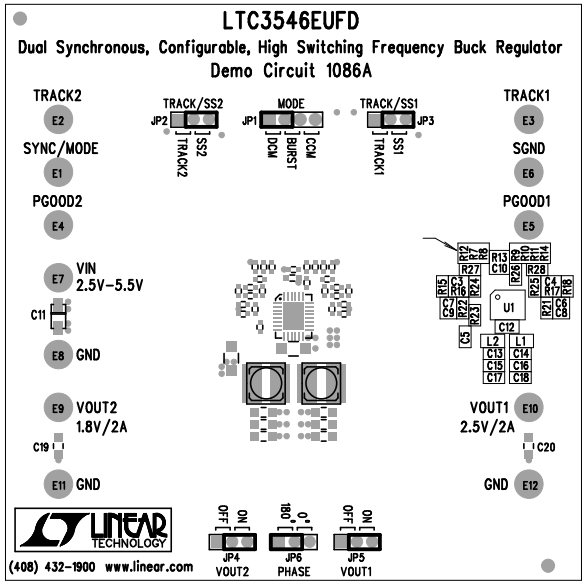
REVISIONS			
REV		APPR	DATE

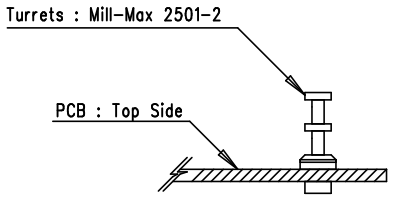
Silkscreen Top



JP1 = DCM
JP2 = SS2
JP3 = SS1
JP4 = ON
JP5 = ON
JP6 = 180°

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC–A–610.



Assembly Drawing Top

APPROVALS		
	INIT	DATE
DRAWN		
CHECK		
DESIGN		
ENGR		
SCALE = NONE		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432–1900

TITLE: LTC3546EUFD, Dual Synchronous, Configurable, High Switching Frequency Buck Regulator		
SIZE NONE	Demo Circuit 1086A	REV. A
DESIGN #		SHT 1 of 1