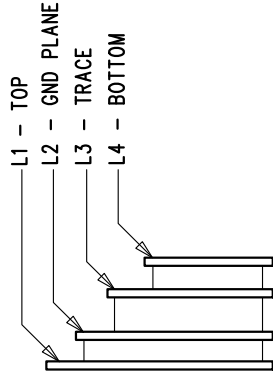


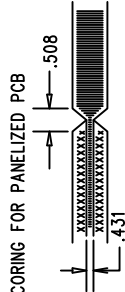
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
	2	PRODUCTION	HAORAN W.
			2-4-14

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE .16 +/- 0.07mm  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.07mm IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. PCBs ARE TO BE RoHS COMPLIANT.



UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN MILLIMETERS  
TOLERANCES:  
0.XX = ± 0.25  
0.XXX = ± 0.127  
INTERPRET DIM AND TOL  
PER ASME Y14.5M-1994  
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	HZ
APP ENG.	HAORAN W.
SCALE = NONE	

9330 MCCARTHY BLVD  
MILPITAS, CA 95035  
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WWW.LINEARTECH.COM  
LTC CONFIDENTIAL  
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING

DUAL WIDE VIN SYNCHRONOUS BUCK REGULATOR  
WITH ULTRA-LOW QUIESCENT CURRENT

SIZE	IC NO.	REV
N/A	LTC3622EMSE	2
	DEMO CIRCUIT 2004A	
FILENAME: DC2004A-2.PCB		SHT 1 OF 1

SIZE	QTY	SYM	PLATED	TOL
4.7498	4	+	YES	+/-0.07
0.9	36	X	YES	+/-0.07
2.413	13	□	YES	+/-0.07
0.254	45	◇	YES	+/-0.07