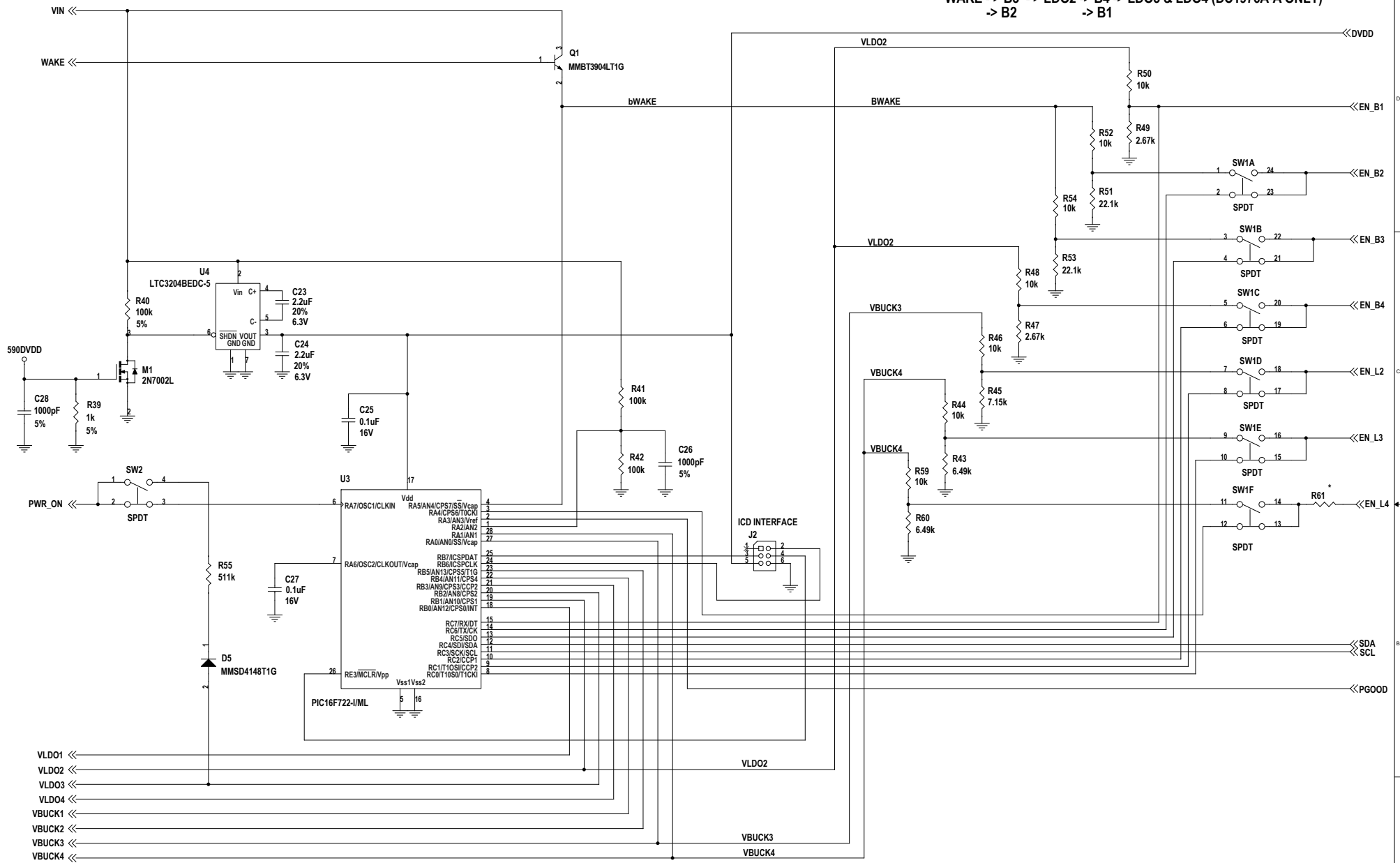


POWER UP SEQUENCE

WAKE -> B3 -> B4 -> LDO2 & LDO4 (DC1976A-A ONLY)
-> B2 -> B1



NOTES: UNLESS OTHERWISE SPECIFIED -
ALL RESISTORS ARE $\pm 1\%$, 0402
ALL CAPACITORS ARE $\pm 10\%$, 50V, 0402

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES. NC
APP ENG. GEORGE B.

SCALE = NONE



TITLE: SCHEMATIC

PMIC FOR APPLICATION PROCESSORS

SIZE N/A IC NO. LTC3676EUJ / LTC3676EUJ-1
DATE: 8 - 6 - 13 DEMO CIRCUIT 1976A - A / B REV. 5

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SHEET 2 OF 2