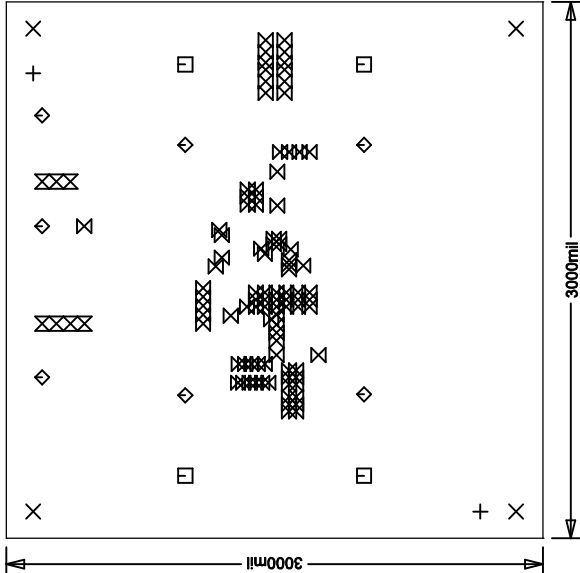
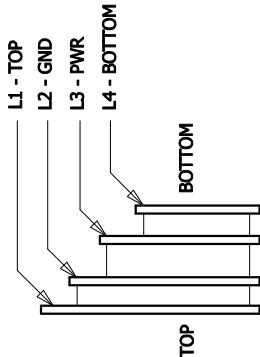


Demo Circuit 1290A
Linear Tech. Corp.



SHOWN FROM COMPONENT SIDE

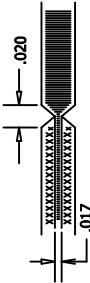
LAYER STRUCTURE



REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
-	1	PRODUCTION	Johans
			09-16-09

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003
IN RELATION TO CENTER
5. FINISH: -SNOBIC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RHHS COMPLIANT.
8. SCORING FOR PANELIZED PCBs:



SIZE	QTY	SYM	PLATED	TOL
70	2	+	NO	+/- 3Mil
187	4	X	YES	+/- 3Mil
205	4	□	YES	+/- 3Mil
94	7	◇	YES	+/- 3Mil
10	95	⊗	YES	+/- 3Mil
31	7	⊗	YES	+/- 3Mil

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON ANGLE ±1°
0.0XX = ± 0.01
0.00XX = ± 0.005
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



DO NOT SCALE DRAWING

APPROVALS

	INIT	DATE
PCB DES.	PSB	09-16-09
ENG.	Johans	09-16-09

SCALE: NONE



TITLE: FABRICATION DRAWING

HIGH FREQUENCY SYNCHRONOUS
STEP-DOWN CONVERTER

SIZE	IC NO.	REV.
A	LTC3775EUD DEMO CIRCUIT 1290A	1

FILENAME: DC1290A-1.PCB

SHT 1 of 1