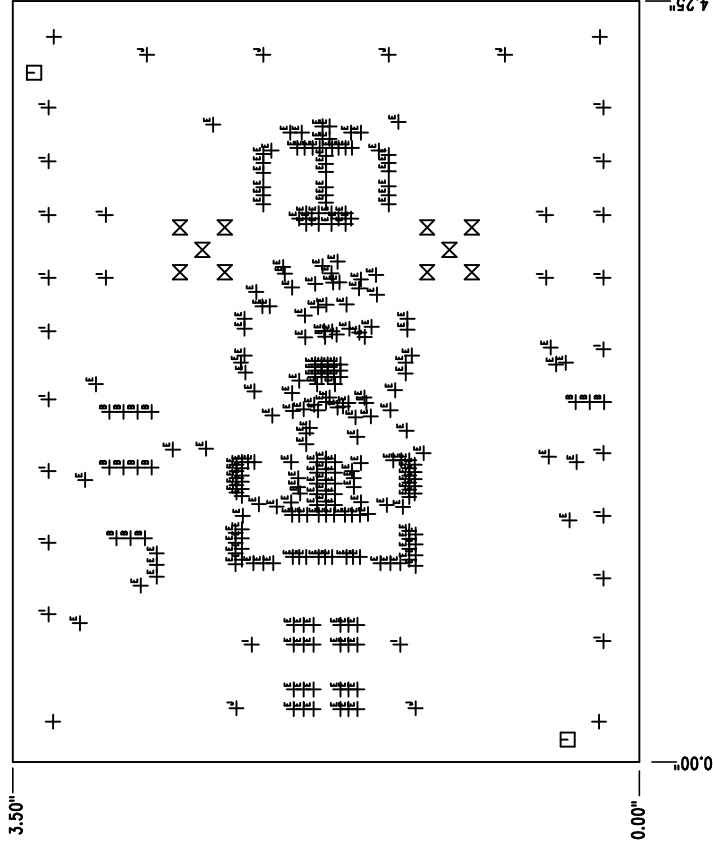
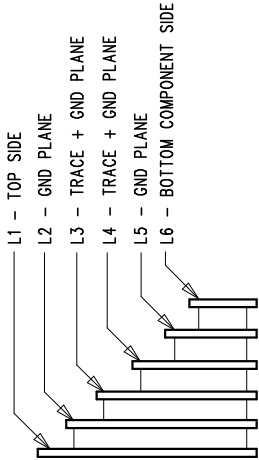


REVISION HISTORY			
ECO REV	DESCRIPTION	APP. ENG.	DATE
-	2	MIKE S.	5-11-11

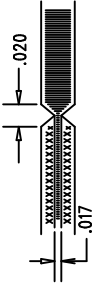


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/- 0.003"
IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS	
DIMENSIONS ARE IN INCHES		PCB DES.	HZ
TOLERANCES:		APP. ENG.	MIKE S.
0.XX" = +/- 0.01"			
0.XXX" = +/- 0.005"			
INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
TITLE: FABRICATION DRAWING		WIDE INPUT RANGE, HIGH EFFICIENCY	
DUAL STEP-DOWN DC-DC CONVERTER		REV	
SIZE		IC NO.	LTC3838EFE
N/A		DEMO CIRCUIT	1626A-A/B
SCALE = NONE		SHT 1 OF 1	

LINEAR
TECHNOLOGY

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LTC CONFIDENTIAL -
FOR CUSTOMER USE ONLY

FILENAME: DC1626A-2.PCB