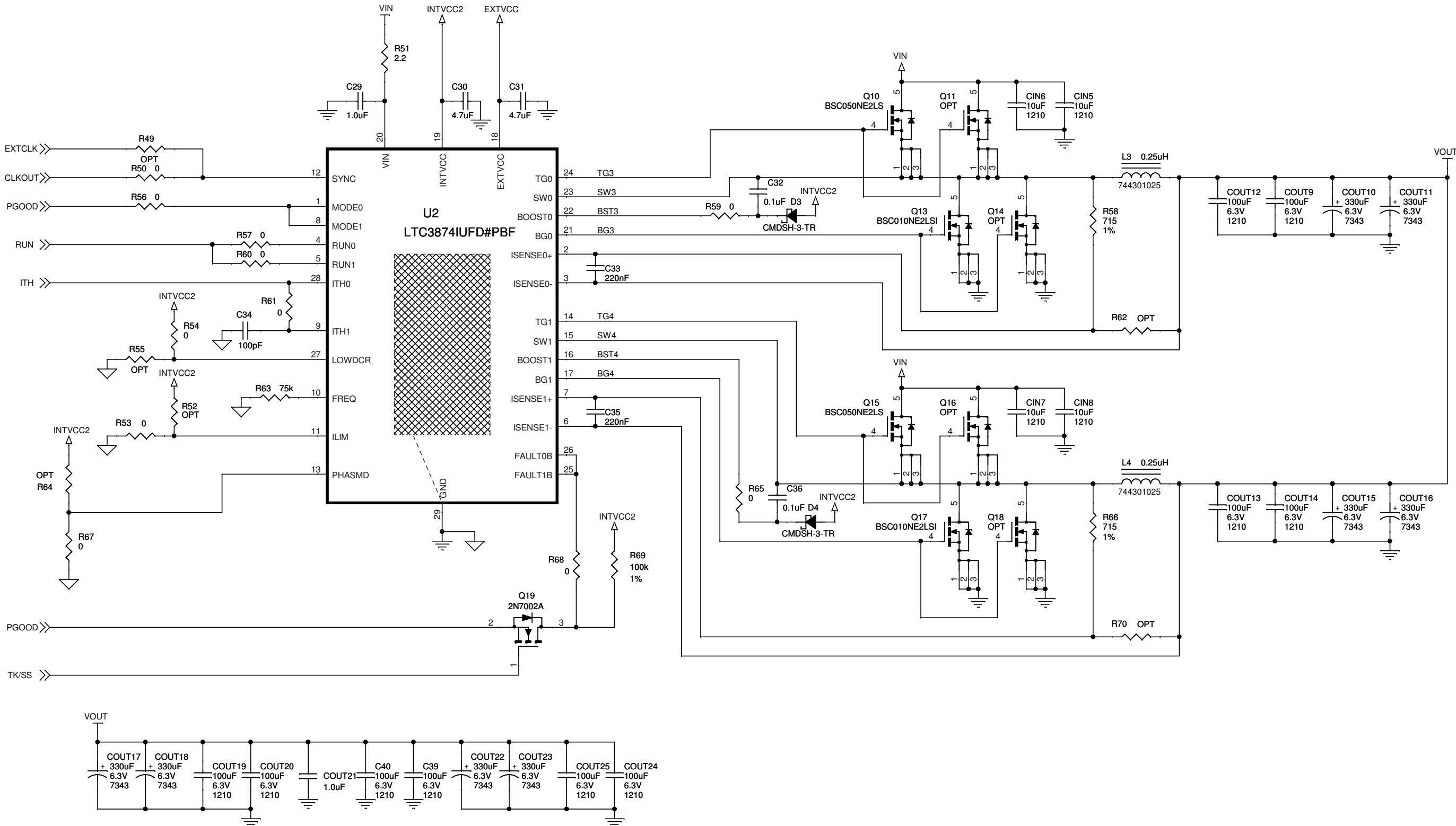


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LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

B DES.	MI			
P ENG.	YINGTI Y.	TITLE: SCHEMATIC HIGH EFFICIENCY POLYPHASE , SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DCR INDUCTOR		
		SIZE N/A	IC NO. LTC3875EUJ / LTC3874EUFD DEMO CIRCUIT 2142A	REV. 2
SCALE = NONE		DATE:	02/26/14 08:23:25	SHEET 1 OF 3



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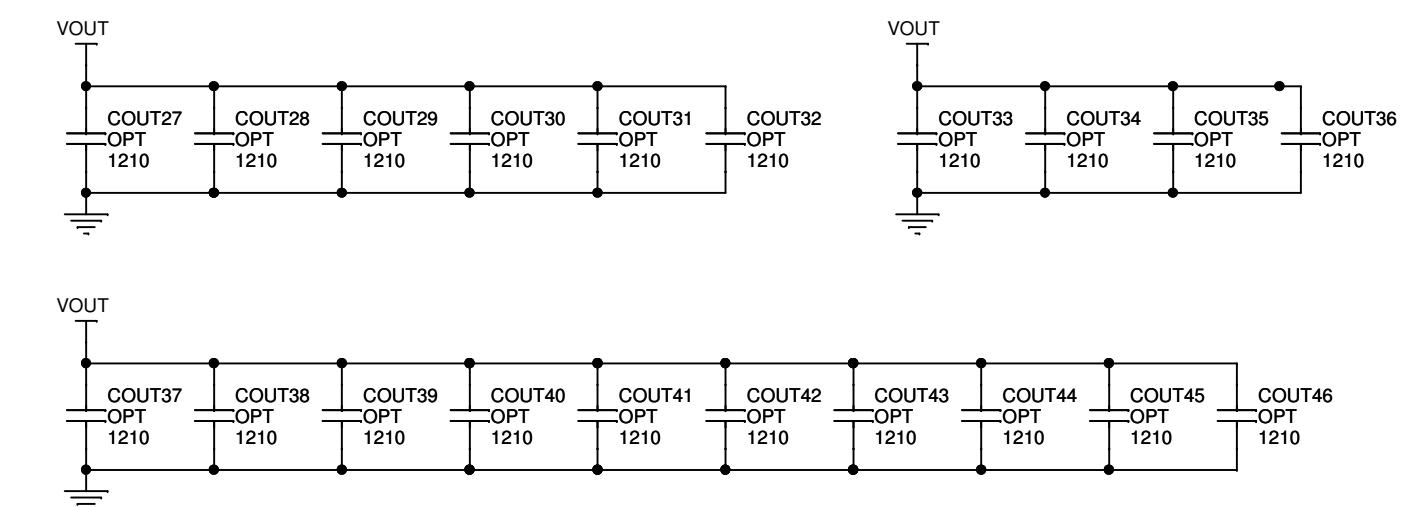
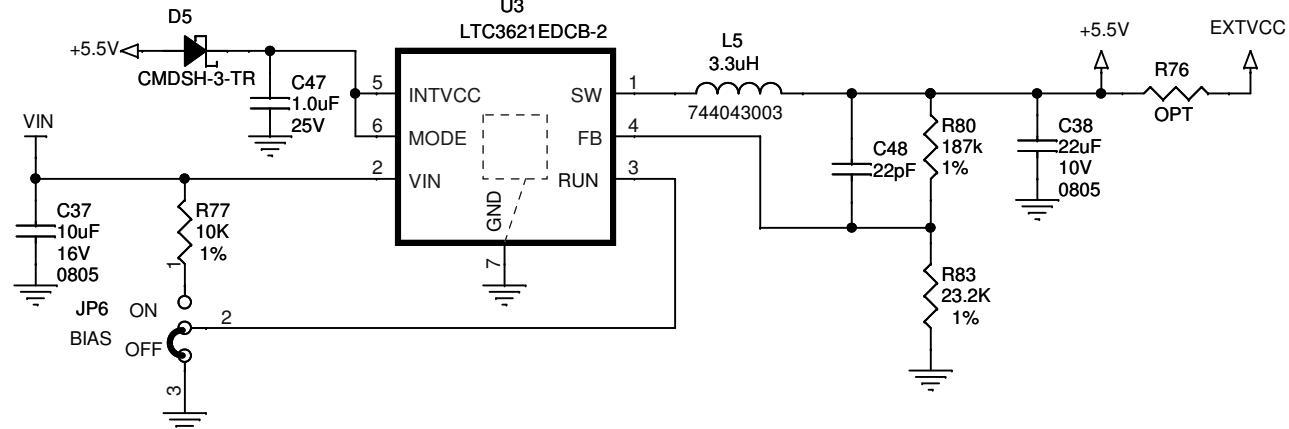
APPROVALS

PCB DES.	MI	TITLE: SCHEMATIC HIGH EFFICIENCY POLYPHASE, SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DCR INDUCTOR	
APP ENG.	YINGTI Y.	SIZE N/A	IC NO. LTC3875EUJ / LTC3874EUFD DEMO CIRCUIT 2142A
		REV. 2	
		DATE: 03/05/14 13:03:42	SHEET 2 OF 3
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.	SCALE = NONE		

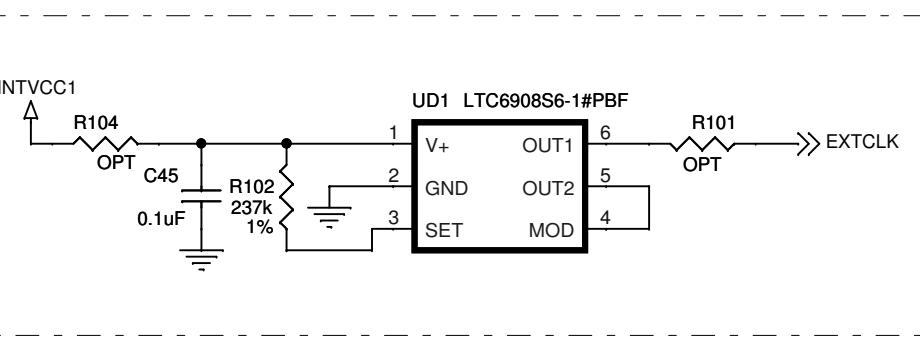
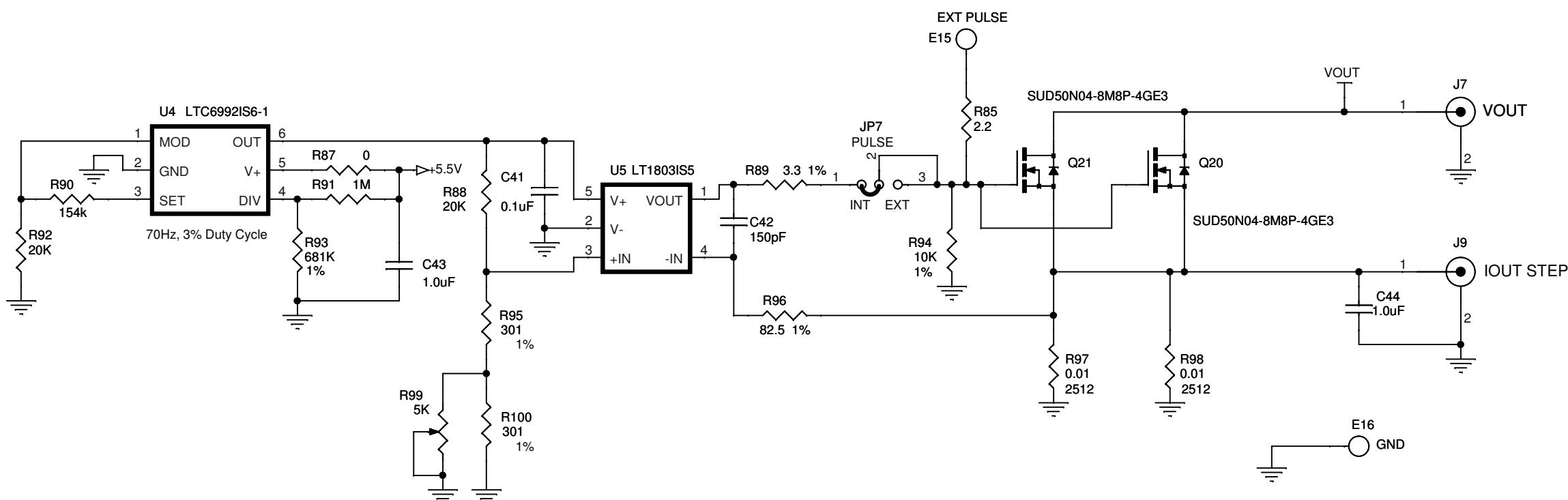


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OPTIONAL 5.5V BIAS



DYNAMIC LOAD CIRCUIT



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APPROVALS

PCB DES.	MI	TITLE: SCHEMATIC HIGH EFFICIENCY POLYPHASE, SYNCHRONOUS BUCK CONVERTER WITH VERY LOW DCR INDUCTOR
APP ENG.	YINGTI Y.	
SIZE N/A	IC NO. LTC3875EUJ / LTC3874EUFD DEMO CIRCUIT 2142A	REV. 2
DATE: 03/05/14 13:05:40	SHEET 3 OF 3	

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE



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