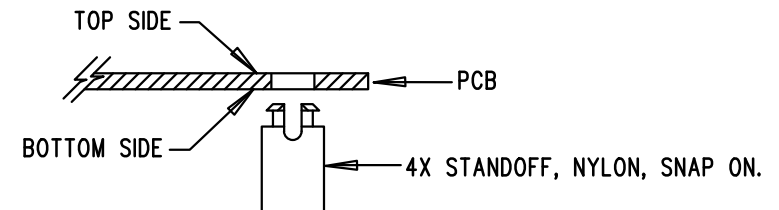



REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
1	2	PRODUCTION REBUILD W CHANGES	RYAN H.	05-24-13

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. NO SHUNT.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



8. THERE ARE 3 ASSEMBLY VERSIONS ON THIS DESIGN: A, B, AND C. SEE BOM TO WRITE THE APPROPRIATE ASSY VERSION AT THE WHITE BLOCK AREA WITH BLACK PERMANENT MARKER.

APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
PCB DES.	KT			
APP ENG.	RYAN H.	TITLE: TOP ASSEMBLY DRAWING		
		HIGH POWER, HIGH EFFICIENCY POE PD INTERFACE WITH INTEGRATED SWITCHING REGULATOR		
		SIZE	IC NO.	REV.
		N/A	LTC4269IDKD-1 DEMO CIRCUIT 1335B	2
SCALE = NONE		FILENAME: DC1335B-2.PCB		SHT 1 OF 2