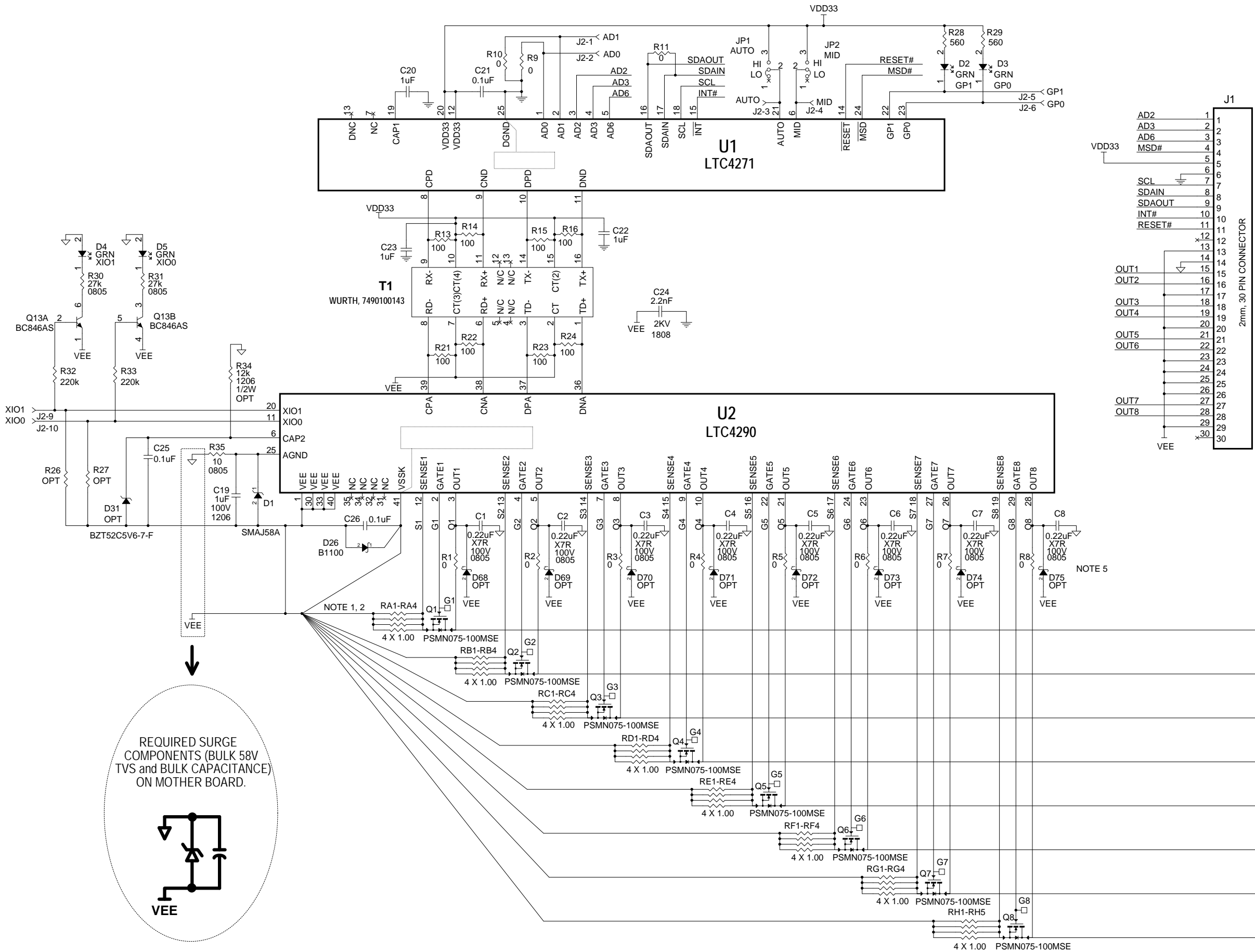
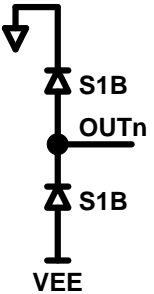


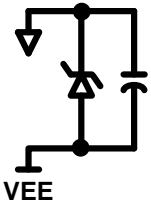
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	REBUILD WITH CHANGE	DILIAN R.	08-18-15



REQUIRED S1B DIODE PROTECTION
SHOWN ON MOTHER BOARD.
See NOTE 3



REQUIRED SURGE
COMPONENTS (BULK 58V
TVS and BULK CAPACITANCE)
ON MOTHER BOARD.



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL SENSE RESISTORS TIE TO VEE AT ONE COMMON POINT.
2. VSSK CONNECTS TO THE COMMON SENSE RESISTOR VEE POINT THROUGH AN ISOLATED KELVIN SENSE TRACE.
3. ONE S1B PROTECTION DIODE FROM VEE TO OUTn AND ONE S1B PROTECTION DIODE FROM OUTn TO AGND SHOWN ON MOTHER BOARD.
4. ALL RESISTORS AND CAPACITORS ARE 0603.
5. OPTIONAL REPLACE R1- R8 WITH 10 OHMS AND D68 - D75 WITH BAT41JFILM FOR INCREASED SURGE PROTECTION.

CUSTOMER NOTICE		APPROVALS		LINEAR TECHNOLOGY	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES. KIM T.	APP ENG. DILIAN R.	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0607 LTC Confidential-For Customer Use Only	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		TITLE: SCHEMATIC 8-PORT PSE DAUGHTER CARD WITH DIGITAL ISOLATION		SIZE N/A	IC NO. LTC4290BIUJ, LTC4271IUF DEMO CIRCUIT 1842B
SCALE = NONE		DATE: Tuesday, August 18, 2015		REV. 1	
				SHEET 1 OF 1	