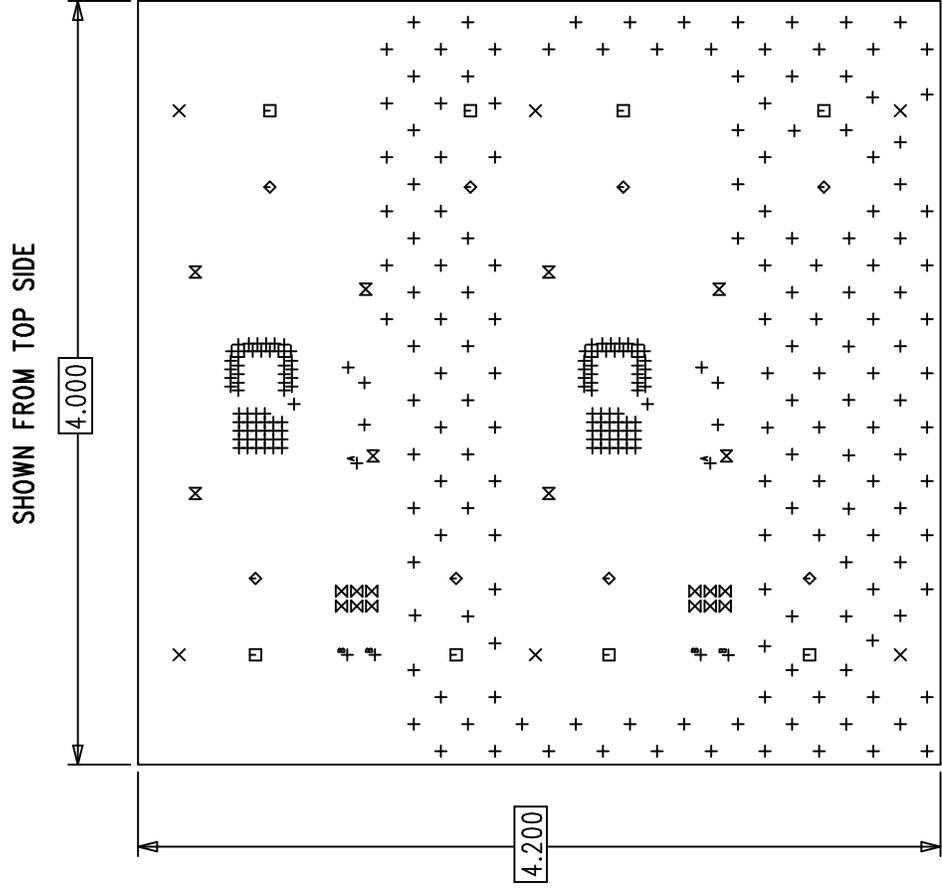


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	4	PRODUCTION	MITCHELL LEE	05-31-12

**NOTES: UNLESS OTHERWISE SPECIFIED**

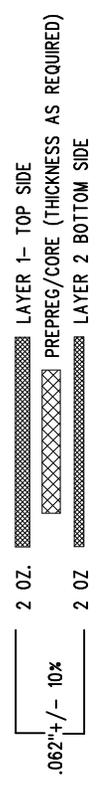
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 2 LAYERS, FINISHED COPPER PLATING SHALL BE 2 OZ, AS PER FIG.1, "LAYER STRUCTURE".  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
- HOLE LOCATION TOLERANCES SHALL BE 6 MILS DIAMETER TRUE POSITION.  
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- BOARDS SHALL BE ROHS COMPLIANT.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD.  
THERE SHALL BE TWO MOUSE BITE PATTERNS ON EACH BOARD EDGE.
- DO NOT INCREASE THE SOLDERMASK APERTURE SIZE FOR THE SOLDERMASK DEFINED PADS ASSOCIATED WITH THESE PARTS: U1A,U1B,CFA,CFB, R1A,R1B, R2A,R2B.



**DRILL CHART**

SIZE	QTY	SYM	PLATED	TOL
20	288	+	YES	+/- 3 MILS
188	6	X	NO	+/- 3 MILS
95	8	□	YES	+/- 3 MILS
207	8	◇	YES	+/- 3 MILS
35	12	⊗	YES	+/- 3 MILS
25	8	⊗	YES	+/- 3 MILS
12	2	⊕	YES	+/- 3 MILS
65	4	⊕	YES	+/- 3 MILS

**FIG. 1 LAYER STRUCTURE**



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES  
TOLERANCES:  
0.XX" = ±0.01"  
0.XXX" = ±0.005"  
INTERPRET DIM AND TOL PER ASME Y14.5M-1994  
THIRD ANGLE PROJECTION

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TITLE: FABRICATION DRAWING  
12V/20A IDEAL DIODE  
WITH REVERSE INPUT PROTECTION

APPROVALS  
PCB DES. M. HAWKINS  
APP. ENG. MITCHELL LEE

SCALE = NONE

SIZE IC NO. LTC4359HDCB  
N/A DEMO CIRCUIT 1502A

FILENAME: DC1502A-4.PCB

SHT 1 OF 1