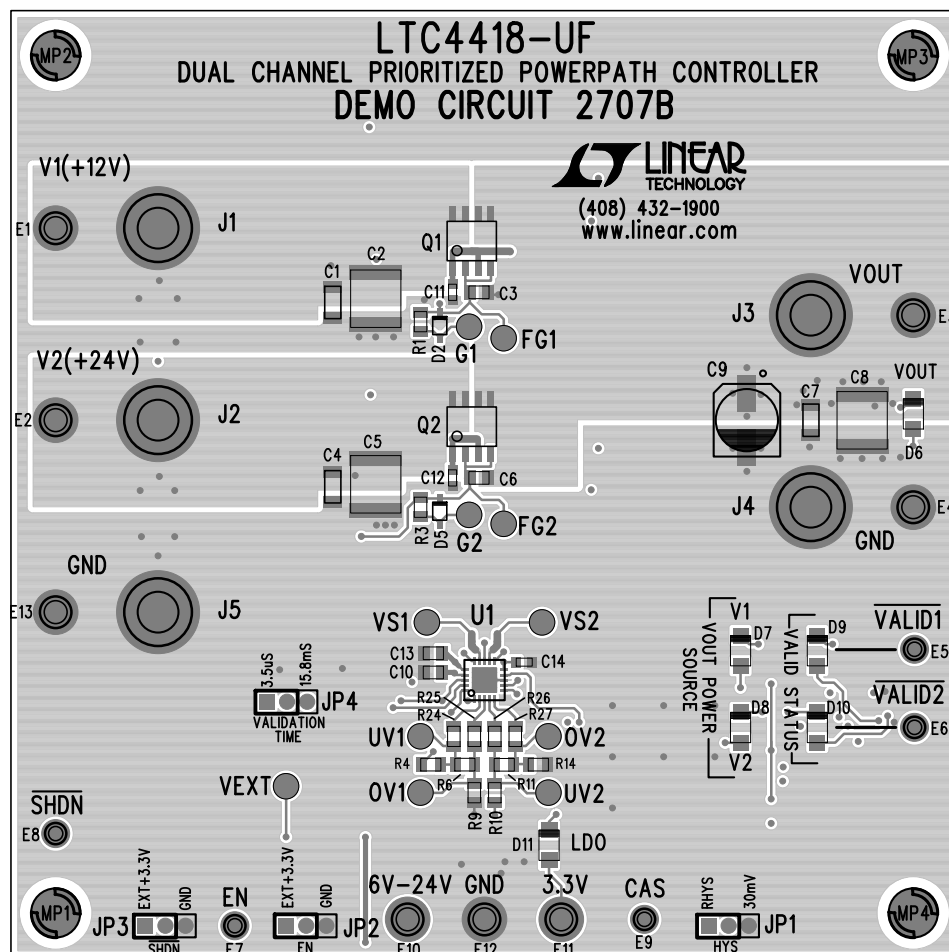


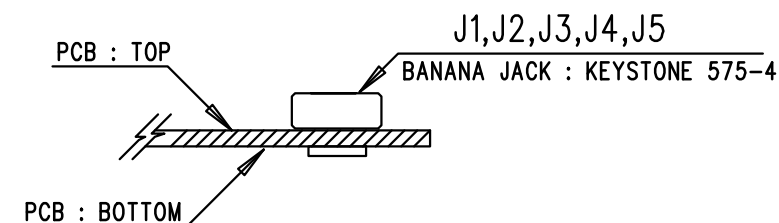
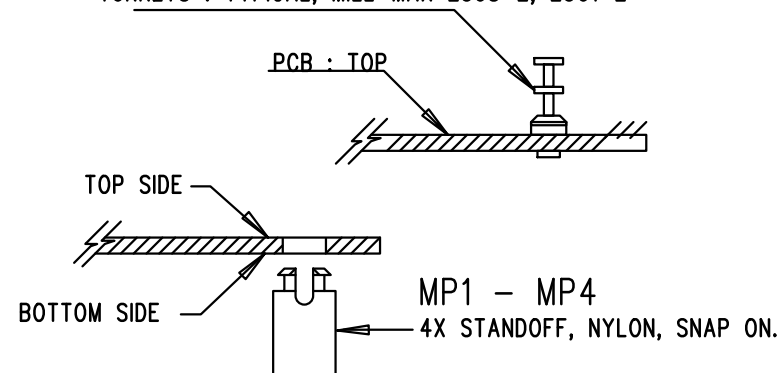
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE




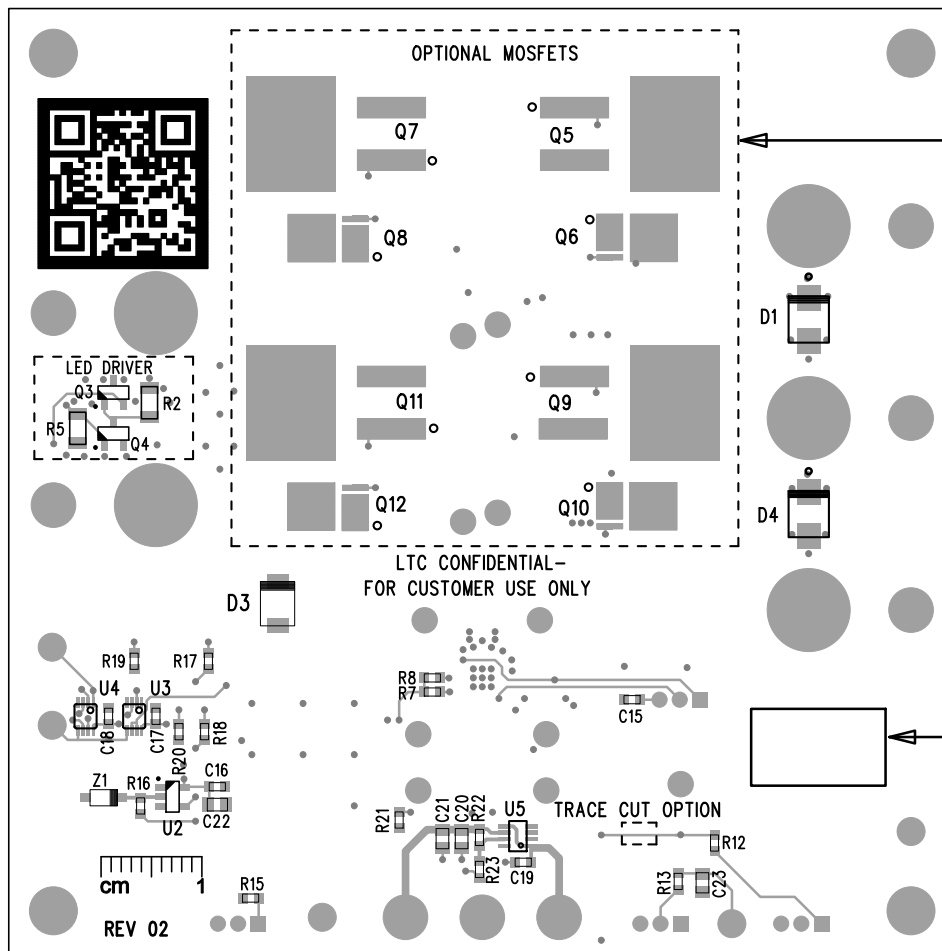
NOTES: UNLESS OTHERWISE SPECIFIED


1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE REFLOW SOLDER, BOTH SIDES, SMD. MAXIMUM SOLDER TEMPERATURE SHALL NOT EXCEED 240 DEGREES CELSIUS.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND SMOOTH THE BOARD EDGES.
4. UNSTUFFED LAND PATTERNS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL AT THESE LOCATIONS.
5. INSTALL SHUNTS FOR JP1-JP4 AS SHOWN.
6. APPLY DEMO S/N AT AREA ON BOTTOM SIDE AS SHOWN ON SHEET 2.
7. INSTALL BANANA JACKS, TURRETS AND STANDOFFS AS SHOWN BELOW:

TURRETS : TYPICAL, MILL-MAX 2308-2, 2501-2



APPROVALS		 LINEAR TECHNOLOGY		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
ENGR.	M.HAWKINS				
APP ENG.	VLADIMIR O.	TITLE: TOP ASSEMBLY DRAWING DUAL CHANNEL PRIORITIZED POWERPATH CONTROLLER			
		SIZE N/A	IC NO. LTC4418-UF DEMO CIRCUIT 2707B	DWG. REV. 02	
SCALE = NONE		SHT 1 OF 2			



APPROVALS		 LINEAR TECHNOLOGY		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
ENGR.	M. HAWKINS				
APP ENG.	VLADIMIR O.	TITLE: BOTTOM ASSEMBLY DRAWING DUAL CHANNEL PRIORITIZED POWERPATH CONTROLLER			
		SIZE	IC NO.	LTC4418-UF	DWG. REV.
		N/A		DEMO CIRCUIT 2707B	02
SCALE = NONE					SHT 2 OF 2