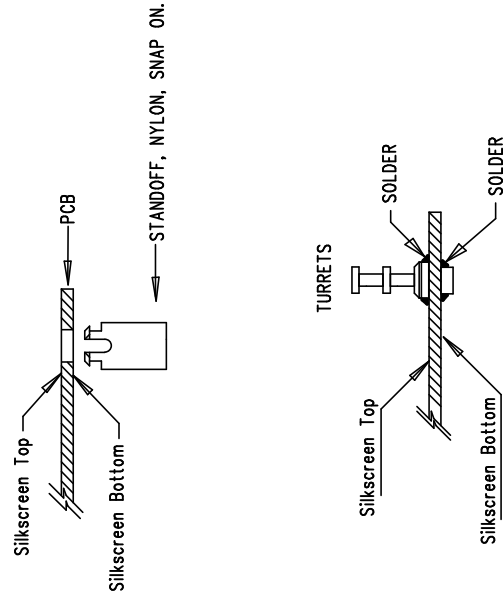
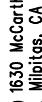


Silkscreen Top

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. JUMPER INSTALLATION: JP1: LOW; JP2: OFF; JP3: EN; JP4: OFF;



ASSY DRAWING

APPROVALS				 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
	INIT	DATE			
DRAWN					
CHECK					
DESIGN	J. Wu	5/7/07	TITLE: LTC6416CDDDB		
ENGR	C. Pei	5/7/07	16-Bit ADC Buffer + ADC		
			SIZE	DEMO	REV. B
				DC1257B	
SCALE = NONE			SHT 1 of 1		