



APPROVALS		DATE	TITLE	DWG NO	REV B			
DRAWN	June Wu	1/3/06				LTC6416CDDB 16-Bit ADC Buffer + ADC	DC1257B	SHEET 1 OF 1
CHECKED								
APPROVED								
DESIGNER	Cheng-Wai Pui	1/3/06						
SCALE:								

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.  
 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

**CONTRACT NO.**  
**FILENAME:**

Thursday, October 02, 2003

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