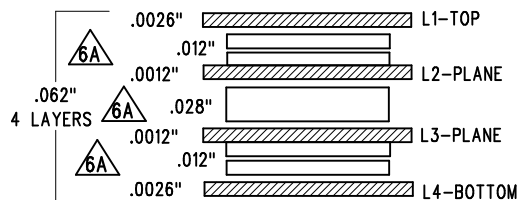


SIZE	QTY	SYM	PLATED	TOL
0.008	500	+	YES	+/-0.003
0.187	4	X	NO	+/-0.003
0.07	2	□	NO	+/-0.003
0.065	5	◇	YES	+/-0.003
0.066	4	⊗	YES	+/-0.003
0.128	4	⊗	NO	+/-0.003
0.04	16	⊕ ^A	YES	+/-0.003
0.096	4	⊕ ^B	YES	+/-0.003

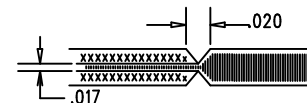
LAYERING STRUCTURE



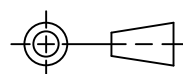
- NOTES :
- FAB PER IPC-A-600.
 - MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
 - SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
 - DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
 - FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
 - CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYER 1-2
TRACE WIDTH 24 MILS.

6A SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	AK.
APP ENG.	PHILIP K.

SCALE = NONE



AHEAD OF WHAT'S POSSIBLE™

TITLE: FABRICATION DRAWING

HIGH SPEED COMPARATOR WITH LVDS OUTPUT

SIZE N/A IC NO. LTC6754-UD
DC2767A

REV 1

FILENAME: DC2767A-1.PCB

SHT 1 OF 1

1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
CONFIDENTIAL-
FOR CUSTOMER USE ONLY