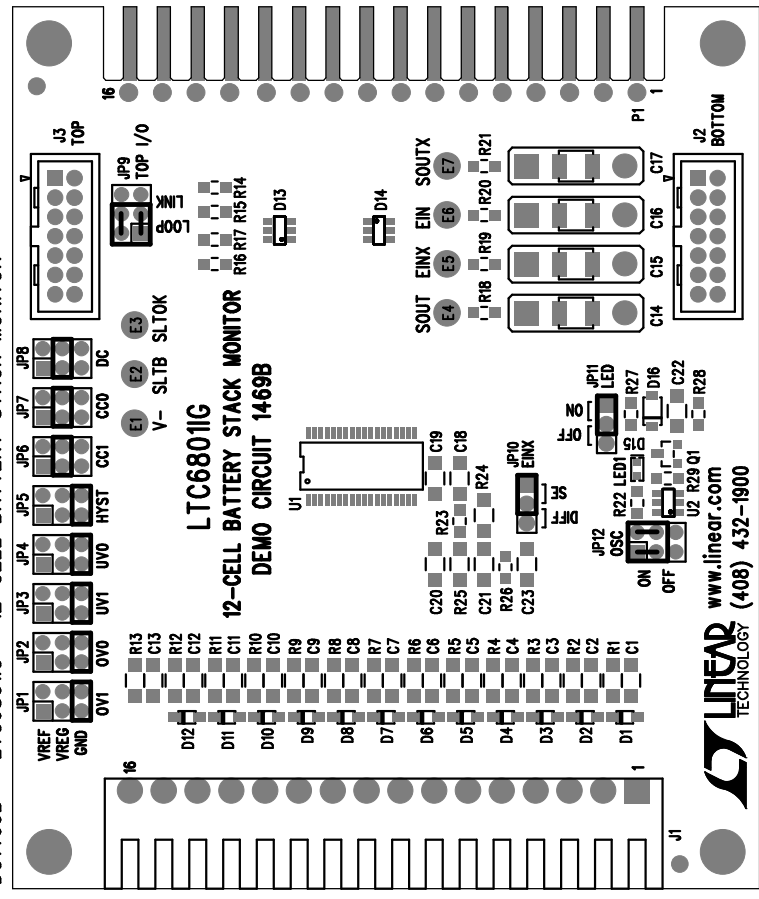
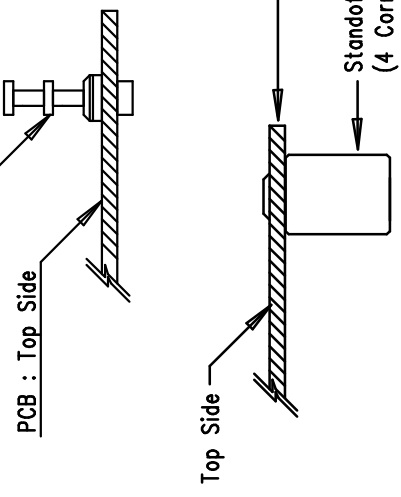


LINEAR TECHNOLOGY DATE: 1-7-10			
DC1469B - LTC6801IG 12-CELL BATTERY STACK MONITOR			
REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
	2	PRODUCTION	JON M.
			DATE
			1-7-10

NOTES: UNLESS OTHERWISE SPECIFIED


1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.

TURRETS : Mill-Max 2501-2



Silkscreen Top

Install Shunts: JP1 = GND  
 JP2 = GND  
 JP3 = GND  
 JP4 = GND  
 JP5 = GND  
 JP6 = VREG  
 JP7 = VREG  
 JP8 = VREG  
 JP9 = LOOP  
 JP10 = SE  
 JP11 = ON  
 JP12 = ON

APPROVALS					1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
TITLE: TOP ASSEMBLY DRAWING			12-CELL BATTERY STACK MONITOR			
PCB DES.	INIT.	DATE	SIZE	IC NO.	REV.	
ENG.	K.L.	1-7-10	N/A	LTC6801IG	2	
	JON M.	1-7-10		DEMO CIRCUIT 1469B		
SCALE: N/A			FILENAME: DC1469B-2.PCB		SHT 1 of 1	