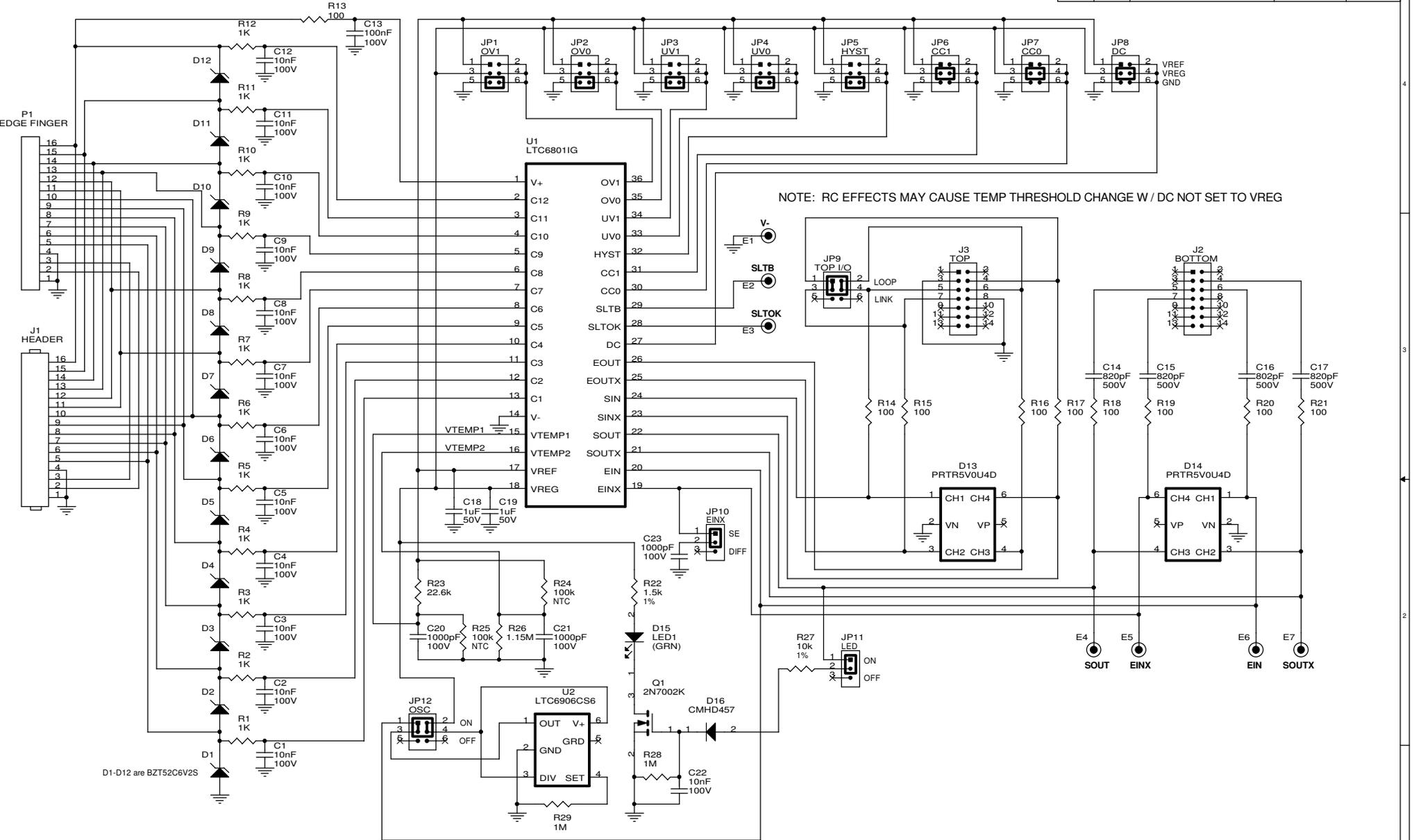


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	2	PRODUCTION RUN	JON M.	1-6-10



NOTE: RC EFFECTS MAY CAUSE TEMP THRESHOLD CHANGE W / DC NOT SET TO VREG

NOTE: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTORS ARE IN OHMS 0805
 ALL CAPACITORS ARE IN MICROFARADS 0805

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.	
APPROVALS	
PCB DES.	K.L.
ENG.	JON M.

LINEAR TECHNOLOGY
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 Milpitas, CA 95035
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 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC		
12-CELL BATTERY STACK MONITOR		
SIZE	IC NO.	REV
N/A	LTC6801G DEMO CIRCUIT 1469B	2
DATE:	Wednesday, January 13, 2010	SHEET 1 OF 1