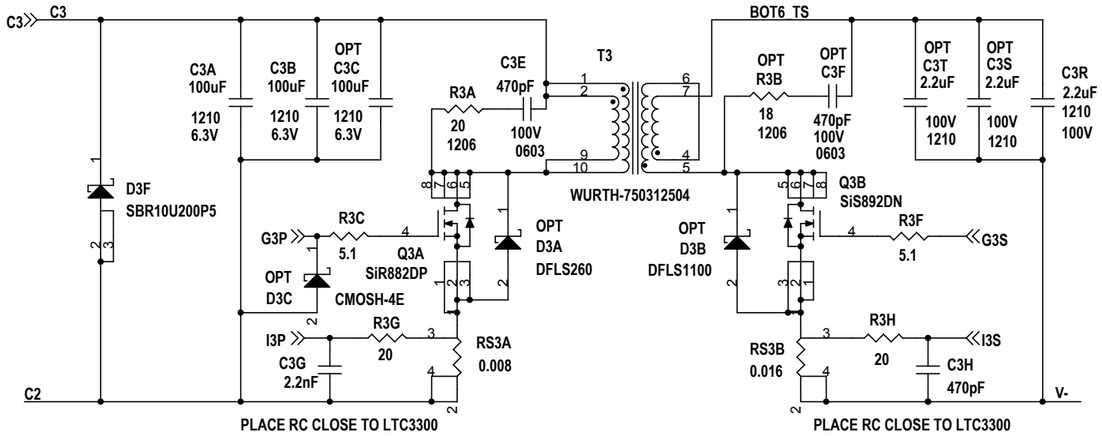
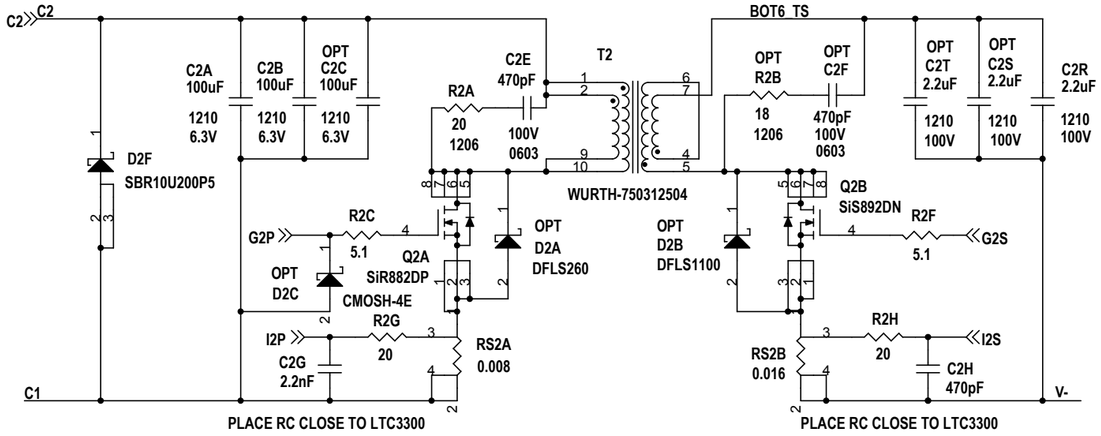
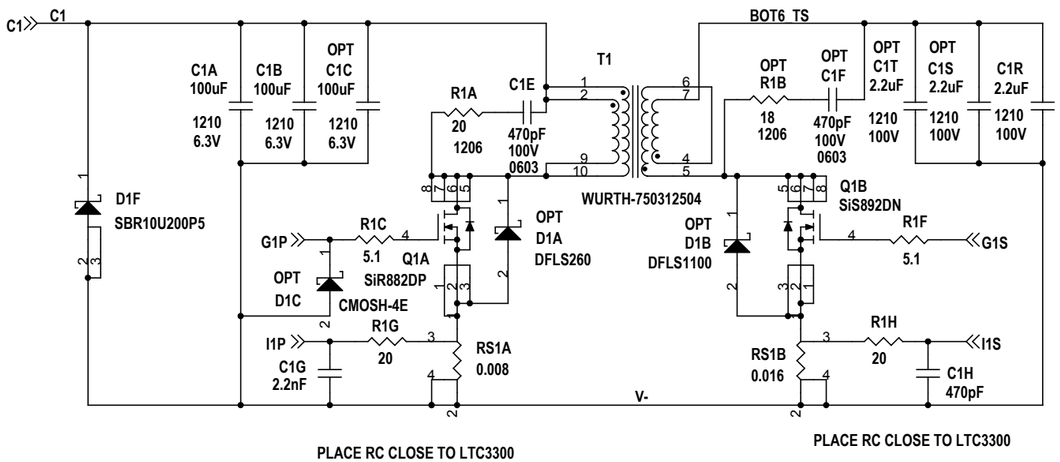
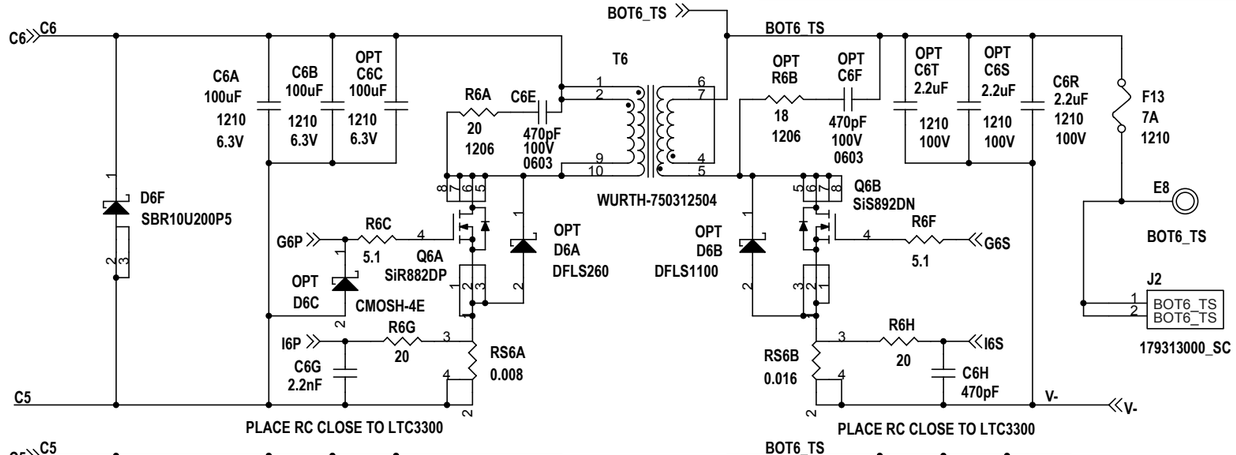
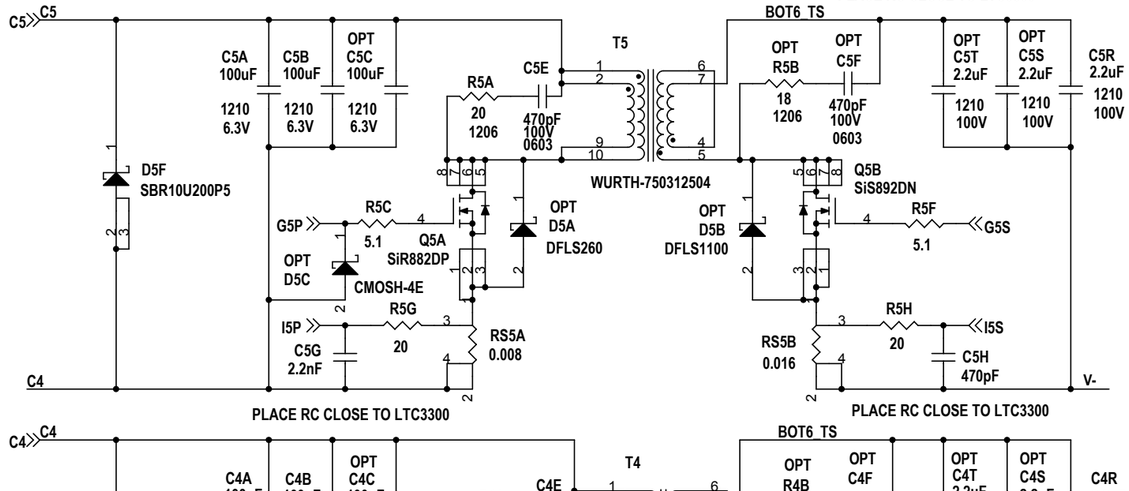
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION FAB	J.DREW	1 - 11 - 13

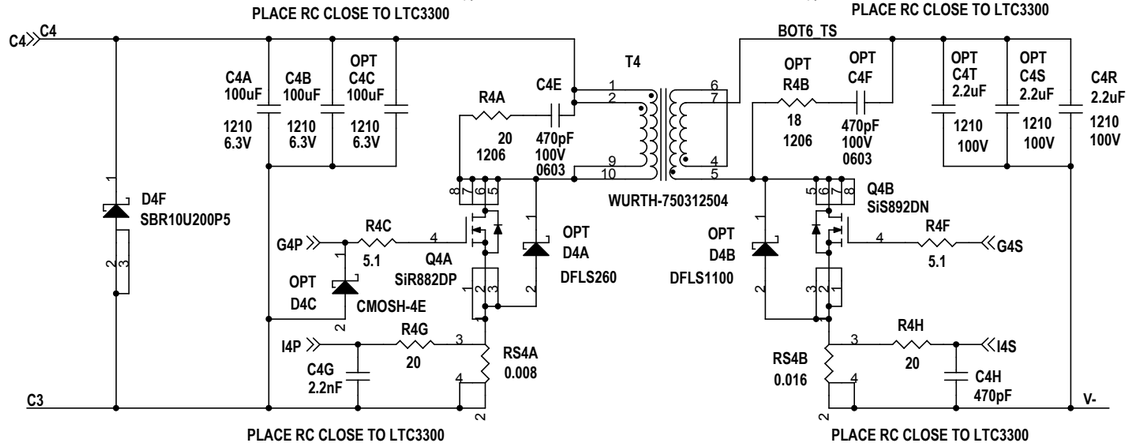












BATTERY CELL VOLTAGES

$C_N - C_{N-1}$

2.5V - 4.5V

2.5A

CUSTOMER NOTICE

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APPROVALS	
PCB DES.	NC
APP ENG.	J. DREW
SCALE = NONE	

LINEAR TECHNOLOGY

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Milpitas, CA 95035
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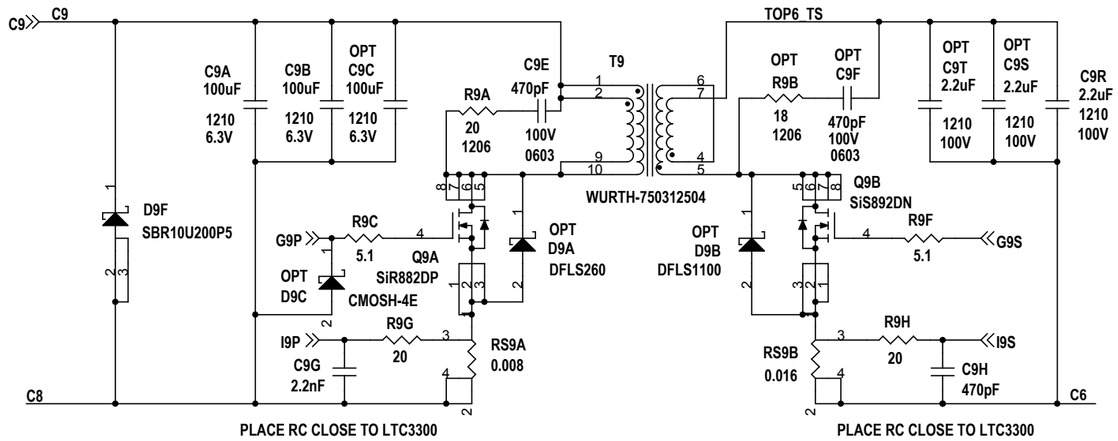
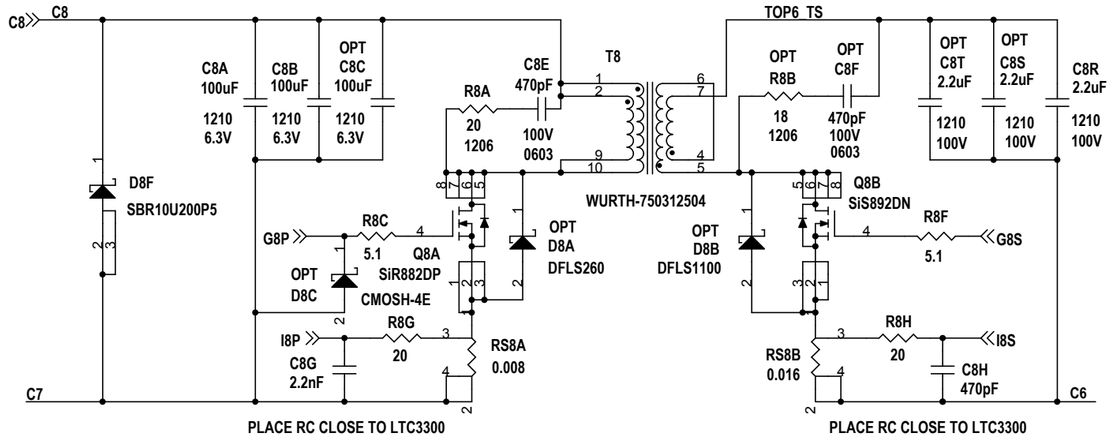
TITLE: SCHEMATIC

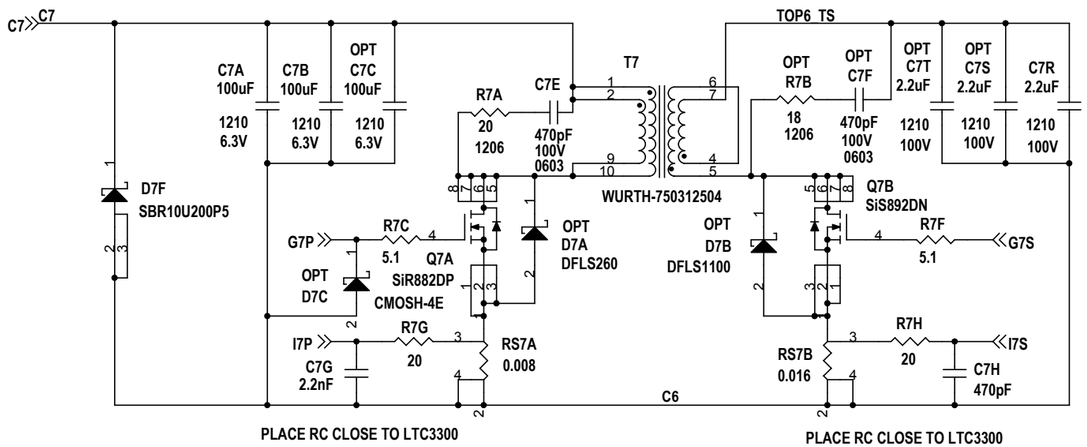
HIGH EFFICIENCY BIDIRECTIONAL MULTICELL BATTERY BALANCER

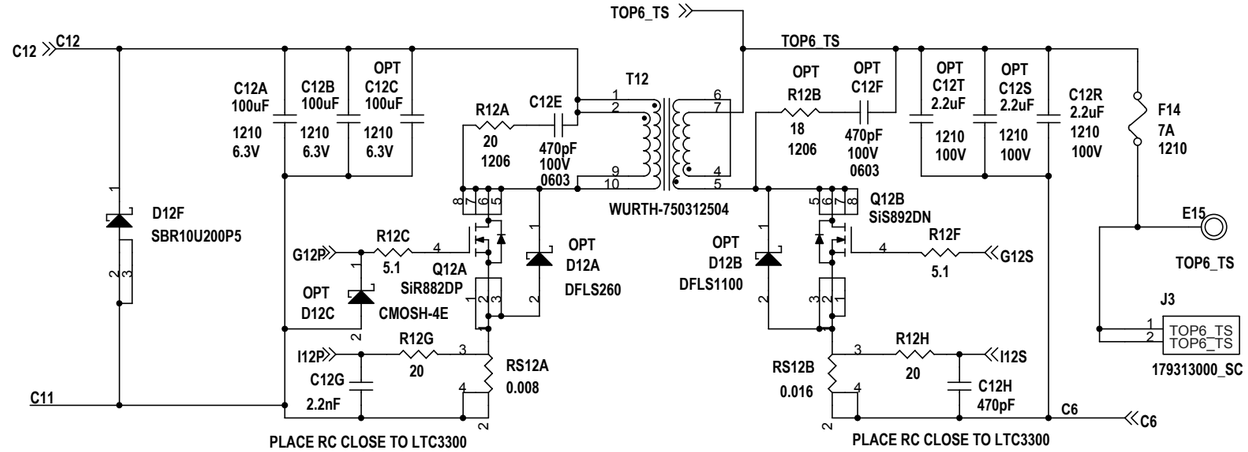
SIZE	IC NO.	REV.
N/A	LTC3300ILXE-1 / LTC6803IG-2 DEMO CIRCUIT 2064A	1

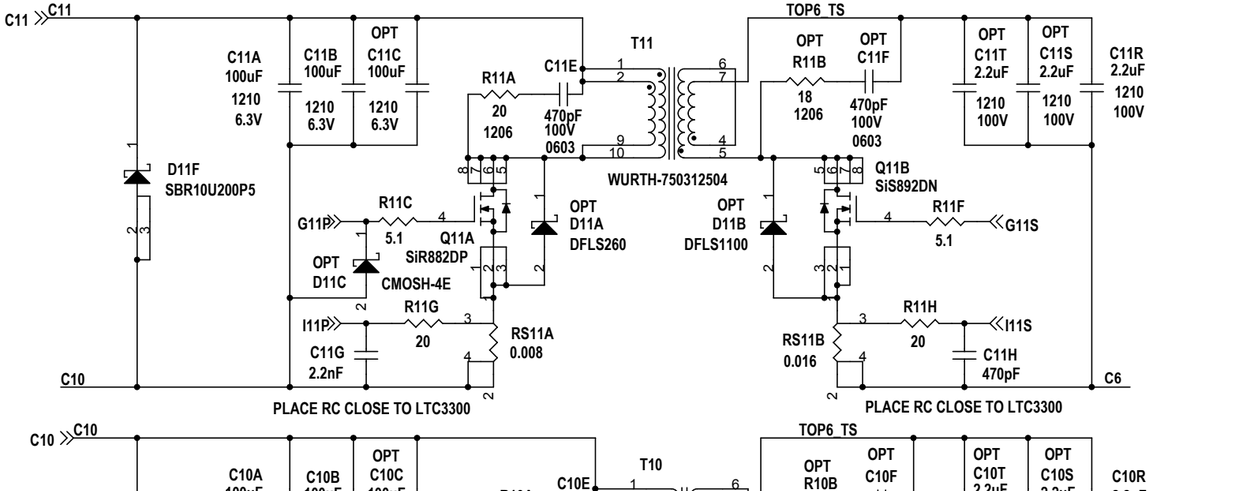
DATE: 1 - 11 - 13 SHEET 1 OF 5

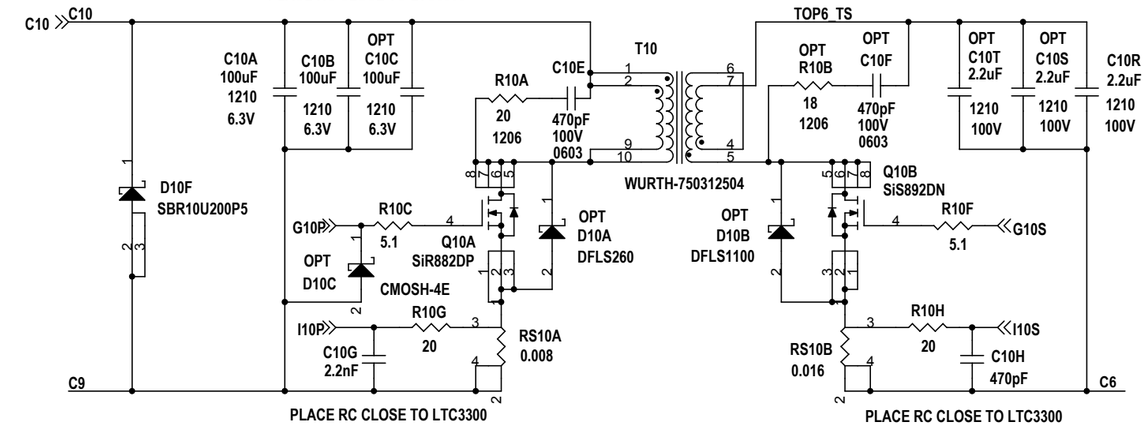












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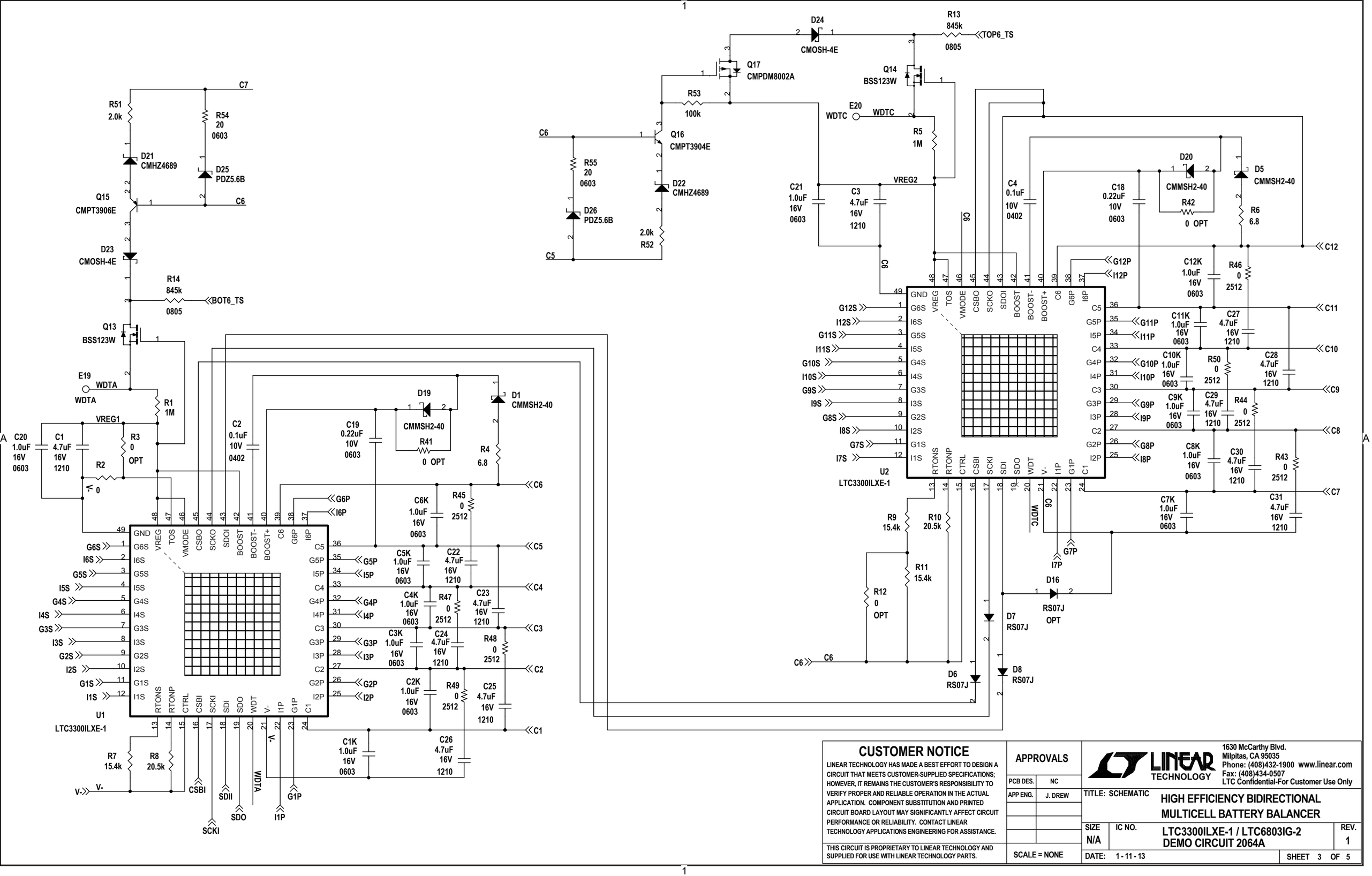
N/A	LTC3300ILXE-1 / LTC6803IG-2 DEMO CIRCUIT 2064A	1

DATE: 1 - 11 - 13 SHEET 2 OF 5



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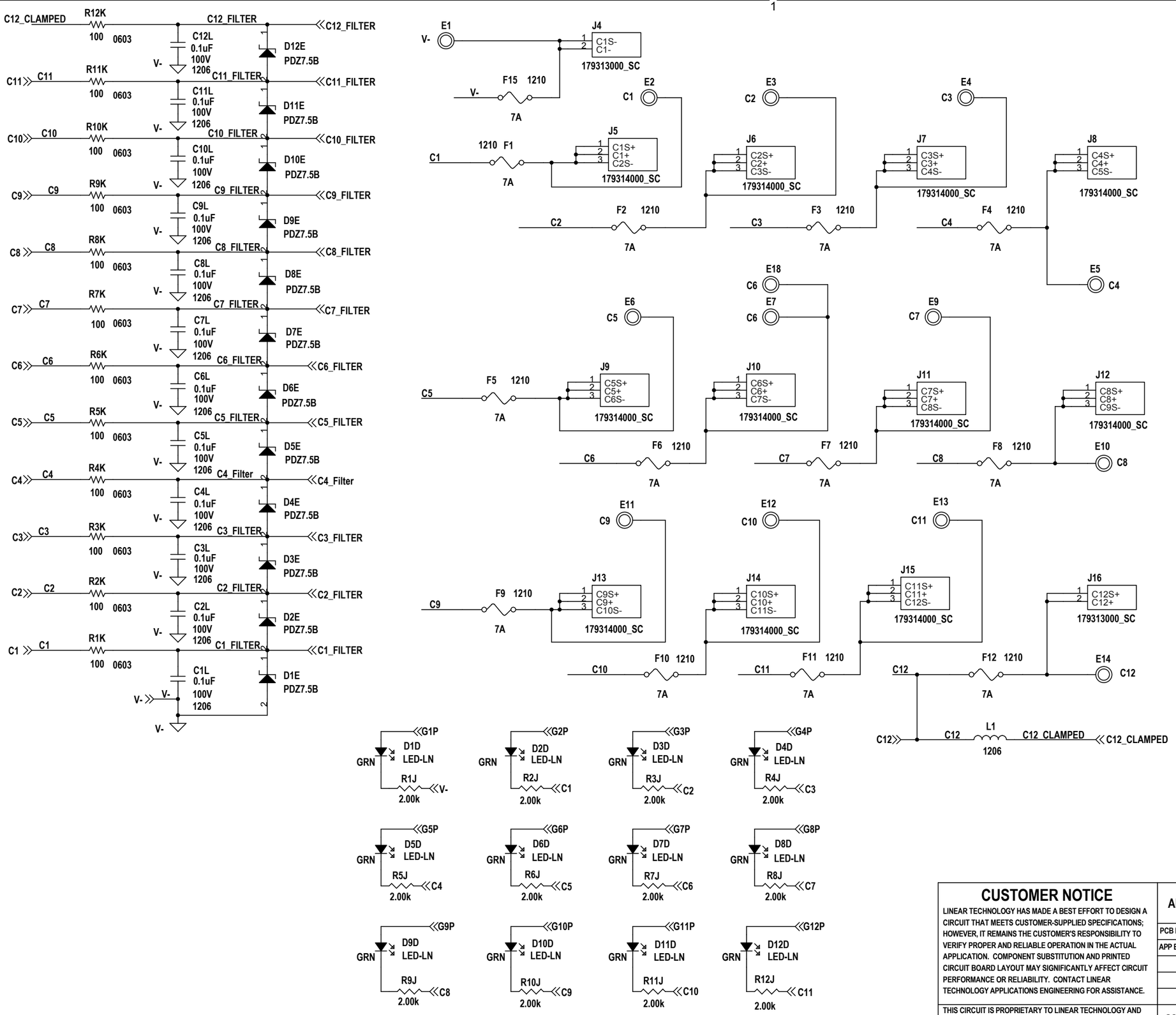
DATE: 1 - 11 - 13

SHEET 3 OF 5



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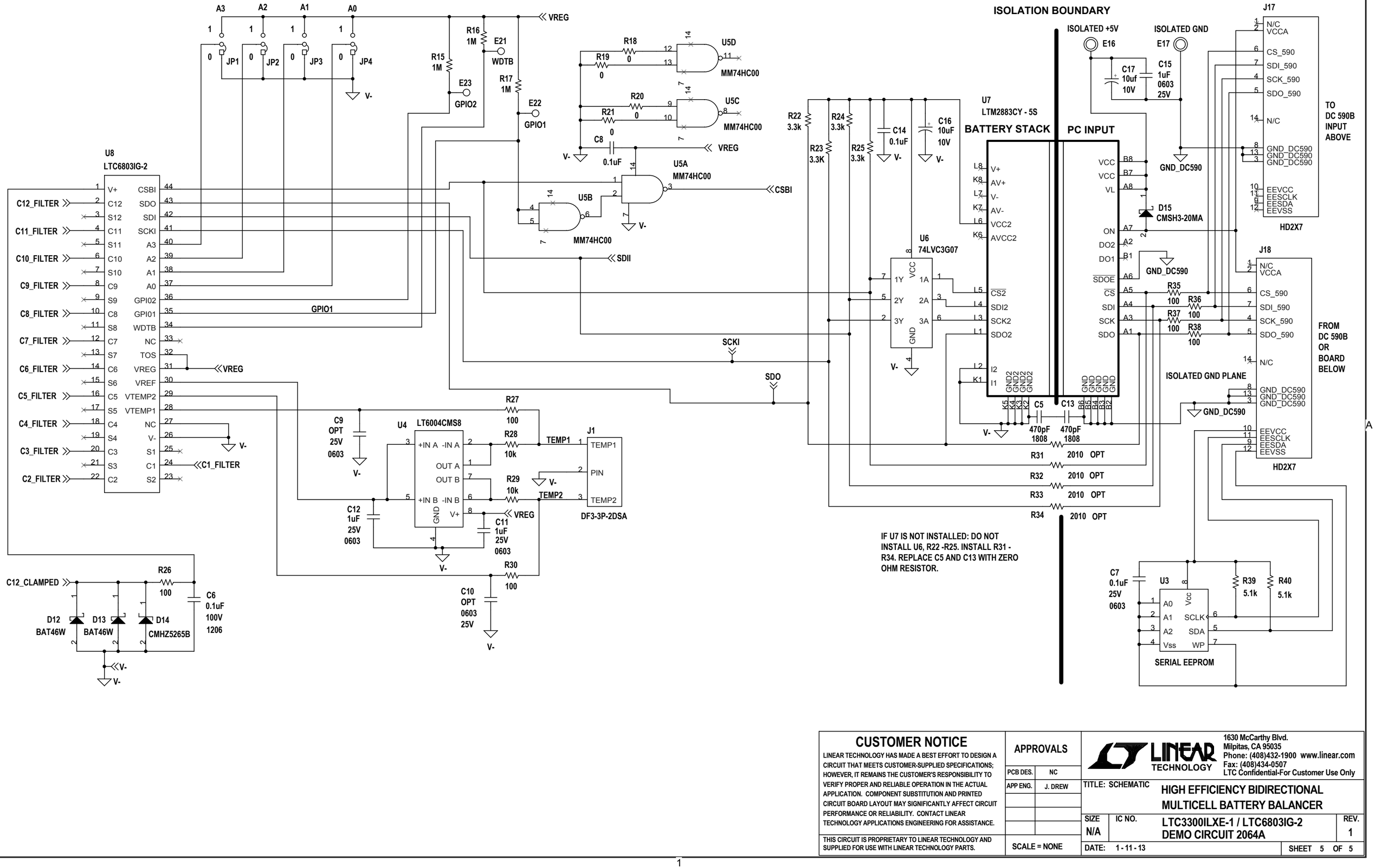

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