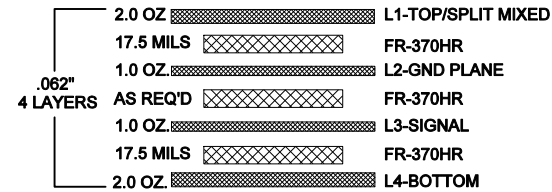


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	4	PRODUCTION	PW	10-10-17

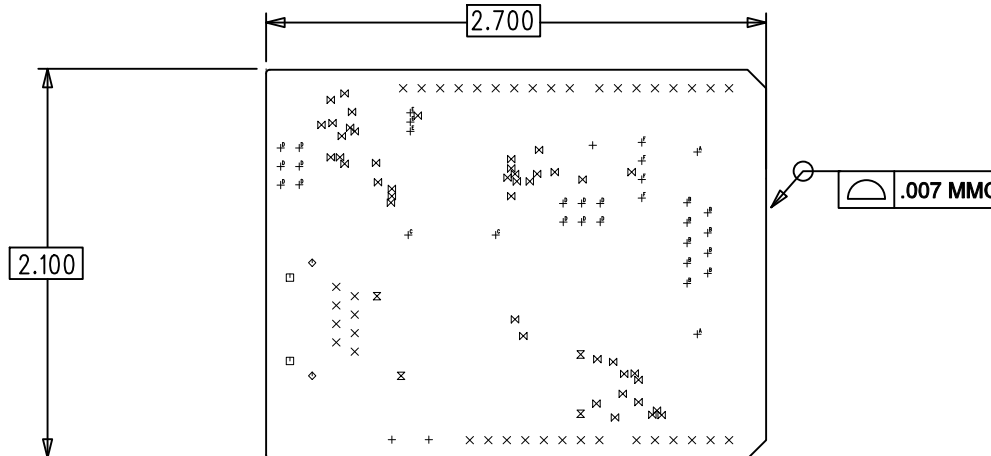
FIG. 1
LAYER STRUCTURE



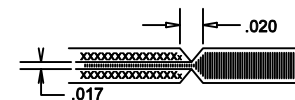
NOTES: UNLESS OTHERWISE SPECIFIED



- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-O MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- DO NOT ALTER SOLDER MASK MAINTAIN .0018" OVERSIZE
ON SMT PADS. A .005" WEBBING IS REQUIRED BETWEEN SMD PADS.
- SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"
- ANY ELECTRICAL TEST/QA STAMPS SHALL BE ON BOTTOM SIDE, "UNSHOWY AREA".

SHOWN FROM TOP SIDE



SIZE	QTY	SYM	PLATED	TOL
10	43	⊗	YES	+/-0.003
15.75	3	⊕	YES	+/-0.003
25	4	⊗	YES	+/-0.003
35	12	⊕	YES	+/-0.003
37	2	⊕	YES	+/-0.003
40	40	⊗	YES	+/-0.003
41.34	9	⊕	YES	+/-0.003
42	4	⊕	YES	+/-0.003
65	3	⊕	YES	+/-0.003
66	2	⊕	YES	+/-0.003
127.95	2	⊕	YES	+/-0.003
128	2	⊕	NO	+/-0.003



<div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE 1°: 0.300° = 0.01° ± 0.300° = 0.005° ± INTERPRET DIM AND TOL PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div> <div>DO NOT SCALE DRAWING</div>	<div>APPROVALS</div> <table><tr><td>PCB DES.</td><td>NC</td></tr><tr><td>APP ENG.</td><td>PW</td></tr><tr><td> </td><td> </td></tr><tr><td> </td><td> </td></tr><tr><td> </td><td> </td></tr><tr><td> </td><td> </td></tr></table>	PCB DES.	NC	APP ENG.	PW									<div> LINEAR TECHNOLOGY</div> <div>1930 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.Linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>
	PCB DES.	NC												
	APP ENG.	PW												
TITLE: FABRICATION DRAWING: isoSPI AND ISOLATED CAN ARDUINO BOARD														
SIZE N/A	IC NO. LTC6820, LTM2889 DEMO CIRCUIT 2617A	REV. 04												
SCALE: NONE		SHT 1 of 1												