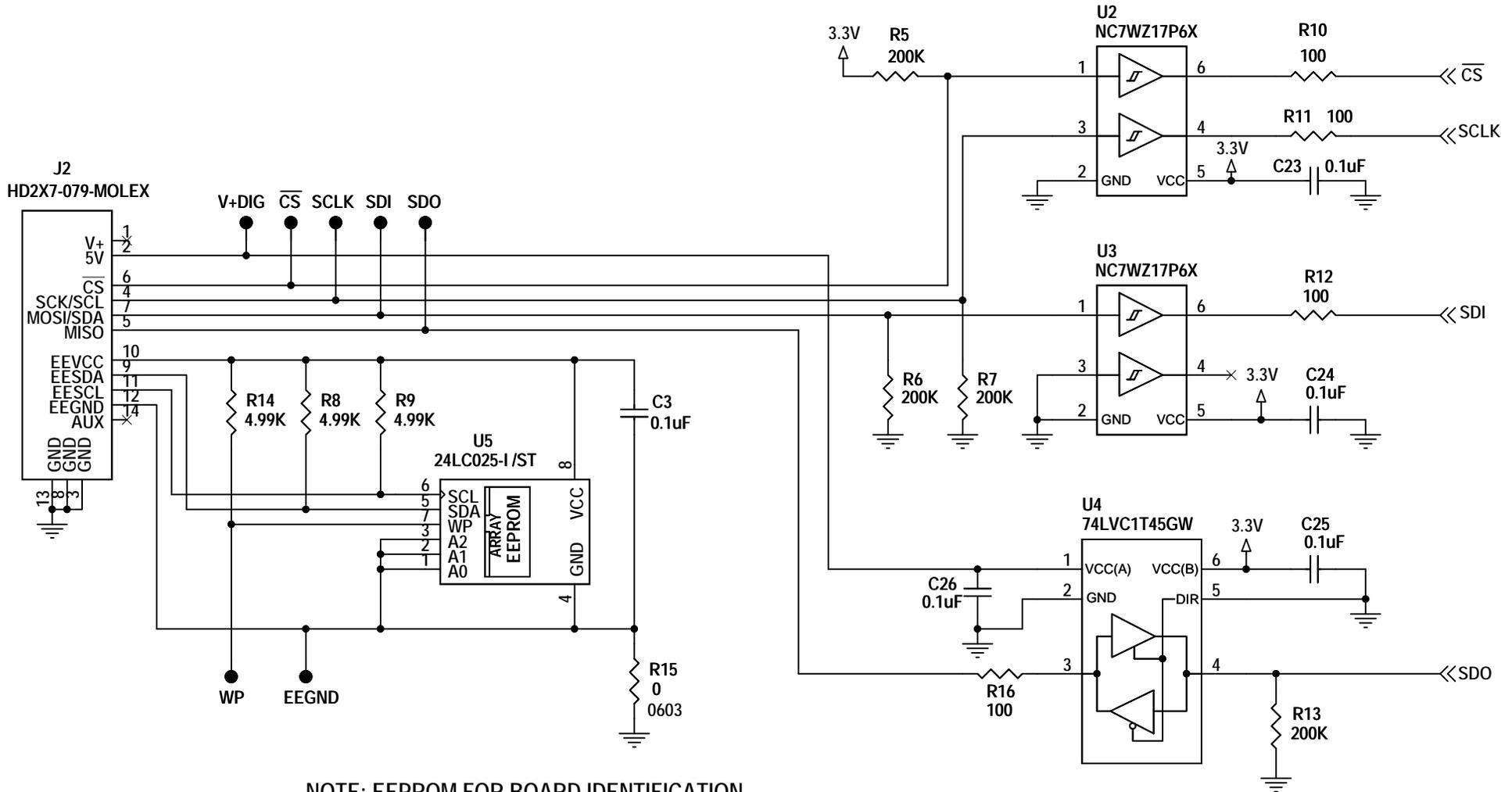




# DC590 SPI INTERFACE



NOTE: EEPROM FOR BOARD IDENTIFICATION

## CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

## APPROVALS

PCB DES. KIM T.

APP ENG. CHRIS P.

SCALE = NONE



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TITLE: SCHEMATIC

ULTRALOW NOISE & SPURIOUS INTEGER-N  
SYNTHESIZER WITH INTEGRATED VCO

SIZE IC NO.

N/A

LTC6946IUFD FAMILY  
DEMO CIRCUIT 1705C

REV.

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DATE: Thursday, October 02, 2014

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