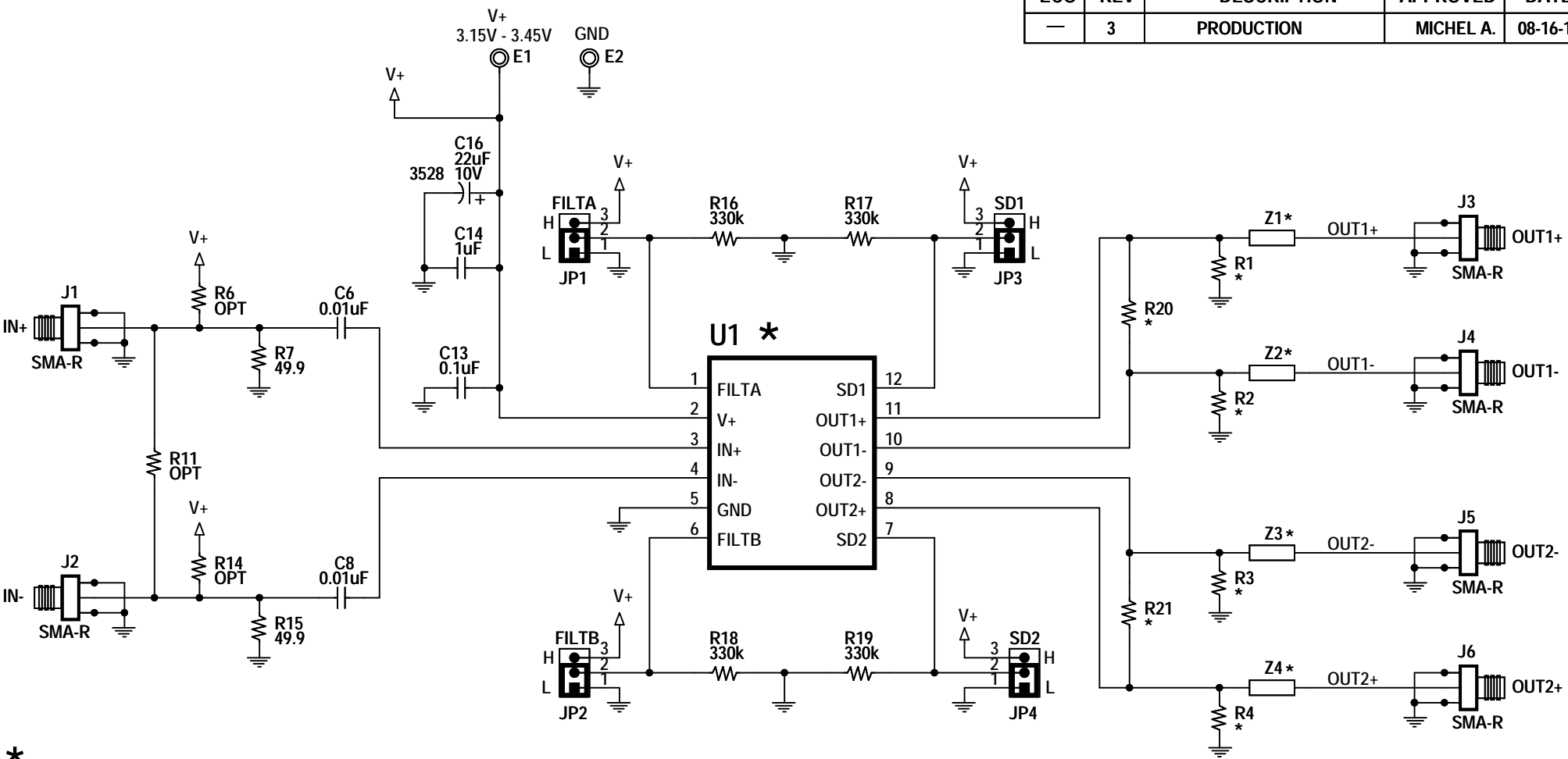


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	3	PRODUCTION	MICHEL A.	08-16-12



\*

ASSY		U1	R1-R4	Z1-Z4	R20-R21
-A	LVPECL	LTC6957HMS-1	130 OHM	0.01uF	OPT
-B	LVDS	LTC6957HMS-2	OPT	0 OHM	100 OHM

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ARE 0603.

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.  
 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	MICHEL A.
SCALE = NONE	



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TITLE: SCHEMATIC		
<b>LOW PHASE NOISE, DUAL LVPECL OR LVDS OUTPUT BUFFER / DRIVER / LOGIC CONVERTER</b>		
SIZE	IC NO.	REV.
N/A	LTC6957HMS-1 / -2	3
DATE: 08/16/2012, 06:45 PM		SHEET 1 OF 1